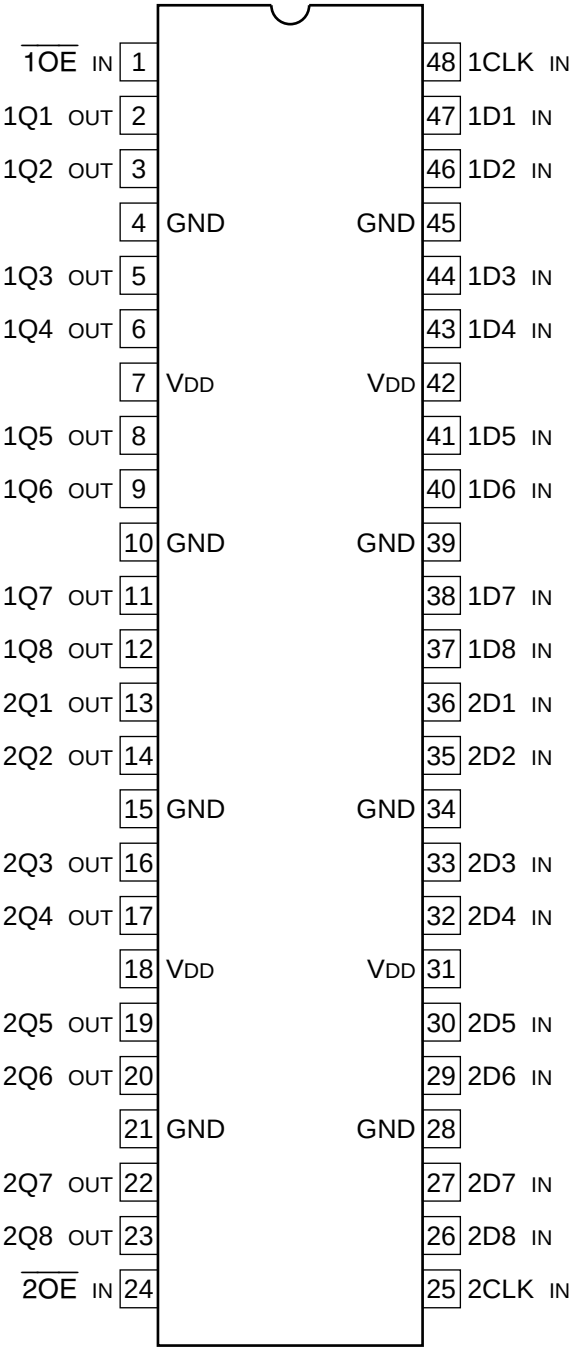


C-MOS 3-STATE 16-BIT D-TYPE FLIP-FLOP

—TOP VIEW—



INPUT

1CLK, 2CLK : CLOCK

1D1 - 1D8 } : DATA

2D1 - 2D8 } : DATA

1OE, 2OE : OUTPUT ENABLE

OUTPUT

1Q1 - 1Q8 } : DATA

2Q1 - 2Q8 } : DATA

INPUTS			OUTPUT
$\overline{OE}$	CLK	D	Q
0		1	1
0		0	0
0	0	x	Q ₀
1	x	x	HI-Z

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

SN74ABT16374DL (2/2)

