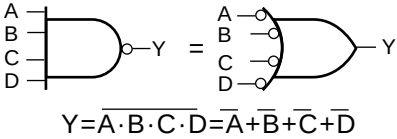
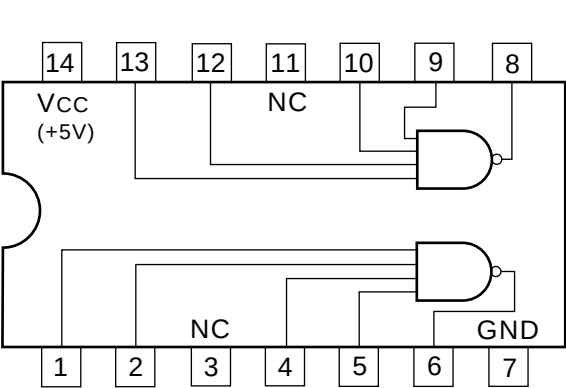

SN7420N
IL00

TTL 4-INPUT POSITIVE NAND GATE
-TOP VIEW-



$$Y = A \cdot B \cdot C \cdot D = \overline{A + B + C + D}$$

A	B	C	D	Y
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	1
⋮	⋮	⋮	⋮	⋮
1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

0 ; LOW LEVEL
1 ; HIGH LEVEL