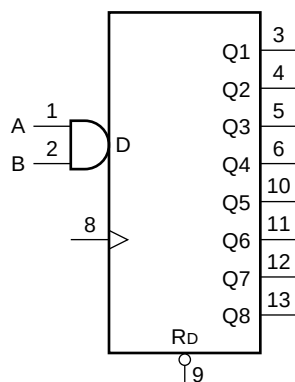
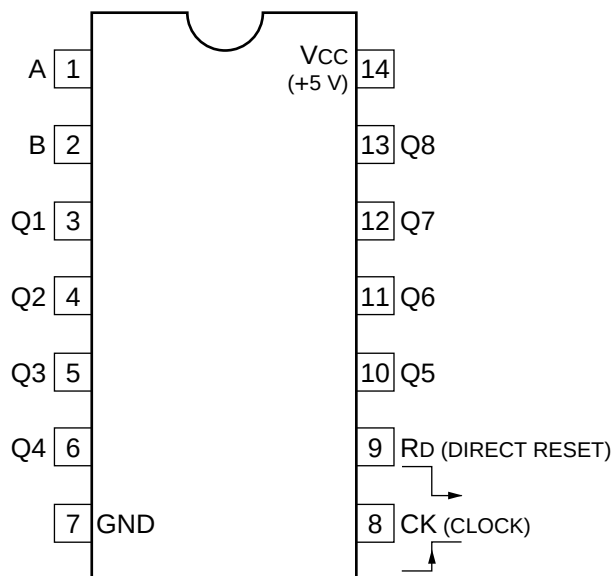


TTL 8-BIT PARALLEL OUT SERIAL SHIFT REGISTER

—TOP VIEW—



INPUTS				OUTPUTS			
Rd	CK	A	B	Q1	Q2	---	Q8
0	x	x	x	0	0	0	0
1	0	x	x	Q1 ₀	Q2 ₀	---	Q8 ₀
1	$\uparrow$	1	1	1	Q1 _n	---	Q7 _n
1	$\uparrow$	0	x	0	Q1 _n	---	Q7 _n
1	$\uparrow$	x	0	0	Q1 _n	---	Q7 _n

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

