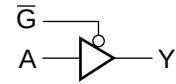
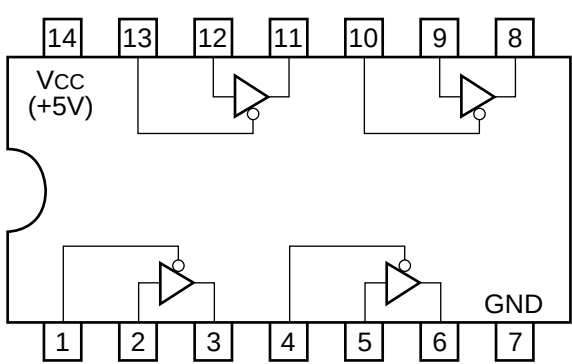

TTL BUS BUFFER GATES WITH 3-STATE OUTPUTS

-TOP VIEW-



$\overline{G}$	A	Y
0	0	0
0	1	1
1	X	HI-Z

0 ; LOW LEVEL X ; DON'T CARE
1 ; HIGH LEVEL HI-Z ; HIGH IMPEDANCE