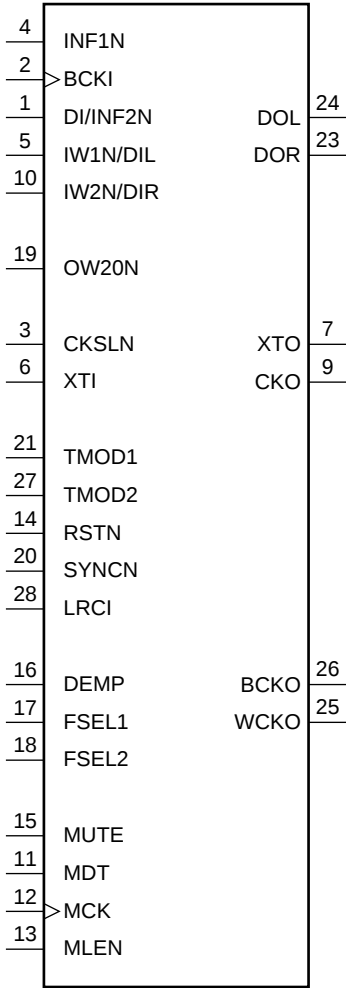
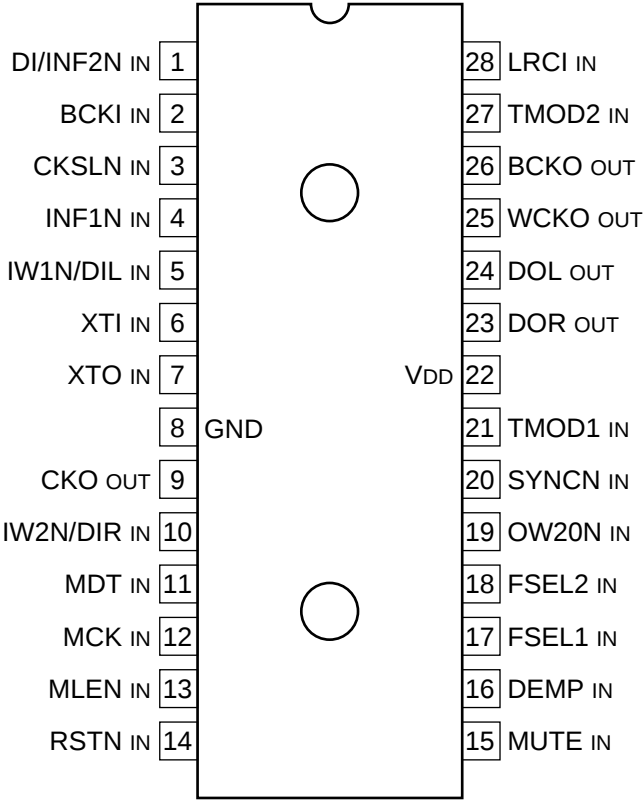


C-MOS DIGITAL FILTER

—TOP VIEW—



INPUT

BCKI : INPUT BIT CLOCK
 CKSLN : INPUT FREQUENCY SELECT (H : 384fs/L : 256fs)
 DEMP : DE-EMPHASIS CONTROL (L : OFF/H : ON)
 DI/INF2N : INPUT DATA (INF1N = L) /INPUT FORMAT SELECT 2 (INF1N = H)
 FSEL1, 2 : DE-EMPHASIS SELECT

fs (Hz)		32 k	44.1 k	48 k	TEST MODE
SETTING	FSEL1	H	L	L	H
	FSEL2	H	L	H	L

INF1N : INPUT FORMAT SELECT 1
 IW1N/DIL : INPUT WORD LENGTH 1 (INF1N = L) /Lch DATA INPUT (TNF1N = H)
 IW2N/DIR : INPUT WORD LENGTH 2 (INF1N = L) /Rch DATA INPUT (TNF1N = H)
 LRCI : SAMPLE RATE CLOCK (fs)
 MCK : ATTENUATION BIT CLOCK
 MDT : ATTENUATION SERIAL DATA
 MLEN : ATTENUATION LATCH CLOCK
 MUTE : MUTE CONTROL
 OW20N : OUTPUT WORD LENGTH

OUTPUT WORD LENGTH (BIT)	18	20
OW20N	H	L

RSTN : SYSTEM RESET (L : RESET/H : NORMAL)
 SYNCN : SYNC MODE SELECT (L : EXECUTION SYNC MODE/H : JITTER FREE MODE)
 TMOD1 : DITHER ON/OFF SELECT (L : ON/H : OFF)
 TMOD2 : FILTER CHARACTER SELECT
 XTI : OSCILLATOR INPUT

OUTPUT

BCKO : OUTPUT BIT CLOCK
 CKO : OSCILLATOR OUTPUT CLOCK
 DOL : Lch DATA
 DOR : Rch DATA
 WCKO : OUTPUT WORD CLOCK
 XTO : OSCILLATOR OUTPUT

