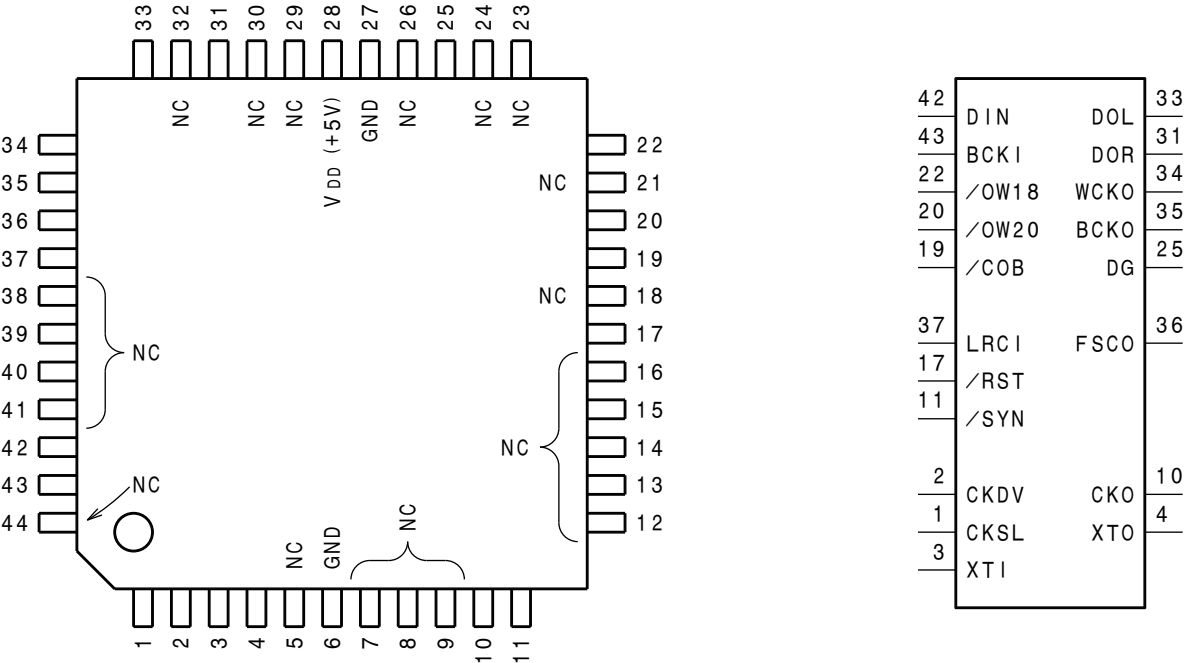

C-MOS 8Fs OVER SAMPLING FILTER
-TOP VIEW-



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	/CKSL	12	-	NC	23	-	NC	34	O	WCKO
2	I	/CKDV	13	-	NC	24	-	NC	35	O	BCKO
3	I	XTI	14	-	NC	25	O	DG	36	O	FSCO
4	O	XTO	15	-	NC	26	-	NC	37	I	LRCI
5	-	NC	16	-	NC	27	-	GND	38	-	NC
6	-	GND	17	I	/RST	28	-	V _{DD} (+5V)	39	-	NC
7	-	NC	18	-	NC	29	-	NC	40	-	NC
8	-	NC	19	I	/COB	30	-	NC	41	-	NC
9	-	NC	20	I	/OW20	31	O	DOR	42	I	DIN
10	O	CKO	21	-	NC	32	-	NC	43	I	BCKI
11	I	/SYN	22	I	/OW18	33	O	DOL	44	-	NC

INPUT

BCKI ; INPUT DATA BIT CLOCK
CKDV, CKSL ; SYSTEM CLOCK SELECT
COB ; 2'S COMPLEMENT/COB SELECT (H:2'S COMPLEMENT, L:COB)
DIN ; INPUT DATA
LRCI ; SAMPLING RATE (Fs) CLOCK FOR INPUT DATA (H:L-CH, L:R-CH)
OW18, 20 ; OUTPUT BIT NUMBER SELECT

OUTPUT BIT	16	18	20
OW18	H	L	H
OW20	H	H	L

RST ; SYSTEM RESET (H:NORMAL, L:SYSTEM RESET)
SYN ; JITTER FREE/FORCED SYNCHRONIZATION SELECT
(H:JITTER FREE, L:FORCED SYNCHRONIZATION)
XTI ; OSCILLATOR

XTI	192fs	256fs	384fs	512fs
CKSL	H	L	H	L
CKDV	H	H	L	L

OUTPUT

BCKO ; OUTPUT DATA BIT CLOCK
CKO ; SYSTEM CLOCK
DG ; DEGLITCH CONTROL CLOCK
DOL, DOR ; LCH, RCH DATA
FSCO ; TIMING CLOCK
WCKO ; OUTPUT DATA WORD CLOCK
XTO ; OSCILLATOR

