### SN100KT5538 OCTAL ECL-TO-TTL TRANSLATOR WITH OPEN-COLLECTOR OUTPUTS SDZS011 – APRIL 1990

- 100K Compatible
- Open-Collector Outputs Drive Bus Lines or Buffer Memory Address Registers
- ECL and TTL Control Inputs
- Flow-Through Architecture Optimizes PCB Layout
- Center-Pin V<sub>CC</sub>, V<sub>EE</sub>, and GND Configurations Minimize High-Speed Switching Noise
- Package Options Include "Small Outline" Packages and Standard Plastic 300-mil DIPs

#### description

This octal ECL-to-TTL translator is designed to provide efficient translation between a 100K signal environment and a TTL signal environment. This device is designed specifically to improve the performance and density of ECL-to-TTL CPU/bus oriented functions such as memory address drivers, clock drivers, and bus-oriented receivers and transmitters while eliminating the need for three-state overlap protection.

Two output enables,  $\overline{OE1}$  and  $\overline{OE2}$ , are provided. These enable inputs are ANDed together with  $\overline{OE1}$  being ECL-compatible and  $\overline{OE2}$  being TTL-compatible. This offers the choice of controlling the outputs of the device from either a TTL or ECL signal environment.

The SN100KT5538 is characterized for operation from 0°C to 85°C.

### R NT PACKAGE



### logic symbol<sup>†</sup>



 $^\dagger$  This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

FUNCTION TABLE							
OUTPUT ENABLE		DATA INPUT	OUTPUT (TTL)				
OE1	OE2	A	Y				
н	Х	Х	Н				
Х	Н	Х	Н				
L	L	L	Н				
L	L	н	L				

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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### logic diagram (positive logic)



#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage range, V <sub>CC</sub>	
Input voltage range (TTL) (see Note 1)	
Input voltage range (ECL)	V <sub>EE</sub> to 0 V
Input current range (TTL)	–30 mA to 5 mA
Current into any output in the low state	96 mA
Voltage applied to any output in the high state	$\dots \dots \dots -0.5$ V to V <sub>CC</sub>
Operating temperature range	0°C to 85°Č
Storage temperature range	−65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The TTL input voltage ratings may be exceeded provided the input current ratings are observed.



#### recommended operating conditions

			MIN	NOM	MAX	UNIT
VCC	TTL supply voltage		4.5	5	5.5	V
VEE	ECL supply voltage		-4.2	-4.5	-4.8	V
		TTL	2		5 5.5	V
VIH	High-level input voltage	ECL (see Note 2)	-1150		-840	mV
	VIL Low-level input voltage	TTL			0.8	V
VIL		ECL (see Note 2)	-1810		-1490	mV
Vон	TTL high-level output voltage	-			5.5	V
IOL	TTL low-level output current				48	mA
IК	TTL input clamp current				-18	mA
Τ <sub>Α</sub>	Operating free-air temperature		0		85	°C

NOTE 2: The algebraic convention, in which the least positive (most negative) value is designated minimum, is used in this data sheet for logic levels only.

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER		TEST CONDITIONS		MIN TYP <sup>†</sup>	MAX	UNIT
VIK	OE2 only	V <sub>CC</sub> = 4.5 V,	$V_{EE} = -4.2 V,$	l <sub>l</sub> = –18 mA		-1.2	V
VOL		V <sub>CC</sub> = 4.5 V,	$V_{\text{EE}} = -4.5 \text{ V} \pm 0.3 \text{ V},$	I <sub>OL</sub> = 48 mA	0.38	0.55	V
Ц	OE2 only	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V,$	V <sub>I</sub> = 7 V		0.1	mA
	OE2 only	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V,$	V <sub>I</sub> = 2.7 V		20	
ΙН	A inputs and OE1	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V,$	$V_{I} = -840 \text{ mV}$		350	μA
	OE2 only	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V,$	Vj = 0.5 V		-0.5	mA
۱ <sub>IL</sub>	A inputs and OE1	V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V,$	Vj = -1810 mV	0.5		μΑ
IОН		V <sub>CC</sub> = 4.5 V,	$V_{EE} = -4.2 V,$	V <sub>OH</sub> = 5.5 V		250	μA
ICCH		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V$		66	95	mA
ICCL		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.8 V$		79.5	114	mA
IEE		V <sub>CC</sub> = 5.5 V,	V <sub>EE</sub> = -4.2 V		-23	-33	mA
Ci		V <sub>CC</sub> = 5.5 V,	V <sub>EE</sub> = -4.5 V		5		pF
Co		V <sub>CC</sub> = 5.5 V,	$V_{EE} = -4.5 V$		5		pF

# switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

	FROM	то	CL = 50 pF, R1 = 500 Ω,			
PARAMETER	(INPUT)	(OUTPUT)	MIN	2 = 500 Ω TYP <sup>†</sup>	MAX	UNIT
tPLH			6.4	9.1	11.7	
tere terestere teres	Any A	Y	2.7	4.9	7.2	ns
<sup>t</sup> PLH			7	10.1	13.3	
<sup>t</sup> PHL	OE1 (ECL)	Y	3.6	6.2	8.8	ns
<sup>t</sup> PLH			6.5	9.1	11.6	
<sup>t</sup> PHL	OE2 (TTL)	Y	2.8	5.3	7.9	ns

<sup>†</sup> All typical values are at V<sub>CC</sub> = 5 V, V<sub>EE</sub> = -4.5 V, T<sub>A</sub> =  $25^{\circ}$ C.



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SDZS011 - APRIL 1990

#### PARAMETER MEASUREMENT INFORMATION



- NOTES: A. CL includes probe and jig capacitance.
  - B. For TTL inputs, input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz, Z<sub>0</sub> = 50  $\Omega$ , t<sub>r</sub>  $\leq$  2.5 ns,  $t_f \le 2.5 \text{ ns.}$
  - C. For ECL inputs, input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz, Z<sub>0</sub> = 50  $\Omega$ , t<sub>r</sub>  $\leq$  0.7 ns,  $t_f \le 0.7$  ns.
  - D. The outputs are measured one at a time with one transition per measurement.

#### Figure 1. Load Circuit and Voltage Waveforms



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