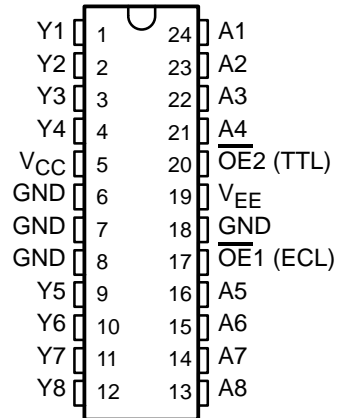


- 10KH Compatible
- Open-Collector Outputs Drive Bus Lines or Buffer Memory Address Registers
- ECL and TTL Output-Enable Inputs
- Flow-Through Architecture Optimizes PCB Layout
- Center-Pin  $V_{CC}$ ,  $V_{EE}$ , and GND Configurations Minimize High-Speed Switching Noise
- Package Options Include “Small Outline” Packages and Standard Plastic 300-mil DIPs

**DW OR NT PACKAGE**  
(TOP VIEW)



### description

This octal ECL-to-TTL translator is designed to provide efficient translation between a 10KH signal environment and a TTL signal environment. This device is designed specifically to improve the performance and density of ECL-to-TTL CPU/bus-oriented functions such as memory-address drivers, clock drivers, and bus-oriented receivers and transmitters while eliminating the need for 3-state overlap protection.

Two pins  $\overline{OE1}$  and  $\overline{OE2}$  are provided for output-enable control. These control inputs are ANDed together with  $\overline{OE1}$  being ECL-compatible and  $\overline{OE2}$  being TTL-compatible. This offers the choice of controlling the outputs of the device from either a TTL or ECL signal environment.

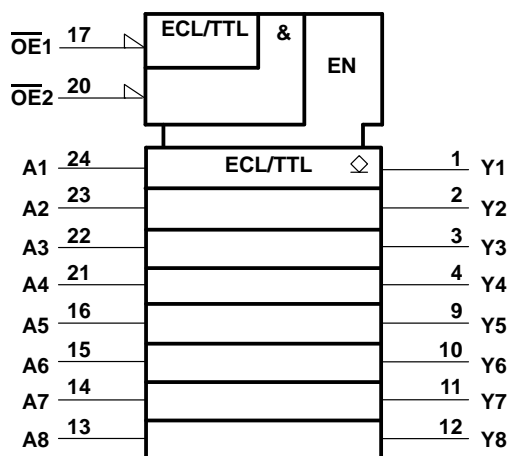
The SN10KHT5539 is characterized for operation from 0°C to 75°C.

**FUNCTION TABLE**

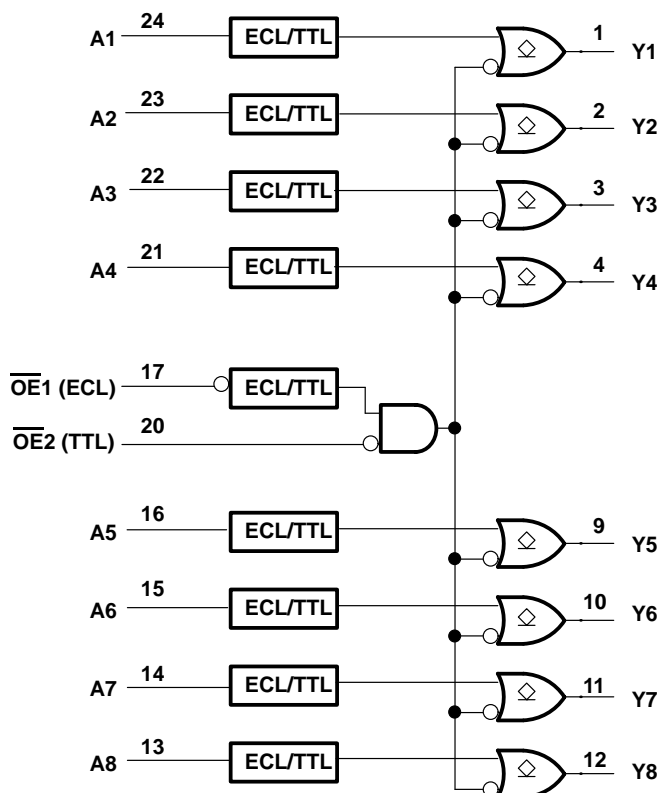
OUTPUT ENABLE		DATA INPUT A	OUTPUT (TTL) Y
$\overline{OE1}$	$\overline{OE2}$		
X	H	X	H
H	X	X	H
L	L	L	L
L	L	H	H

## SDZS007 – JANUARY 1990 – REVISED OCTOBER 1990

**logic diagram (positive logic)**



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.



**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡**

Supply voltage range, $V_{CC}$	-0.5 V to 7 V
Supply voltage range, $V_{EE}$	-8 V to 0 V
Input voltage range: TTL (see Note 1)	-1.2 V to 7 V
ECL	$V_{EE}$ to 0 V
Input current range, TTL	-30 mA to 5 mA
Current into any output in the low state	96 mA
Voltage applied to any output in the high state	-0.5 V to $V_{CC}$
Operating free-air temperature range	0°C to 75°C
Storage temperature range	-65°C to 150°C

‡ Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The TTL input voltage ratings may be exceeded provided the input current ratings are observed.

**recommended operating conditions**

		MIN	NOM	MAX	UNIT
V <sub>CC</sub>	TTL supply voltage	4.5	5	5.5	V
V <sub>EE</sub>	ECL supply voltage	-4.94	-5.2	-5.46	V
V <sub>IH</sub>	TTL high-level input voltage	2			V
V <sub>IL</sub>	TTL low-level input voltage			0.8	V
V <sub>IH</sub>	ECL high-level input voltage†	T <sub>A</sub> = 0°C	-1170	-840	mV
		T <sub>A</sub> = 25°C	-1130	-810	
		T <sub>A</sub> = 75°C	-1070	-735	
V <sub>IL</sub>	ECL low-level input voltage†	T <sub>A</sub> = 0°C	-1950	-1480	mV
		T <sub>A</sub> = 25°C	-1950	-1480	
		T <sub>A</sub> = 75°C	-1950	-1450	
V <sub>OH</sub>	TTL high-level output voltage			5.5	V
I <sub>IK</sub>	TTL input clamp current			-18	mA
I <sub>OL</sub>	Low-level output current			48	mA
T <sub>A</sub>	Operating free-air temperature range	0		75	°C

† The algebraic convention, in which the least positive (most negative) value is designated minimum, is used in this data sheet for logic levels only.

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	MIN	TYP‡	MAX	UNIT
V <sub>IK</sub>	OE2 only V <sub>CC</sub> = 4.5 V, V <sub>EE</sub> = -4.94 V, I <sub>I</sub> = -18 mA			-1.2	V
I <sub>OH</sub>	V <sub>CC</sub> = 4.5 V, V <sub>EE</sub> = -4.94 V, V <sub>OH</sub> = 5.5 V			250	μA
V <sub>OL</sub>	V <sub>CC</sub> = 4.5 V, V <sub>EE</sub> = -5.2 V ± 5%, I <sub>OL</sub> = 48 mA		0.38	0.55	V
I <sub>I</sub>	OE2 only V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V, V <sub>I</sub> = 7 V			0.1	mA
I <sub>IH</sub>	OE2 only V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V, V <sub>I</sub> = 2.7 V			20	μA
I <sub>IL</sub>	OE2 only V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V, V <sub>I</sub> = 0.5 V			-0.5	mA
I <sub>IH</sub>	A inputs and OE1 V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V, V <sub>I</sub> = -840 mV	T <sub>A</sub> = 0°C		350	μA
		T <sub>A</sub> = 25°C		350	
		T <sub>A</sub> = 75°C		350	
I <sub>IL</sub>	A inputs and OE1 V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V, V <sub>I</sub> = -1950 mV	T <sub>A</sub> = 0°C	0.5		μA
		T <sub>A</sub> = 25°C	0.5		
		T <sub>A</sub> = 75°C	0.5		
I <sub>CCH</sub>	V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V		63	91	mA
I <sub>CCL</sub>	V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V		79	114	mA
I <sub>EE</sub>	V <sub>CC</sub> = 5.5 V, V <sub>EE</sub> = -5.46 V		-22	-32	mA
C <sub>i</sub>	V <sub>CC</sub> = 5 V, V <sub>EE</sub> = -5.2 V		6		pF
C <sub>o</sub>	V <sub>CC</sub> = 5 V, V <sub>EE</sub> = -5.2 V		5		pF

‡ All typical values are at V<sub>CC</sub> = 5 V, V<sub>EE</sub> = -5.2 V, T<sub>A</sub> = 25°C.

**SN10KHT5539**  
**OCTAL ECL-TO-TTL TRANSLATOR**  
**WITH OPEN-COLLECTOR OUTPUTS**

SDZS007 – JANUARY 1990 – REVISED OCTOBER 1990

switching characteristics over recommended ranges of operating supply voltage and free-air temperature (unless otherwise noted) (see Figure 1)

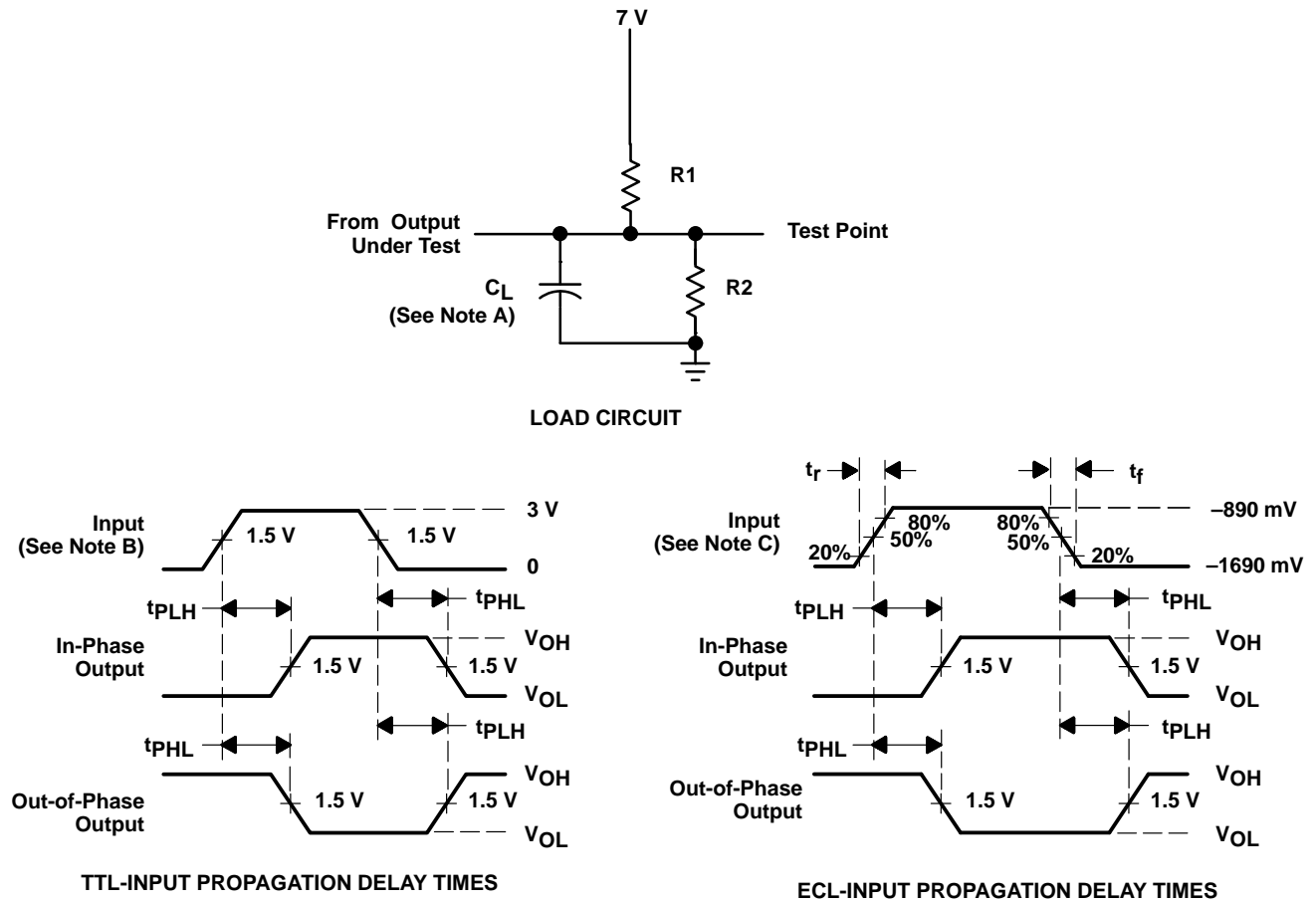
PARAMETER	FROM (INPUT)	TO (OUTPUT)	C <sub>L</sub> = 50 pF, R1 = 500 Ω, R2 = 500 Ω			UNIT
			MIN	TYP†	MAX	
t <sub>PLH</sub>	Any A	Y	6.2	9.3	12.4	ns
t <sub>PHL</sub>			2.6	4.9	7.3	
t <sub>PLH</sub>	$\overline{OE}1$ (ECL)	Y	7.1	10.3	13.5	ns
t <sub>PHL</sub>			3.2	5.8	8.4	
t <sub>PLH</sub>	$\overline{OE}2$ (TTL)	Y	6.5	9.5	12.4	ns
t <sub>PHL</sub>			2.7	5.3	8	

† All typical values are at V<sub>CC</sub> = 5 V, V<sub>EE</sub> = -5.2 V, T<sub>A</sub> = 25°C.



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## PARAMETER MEASUREMENT INFORMATION



- NOTES: A.  $C_L$  includes probe and jig capacitance.  
B. For TTL inputs, input pulses are supplied by generators having the following characteristics:  $PRR \leq 10$  MHz,  $Z_O = 50 \Omega$ ,  $t_r \leq 2.5$  ns,  $t_f \leq 2.5$  ns.  
C. For ECL inputs, input pulses are supplied by generators having the following characteristics:  $PRR \leq 10$  MHz,  $Z_O = 50 \Omega$ ,  $t_r \leq 1.5$  ns,  $t_f \leq 1.5$  ns.  
D. The outputs are measured one at a time with one transition per measurement.

**Figure 1. load circuit and voltage waveforms**

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