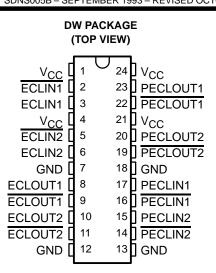
- Dual ECL to Pseudo-ECL and Pseudo-ECL to ECL Translators
- Single 5-V Power Supply
- Advanced BiCMOS Technology
- Typical Application: Interface Between an ECL-Level Optical Transmitter and a Pseudo-ECL-Output Level Parallel-to-Serial Converter
- Packaged in 24-Pin Plastic Small-Outline Package

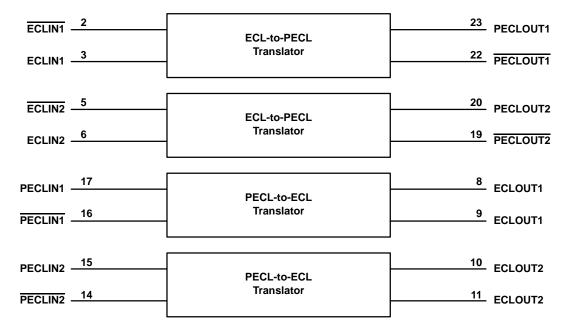
#### description

The TNETA1545 provides four buffers. Two buffers for two differential ECL-input signals referenced to GND are translated to differential psuedo-ECL (PECL) outputs referenced to 5 V instead of GND.

Two buffers for two differential PECL-input signals referenced to 5 V instead of GND are translated to differential ECL outputs referenced to GND.

### functional block diagram







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## TNETA1545 DUAL DIFFERENTIAL PSEUDO-ECL TO ECL TRANSLATORS AND DUAL DIFFERENTIAL ECL TO PSEUDO-ECL TRANSLATORS

SDNS005B - SEPTEMBER 1993 - REVISED OCTOBER 1995

#### TERMINAL 1/0 DESCRIPTION NAME NO. ECLIN1, 3, I ECL-compatible inputs for ECL-to-PECL translator ECLIN1 2 PECLOUT1, 23, 0 PECL-compatible outputs from ECL-to-PECL translator PECLOUT1 22 ECLIN2, 6, I ECL-compatible intpus for ECL-to-PECL translator ECLIN2 5 PECLOUT2, 20, 0 PECL-compatible otuputs from ECL-to-PECL translator PECLOUT2 19 PECLIN1, 17, I PECL-compatible inputs fro PECL-to-ECL translator PECLIN1 16 ECLOUT1. 8, 0 ECL-compatible outputs from PECL-to-ECL translator ECLOUT1 9 PECLIN2, 15. I PECL-compatible inputs for PECL-to-ECL translator PECLIN2 14 ECLOUT2, 10, 0 ECL-compatible outputs from PECL-to-ECL translator ECLOUT2 11 GND 7,12,13,18 Ground (0-V reference) 1,4,21,24 Supply voltage Vcc

#### **Terminal Functions**

#### absolute maximum rating over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage range, V <sub>CC</sub> (see Note 1)	$\dots \dots $
Input voltage range: ECL	–2.5 V to 0 V
PECL	0 V to 7 V
Operating free-air temperature range, T <sub>A</sub>	40° C to 85°C
Storage temperature range, T <sub>stg</sub>	–65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: All voltage values are with respect to GND.

#### recommended operating conditions

			MIN	NOM	MAX	UNIT	
V <sub>CC</sub> Supply voltage			4.75	5	5.25	V	
VIH	High lovel input veltage	ECL (see Note 2)	-1.165		-0.88	v	
	High-level input voltage	PECL (see Note 2)	V <sub>CC</sub> -1.165	V	′CC-0.88		
VIL	Low-level input voltage	ECL (see Note 2)	-1.81		-1.475	v	
		PECL (see Note 2)	V <sub>CC</sub> -1.81	٧c	C-1.475		
TA	Operating free-air temperature		-40		85	°C	

NOTE 2. The algebraic convention, in which the least positive (most negative) value is designated minimum, is used in this data sheet for logic-level voltages only.



#### TNETA1545 DUAL DIFFERENTIAL PSEUDO-ECL TO ECL TRANSLATORS AND DUAL DIFFERENTIAL ECL TO PSEUDO-ECL TRANSLATORS

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#### electrical characteristics over recommended operating free-air temperature range

			-	•	-	-		
	PARA	METER	TEST (	CONDITIONS	MIN	MAX	UNIT	
	High-level output voltage	ECLOUT1, <u>ECLOUT1</u> , ECLOUT2, ECLOUT2	V <sub>CC</sub> = 4.75 V,	See Note 3	-1.025	-0.88	v	
Vон		PECLOUT1, <u>PECLOUT1</u> , PECLOUT2, PECLOUT2	V <sub>CC</sub> = 4.75 V,	See Note 4	V <sub>CC</sub> -1.025	V <sub>CC</sub> -0.84	V	
VOL	Low-level output voltage	ECLOUT1, <u>ECLOUT1</u> , ECLOUT2, ECLOUT2	V <sub>CC</sub> = 4.75 V,	See Notes 2 and 3	-1.81	-1.62	V	
		PECLOUT1, <u>PECLOUT1</u> , PECLOUT2, PECLOUT2	V <sub>CC</sub> = 4.75 V,	See Note 4	V <sub>CC</sub> –1.85	V <sub>CC</sub> –1.62	V	
ін	High-level input current	PECLIN1, <u>PECLIN1</u> , PECLIN2, <u>PECLIN2</u>	V <sub>CC</sub> = 5.25 V,	V <sub>I</sub> = 4.45 V		50	μA	
		ECLIN1, <u>ECLIN1</u> , ECLIN2, ECLIN2	V <sub>CC</sub> = 5.25 V,	V <sub>I</sub> = -0.88 V		-1.5	mA	
۱ <sub>IL</sub>	Low-level input current	PECLIN1, <u>PECLIN1</u> , PECLIN2, <u>PECLIN2</u>	V <sub>CC</sub> = 5.25 V,	V <sub>I</sub> = 3.35 V		50	μA	
		ECLIN1, <u>ECLIN1,</u> ECLIN2, ECLIN2	V <sub>CC</sub> = 5.25 V,	V <sub>I</sub> = -1.81 V		-2.5	mA	
lcc	Supply current		V <sub>CC</sub> = 5.25 V, See Note 5	See Note 5		75	mA	
			V <sub>CC</sub> = 5.25 V,	See Note 6		125	mA	

NOTES: 2. The algebraic convention, in which the least positive (most negative) value is designated minimum, is used in this data sheet for logic-level voltages only.

3. These outputs are terminated through a 50- $\Omega$  resistor to -2 V.

4. These outputs are terminated with a 50- $\Omega$  resistor to V<sub>CC</sub>-2 V.

5. All outputs open

6. ECLOUT1, ECLOUT2, ECLOUT2 terminated with a 50-Ω resistor to -2 V.
PECLOUT1, PECLOUT1, PECLOUT2, PECLOUT2 terminated with a 50-Ω resistor to V<sub>CC</sub>-2 V.

# switching charactertistics over recommended operating free-air temperature range, $V_{CC}$ = 5 V $\pm 0.25$ V

PARAMETER	FROM (INPUT)	TO (OUTPUT)	MIN	МАХ	UNIT
fmax			250		MHz
<sup>t</sup> PLH	ECLIN/ECLIN or PECLIN/PECLIN	PECLOUT/PECLOUT or ECLOUT/ECLOUT	1.5	4	ns
<sup>t</sup> PHL	ECLIN/ECLIN or PECLIN/PECLIN	PECLOUT/PECLOUT or ECLOUT/ECLOUT	1.5	4	ns



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