SDLS131

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

These devices contain three independent 3-input AND gates.

The SN54LS11 and SN54S11 are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN74LS11 and SN74S11 are characterized for operation from 0 °C to 70 °C.

FUNCTION TABLE	(each gate)
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H	NPUT	s	OUTPUT
A	В	С	Y
н	н	н	н
L	х	x	L
Х	L	x	L
х	х	L	L

logic symbol[†]

•



[†]This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

SN54LS11, SN54S11, SN74LS11, SN74S11 TRIPLE 3-INPUT POSITIVE-AND GATES APRIL 1985—REVISED MARCH 1988

SN54LS11, SN74S11 ... J OR W PACKAGE SN74LS11, SN74S11 . . . D OR N PACKAGE (TOP VIEW) 1A 🗖 1 J14∐ Vcc 1**B** 13 1C 120 1Y ₫3 2A 11 30 28 ₫4 2C 3B 10 3A 2Υ 6 Цe GND 80 3Y П,





NC-No internal connection

logic diagram (positive logic)



PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



SN54LS11, SN54S11, SN74LS11, SN74S11 TRIPLE 3-INPUT POSITIVE AND GATES

schematics (each gate)



Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	• • • • • • • • • • • • • • • • • • • •	
Input voltage: 'S11		5. 5 V
'LS11		7V
Operating free-air temperature range:	SN54′	-55°C to 125°C
	SN74'	0°C to 70°C
Storage temperature range	••••••••••••••••••	-65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.



recommended operating conditions

	s	SN54LS11 SN74LS11		UNIT			
	MIN	NOM	МАХ	MIN	NOM	MAX	UNIT
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	v
V _{IH} High-level input voltage	2			2			v
VIL Low-level input voltage			0,7			0.8	V
IOH High-level output current			- 0.4			- 0.4	mA
IOL Low-level output current			4			8	mA
T _A Operating free-air temperature	55		1 25	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	TEST CONDITIONS +		SN54LS11			S				
PARAMETER		TEST CONDI	TIONS T	MIN	TYP‡	MAX	MIN	TYP ‡	MAX	UNIT
VIK	V _{CC} = MIN,	lj ≠ — 18 mA				- 1.5			- 1.5	v
∨он	V _{CC} = MIN,	V _{IH} = 2 V,	l _{OH} = - 0.4 mA	2.5	3.4		2.7	3.4		V
	Vcc - MIN,	VIL = MAX,	I _{OL} = 4 mA		0.25	0.4		0.25	0.4	- V
VOL	VCC = MIN,	V _{IL} = MAX,	I _{OL} = 8 mA					0.35	0.5	
4	V _{CC} = MAX,	V ₁ = 7 V				0.1			0.1	mΑ
Чн	V _{CC} = MAX,	V ₁ = 2.7 V				20			20	μA
۱۱L	V _{CC} = MAX,	V ₁ = 0.4 V				0.4			- 0.4	mA
IOS §	V _{CC} = MAX			- 20		- 100	- 20		- 100	mΑ
^I ссн	V _{CC} = MAX,	V ₁ = 4.5 V			1.8	3.6		1.8	3.6	mΑ
ICC∟	V _{CC} = MAX,	V = 0 V			3.3	6.6		3,3	6.6	mA

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† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$. § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

switching characteristics, V_{CC} = 5 V, T_A = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN	түр	MAX	UNIT
ΦLH	A, B or C	v	B ₁ = 2 kΩ,	C _I = 15 pF		8	15	ns
^t PHL				6L - 19 bi		10	20	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

SN54S11, SN74S11 **TRIPLE 3-INPUT POSITIVE-AND GATES**

recommended operating conditions

		SN54S11 SN7		SN74S11	V74S11		
	MIN	NOM	MAX	MIN	NOM	МАХ	UNIT
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	v
VIH High-level input voltage	2			2			v
V _{IL} Low-level input voltage			0.8		• •	0.8	V
OH High-level output current			- 1			- 1	mΑ
IOL Low-level output current			20			20	mA
T _A Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		SN54S11	SN74S11	
	TEST CONDITIONS †	MIN TYP # MAX	MIN TYP # MAX	
V _{1K}	$V_{CC} = MIN$, $I_I = -18 \text{ mA}$	- 1.2	- 1.2	v
V _{OH}	$V_{CC} = MIN$, $V_{IH} = 2V$, $I_{OH} = -1 mA$	2.5 3.4	2.7 3.4	v
V _{OL}	$V_{CC} = MIN, V_{1L} = 0.8 V, I_{OL} = 20 mA$	0.5	0.5	V
4	V _{CC} = MAX, V _I = 5.5 V	1	1	mA
ЧΗ	V _{CC} = MAX, V _I = 2.7 V	50	50	μA
կլ	V _{CC} = MAX, V _t = 0.5 V	- 2	- 2	mA
IOS §	V _{CC} = MAX	-40 -100	- 40 - 100	mA
ссн	V _{CC} = MAX, V _I = 4.5 V	13.5 24	13.5 24	mΑ
ICCL	V _{CC} = MAX, V ₁ = 0 V	24 42	24 42	mA

1 For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. For contribute and the short of the short-circuit should not exceed one second. § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	TYP	мах	UNIT	
^t PLH			P 290 O	C ₁ = 15 pF		4.5	7	ns
^t PHL	A, BorC	v L	R _L = 280 Ω,			5	7.5	ns
^t PLH	A, 0 0, 0	. ſ	B. = 200 o	0 - 50 - 5		6	-	រាទ
tph L			R _L = 280 Ω,	С _L = 50 рF		7.5		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

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