

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

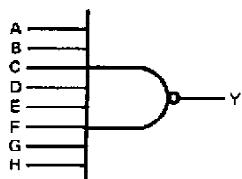
**description**

These devices contain a single 8-input NAND gate.

The SN5430, SN54LS30, and SN54S30 are characterized for operation over the full military range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN7430, SN74LS30, and SN74S30 are characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

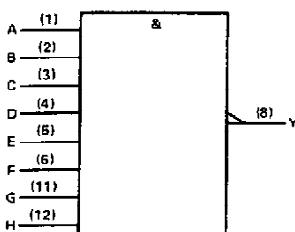
**FUNCTION TABLE**

INPUTS A THRU H	OUTPUT Y
All inputs H	L
One or more inputs L	H

**logic diagram****positive logic**

$$Y = \overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D} \cdot \overline{E} \cdot \overline{F} \cdot \overline{G} \cdot \overline{H} \quad \text{or}$$

$$Y = \overline{\overline{A}} + \overline{\overline{B}} + \overline{\overline{C}} + \overline{\overline{D}} + \overline{\overline{E}} + \overline{\overline{F}} + \overline{\overline{G}} + \overline{\overline{H}}$$

**logic symbol†**

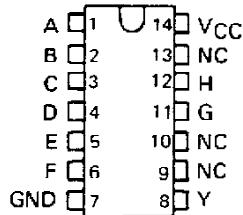
†This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

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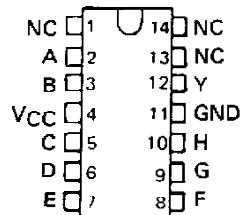
**SN5430 . . . J PACKAGE**  
**SN54LS30, SN54S30 . . . J OR W PACKAGE**  
**SN7430 . . . N PACKAGE**  
**SN74LS30, SN74S30 . . . D OR N PACKAGE**

(TOP VIEW)



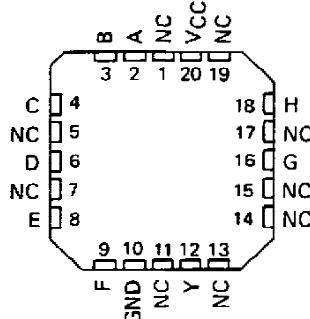
**SN5430 . . . W PACKAGE**

(TOP VIEW)



**SN54LS30, SN54S30 . . . FK PACKAGE**

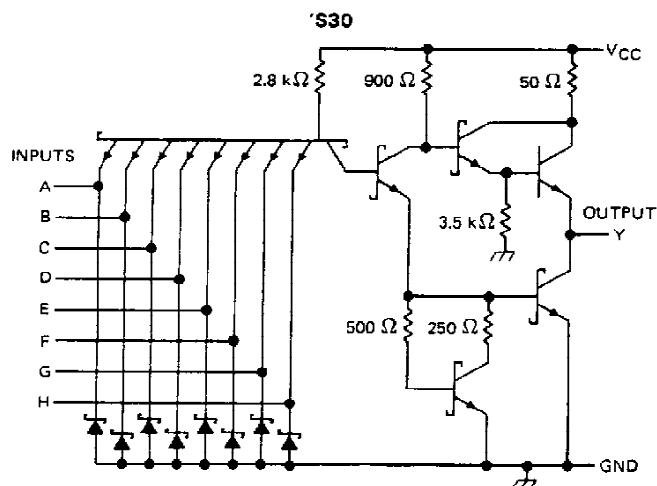
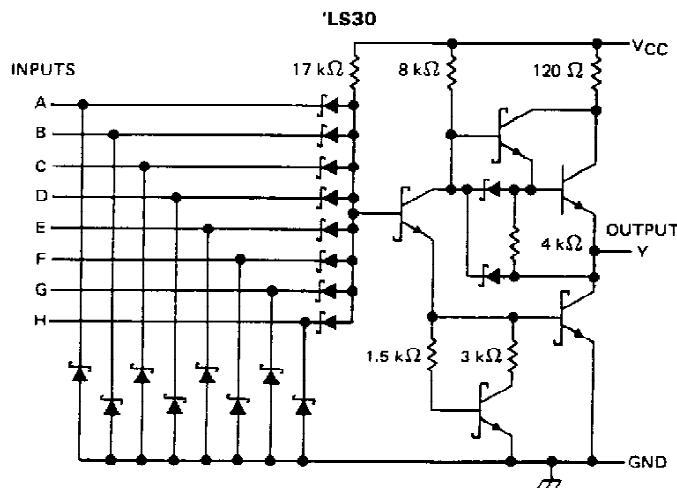
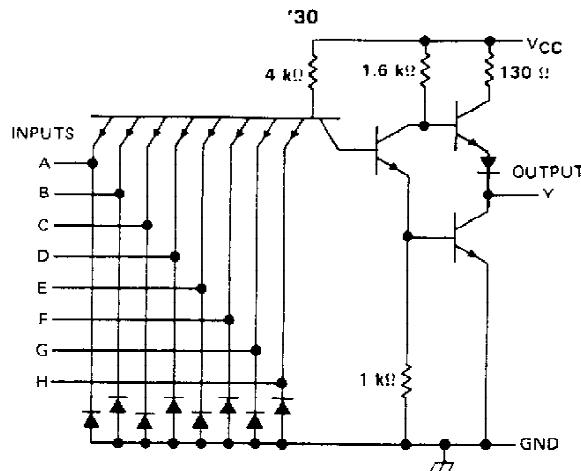
(TOP VIEW)



NC - No internal connection

**SN5430, SN54LS30, SN54S30,  
SN7430, SN74LS30, SN74S30  
8-INPUT POSITIVE-NAND GATES**

schematics (each gate)



Resistor values shown are nominal.

**TEXAS  
INSTRUMENTS**

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## **SN5430, SN7430 8-INPUT POSITIVE-NAND GATES**

**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)**

NOTE 1: Voltage values are with respect to network ground terminal.

#### **recommended operating conditions**

	V <sub>CC</sub>	SN5430			SN7430			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V <sub>CC</sub>	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High-level input voltage		2		2			V
V <sub>IL</sub>	Low-level input voltage			0.8			0.8	V
I <sub>OH</sub>	High-level output current			-0.4			-0.4	mA
I <sub>OL</sub>	Low-level output current			16			16	mA
T <sub>A</sub>	Operating free-air temperature	-55		125	0		70	°C

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS †	SN5430			SN7430			UNIT
		MIN	TYP‡	MAX	MIN	TYP‡	MAX	
V <sub>IK</sub>	V <sub>CC</sub> = MIN, I <sub>I</sub> = -12 mA			-1.5			-1.5	V
V <sub>OH</sub>	V <sub>CC</sub> = MIN, V <sub>IIL</sub> = 0.8 V, I <sub>OH</sub> = -0.4 mA	2.4	3.4		2.4	3.4		V
V <sub>OL</sub>	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 16 mA		0.2	0.4		0.2	0.4	V
I <sub>I</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V			1			1	mA
I <sub>IH</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.4 V			40			40	µA
I <sub>IL</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4 V			-1.6			-1.6	mA
I <sub>OS\$</sub>	V <sub>CC</sub> = MAX	-20	-55	-18	-55			mA
I <sub>CCH</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0		1	2		1	2	mA
I <sub>CCL</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V		3	6		3	6	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

All typical values are at  $V_{CC} = 5\text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

† All typical values are at  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$ .  
§ Not more than one output should be shorted at a time.

switching characteristics.  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$  (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t <sub>PLH</sub>	Any	Y	R <sub>L</sub> = 400 Ω, C <sub>L</sub> = 15 pF	13	22	ns	
t <sub>PHL</sub>				8	15	ns	

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

## **SN54LS30, SN74LS30 8-INPUT POSITIVE-NAND GATES**

**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)**

NOTE 1: Voltage values are with respect to network ground terminal.

#### **recommended operating conditions**

			SN54LS30			SN74LS30			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX			
V <sub>CC</sub> Supply voltage	4.5	5	5.5	4.75	5	5.25			V
V <sub>IH</sub> High-level input voltage		2			2				V
V <sub>IL</sub> Low-level input voltage				0.7			0.8		V
I <sub>OH</sub> High-level output current				-0.4			-0.4		mA
I <sub>OL</sub> Low-level output current					4		8		mA
T <sub>A</sub> Operating free-air temperature	-55		125	0		70			°C

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS †	SN54LS30			SN74LS30			UNIT
		MIN	TYP‡	MAX	MIN	TYP‡	MAX	
V <sub>IK</sub>	V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA			-1.5			-1.5	V
V <sub>OH</sub>	V <sub>CC</sub> = MIN, V <sub>I</sub> L = MAX, I <sub>OH</sub> = -0.4 mA	2.5	3.4		2.7	3.4		V
V <sub>OL</sub>	V <sub>CC</sub> = MIN, V <sub>I</sub> H = 2 V, I <sub>OL</sub> = 4 mA		0.25	0.4			0.4	V
	V <sub>CC</sub> = MIN, V <sub>I</sub> H = 2 V, I <sub>OL</sub> = 8 mA					0.25	0.5	
I <sub>I</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 7 V			0.1			0.1	mA
I <sub>I</sub> H	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.7 V			20			20	µA
I <sub>I</sub> L	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4 V			-0.4			-0.4	mA
I <sub>OS</sub> §	V <sub>CC</sub> = MAX	-20	-100	-20	-100			mA
I <sub>CCH</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0	0.35	0.5		0.35	0.5		mA
I <sub>CCI</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V	0.6	1.1		0.6	1.1		mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

<sup>†</sup> For conditions shown as MIN or MAX, use the lower value for MIN and the higher value for MAX.

6. Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

switching characteristics,  $V_{CC} = 5\text{ V}$ ,  $T_A = 25^\circ\text{C}$  (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t <sub>PLH</sub>	Any	Y	R <sub>L</sub> = 2 kΩ, C <sub>L</sub> = 15 pF	8	15	ns	
t <sub>PHL</sub>				13	20	ns	

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



## **SN54S30, SN74S30 8-INPUT POSITIVE-NAND GATES**

**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)**

NOTE 1: Voltage values are with respect to network ground terminal.

#### **recommended operating conditions**

		SN54S30			SN74S30			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V <sub>CC</sub>	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High-level input voltage		2			2		V
V <sub>IL</sub>	Low-level input voltage				0.8		0.8	V
I <sub>OH</sub>	High-level output current				-1		-1	mA
I <sub>OL</sub>	Low-level output current				20		20	mA
T <sub>A</sub>	Operating free-air temperature	-55		125	0		70	°C

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS <sup>†</sup>	SN54S30			SN74S30			UNIT
		MIN	TYP <sup>‡</sup>	MAX	MIN	TYP <sup>‡</sup>	MAX	
V <sub>IK</sub>	V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA			-1.2			-1.2	V
V <sub>OH</sub>	V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.8 V, I <sub>OH</sub> = -1 mA	2.5	3.4		2.7	3.4		V
V <sub>OL</sub>	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OL</sub> = 20 mA			0.5			0.5	V
I <sub>I</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5 V			1			1	mA
I <sub>IH</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.7 V			50			50	μA
I <sub>IL</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.5 V			-2			-2	mA
I <sub>OS</sub> <sup>§</sup>	V <sub>CC</sub> = MAX	-40		-100	-40		-100	mA
I <sub>CCH</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0		3	5		3	5	mA
I <sub>CCL</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V		5.5	10		5.5	10	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

All typical values are at  $V_{CC} = 5\text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

**Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.**

switching characteristics.  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$  (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$t_{PLH}$	Any	Y	$R_L = 280 \Omega$ , $C_L = 15 \text{ pF}$	4	6		ns
$t_{PHL}$				4.5	7		ns
$t_{PLH}$		Y	$R_L = 280 \Omega$ , $C_L = 50 \text{ pF}$	5.5			ns
$t_{PHL}$				6.5			ns

**NOTE 2:** Load circuits and voltage waveforms are shown in Section 1.



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