SN54125, SN54126, SN54LS125A, SN54LS126A, SN74125, SN74126, SN74LS125A, SN74LS126A QUADRUPLE BUS BUFFERS WITH 3-STATE OUTPUTS December 1983 - REVISED MARCH 1988

SDLS044

- Quad Bus Buffers
- 3-State Outputs
- Separate Control for Each Channel

description

These bus buffers feature three-state outputs that, when enabled, have the low impedence characteristics of a TTL output with additional drive capability at high logic levels to permit driving heavily loaded bus lines without external pull-up resistors, when disabled, both output transistors are turned off presenting a high-impedance state to the bus so the output will act neither as a significant load nor as a driver. The '125 and 'LS125A outputs are disabled when \overline{G} is high. The '126 and 'LS126A outputs are disabled when G is low.

logic diagram (each gate)







positive logic Y = A

logic symbols[†]



SN54125, SN54126, SN54LS125A.

SN54LS126A ... J OR W PACKAGE SN74125, SN74126 ... N PACKAGE

SN74LS125A, SN74LS126A . . . D OR N PACKAGE

SN54LS125A, SN54LS126A . . . FK PACKAGE (TOP VIEW)





NC - No internal connection

'LS126A ʻ125 'LS125A **'126** 1G (1) 16 <u>(1)</u> (1) (1)(3) 17 10 (3) 17 1G EN 1D (3) 1Y 16 EN ΕN 131 IY ΕN 1A 121 1A (2) 1A <u>(2)</u> 2G (4) ∇ 5 5 V (2) 1A 26 (4) 2G [4] (4) (<u>6)</u> ____ 2Y (6) 2Y (<u>B)</u> 2Y (<u>6)</u> 2Y 2G 24 (5) 2A (5) 2A (51 (5) 2A 3G (10) p (10) (10) (10) 181 3Y (8) 3Y (8) 3Y 3**G** 3G ЗG (8) 3Y 3A (9) (8) (9) 191 3A 3A 3A (13) (13) (13) (13) (<u>11)</u> 4Y (11) 4Y 1<u>11)</u> 49 4Ĝ 4G <u>(11)</u> 4¥ 40 4G (12) (12) (12) (1Z) ۵۵ 4A 4A 4A

 † These symbols are in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12. Pin numbers shown are for D, J, N, and W packages.

PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



SN54125, SN54126, SN74125, SN74126 QUADRUPLE BUS BUFFERS WITH 3-STATE OUTPUTS

schematics (each gate)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (See Note 1)		
Input voltage		5.5 V
Operating free-air temperature range:	SN54'	–55°C to 125°C
	SN74'	0°C to 70°C
Storage temperature range	•••••••••••••••••	-65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

pply voltage, VCC (see Note 1)		 	 	 	 	 	 				7 V
out voltage		 	 	 	 	 	 				7 V
erating free-air temperature range	: SN54'	 	 	 	 	 	 	- 5	5°C to	12	5 °C
-	SN74′										
prage temperature range		 	 	 	 	 	 	- 6	5°C to	150)°C

NOTE 1: Voltage values are with respect to network ground terminals.

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SN54125, SN54126, SN74125, SN74126 QUADRUPLE BUS BUFFERS WITH 3-STATE OUTPUTS

recommended operating conditions

		SN54	SN54125, SN54126					
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			V
¥IL.	Low-level input voltage			0.8			0.8	V
юн	High-level output current			- 2			- 5.2	mА
IOL .	Low-level output current			16			16	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS T			SN54	125, SN	54126	SN74	· · · · -		
/ ANAMIE I CH		MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT		
Viк	V _{CC} = MIN,	lj = 12 mA				1.5			1.5	V
V _{CC} = MIN, \		V _{1H} = 2 V,	$I_{OH} = -2 \text{ mA}$	2.4	3.3					
∨он	V _{IL} = 0.8 V		$I_{OH} = -5.2 \text{ mA}$				2.4	3.1		v
VOL	V _{CC} = MIN, I _{OL} = 16 mA	V _{IH} ≠ 2 V,	V _{1L} = 0.8 V,			0.4			0.4	v
Vcc = M	V _{CC} = MAX,	V _{IH} = 2 V,	V ₀ = 2.4 V			40			40	<u> </u>
'oz	V _{IL} = 0.8 V	ŀ	V ₀ - 0.4 V			- 40			- 40	μA
11	V _{CC} = MAX,	V _I = 6.5 V				1			1	mA
ЧН	VCC = MAX,	VI = 2.4 V				40	!		40	μA
ΊL	V _{CC} = MAX,	Vi = 0.4 V				- 1.6			- 1.6	mA
os\$	V _{CC} = MAX			- 30		- 70	- 28		70	mΑ
100	VCC = MAX,		125		32	54		32	54	
'cc	(see Note 2)		'126		36	62		36	62	mΑ

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25 °C$. [§] Not more than one output should be shorted at a time.

NOTE 2: Data inputs = 0.V; output control = 4.5 V for '125 and 0 V for '126,

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 3)

PARAMETER	TEST COND!	S	SN						
	TEST CONUT	MIN	TYP	MAX	MIN	TYP	MAX		
^t PLH				8	13		8	13	пя
tPHL .	RL = 400 Ω,	С _L = 50 р F	-	12	18		12	18	ns
^t PZH				11	17		11	18	ns
TPZL				16	25		16	25	ns
^t PHZ	R ₁ = 400 Ω,			5	8		10	16	ns
TPLZ	N(- 400 11,	С _L = 5 рҒ		7	12		12	18	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



SN54LS125A, SN54LS126A, SN74LS125A, SN74LS126A QUADRUPLE BUS BUFFERS WITH 3-STATE OUTPUTS

recommended operating conditions

		SN SN	SN SN					
		MIN	NOM	MAX	MIN	NOM	MAX	
VCC	Supply voltage	4.5	5	5.5	4.75	5	5.25	v
ViH	High-level input voltage	2			Ż			v
VIL	Low-level input voltage	· · · · · · · · · · · · · · · · · · ·		0.7			0.8	V
ЮН	High-level output current			- 1			- 2.6	mА
l0L	Low-level output current			12			24	mA
Τ _Α	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS			SN SN	SN SN	UNIT						
				MIN	TYP‡	MAX	MIN	TYP‡	MAX			
Viκ	V _{CC} = MIN,	lj = − 18 mA				- 1.5	1		1.5	V		
Voн	V _{CC} = MIN,	V _{IL} = 0.7 V,	10H = - 1 mA	2.4			1					
•UH	VIH = 2 V	V _{IL} = 0.8 V,	¹ ОН = — 2.6 mA				2.4					
	Vcc = MIN,	VIL = 0.7 V,			0.25	0,4						
VOL	VIH = 2 V	VIL = 0.8 V.	lot = 12 mA	-		_		0.25	0.4			
		V _{IL} = 0.8 V,	IOL = 24 mA	-				0.35	0.5	ţ		
	IOZ V _{CC} = MAX, V _{IH} = 2 V	V _{IL} = 0.7 V	V ₀ = 2.4 V	- 1		20						
107		V _{CC} = MAX, V _{IL} = 0.7 V V _Q = 0.4 V	Vo - 0.4 V			- 20						
102		V _{IH} = 2 V	/IH = 2 V VIL = 0.8 V	V _O = 2.4 V			•			20	μA	
		VIL = 0.8 V	Vo = 0.4 V	_					- 20	1		
<u> </u>	V _{CC} = MAX,	Vi = 7 V				0.1			0.1	mA		
ЧН	V _{CC} = MAX,	V ₁ = 2.7 V				20			20	μA		
	VCC = MAX,	'LS125A-G in	puts			- 0.2			- 0.2	mА		
^I IL	VI = 0.4 V (LS125A-A inputs; /LS126A All inputs		puts: 'LS126A All inputs			- 0.4	·		- 0.4	mA		
'OS\$	V _{CC} = MAX		··	- 40		- 225	- 40		- 225	mA		
1cc	V _{CC} = MAX,		LS125A		11	20		11	20			
	(see Note 2)		'LS126A		12	22		12	22	mΑ		

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at $V_{CC} = 5 V_1 T_A = 25^2 C_2$

§ Not more than one output should be shorted at a time, and duration of the short circuit should not exceed one second.

NOTE 2: Data inputs = 0 V: Output controls = 4.5 V for 'LS125A and 0 V for 'LS126A.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25 °C$ (see note 3)

PARAMETER	TEST CONDIT	SN	SN						
		MIN	TYP	MAX	MIN	TYP	MAX	UNIT	
^T PLH	R <u>L</u> = 667 Ω,			9	15		9	15	ns
TPHL		C ₁ = 45 pF		7	18		8	18	ns
^t PZH		0[-40 pF		12	20	1	16	25	nş
^t PZL				15	25		21	35	ns
IPHZ	R ₁ = 667 Ω,	<u>г</u> . – Е – Г			20			25	пѕ
tPLZ	NL - 007 12,	С _L = 5 р F			20			25	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

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