SDLS027

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

These devices contain four independent 2-input-NOR gates.

The SN5402, SN54LS02, and SN54S02 are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN7402, SN74LS02, and SN74S02 are characterized for operation from 0 °C to 70 °C.

FUNCTION TABLE	leach	gate)
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INP	UTS	OUTPUT
A	в	Y
н	x	L
х	н	Ĺ
Ĺ	L	н

logic symbol[†]



[†]This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

logic diagram (positive logic)



PRODUCTION DATA documents contain information current as of publication dats. Preducts conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include tasting of all parameters.

SN5402, SN54LS02, SN54S02, SN7402, SN74LS02, SN74S02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

DECEMBER 1983-REVISED MARCH 1988



2Y [4	11 <u> </u> 4A
2A 🗋	5	10 J 3 Y
2B	6	9 🗍 3 B
GND	7	8 3A

SN5402 ... W PACKAGE (TOP VIEW)

1A [ſ,	U	14	Ь	4Y
18 [2		13	þ	4B
1Y 🗆	3		12	þ	4A
Vcc ⊑	4	•	11	þ	GND
2Y [5		10	þ	3B
2A [6		9	þ	3A
28	2		8	þ	3Y

SN54LS02, SN54S02 . . . FK PACKAGE



NC - No internal connection



SN5402, SN54LS02, SN54S02, SN7402, SN74LS02, SN74S02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

schematics (each gate)



Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	7 V
Input voltage: '02, 'S02	
'LS02	7 V
Off-state output voltage	7V
Operating free-air temperature range: SN54'	-55°C to 125°C
SN74′	
Storage temperature range	-65°C to 150°C

NOTE 1. Voltage values are with respect to network ground terminal.



SN5402, SN7402 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

recommended operating conditions

		SN5402			SN7402			
	MIN	NOM	MAX	MIN	NOM	мах	UNIT	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	ν	
VIH High-level input voltage	2			2			v	
VIL Low-level input voltage			0.8			0.8	v	
OH High-level output current			- 0.4			- 0.4	mΑ	
OL Low-level output current			16			16	mΑ	
TA Operating free-air temperature	55		125	0		70	°c	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	TEST CONDITIONS T			SN5402			SN7402			
PARAMETER				MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
¥iκ	V _{CC} = MIN,	l ₁ = - 12 mA				- 1.5			- 1.5	V
∨он	V _{CC} = MIN,	V _{IL} = 0.8 V,	I _{OH} = - 0.4 mA	2.4	3.4		2.4	3.4		V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 16 mA		0.2	0.4		0.2	0.4	V
4	V _{CC} = MAX,	V ₁ = 5.5 V				1			1	mA
ін	V _{CC} = MAX,	Vi = 2.4 V				40			40	μA
μL	V _{CC} = MAX,	V ₁ = 0.4 V				- 1. 6			— 1. 6	mΑ
IOS§	V _{CC} = MAX			- 20		- 55	- 18		- 55	mΑ
^і ссн	V _{CC} = MAX,	V = 0 V			8	16		8	16	mΑ
ICCL	V _{CC} = MAX,	See Note 2			14	27		14	27	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

\$ Not more than one output should be shorted at a time.

NOTE 2: One input at 4.5 V, all others at GND.

switching characteristics, V_{CC} = 5 V, T_A = 25°C (see note 3)

PARAMETER	FROM (INPUT)	то (о υт рит)	TEST CONDITIONS	MIN	түр	MAX	UNIT
^t PLH		N			12	22	ns
^t PHL	A or B	Ŷ	R _L = 400 Ω, C _L = 15 pF		8	15	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

SN54LS02, SN74LS02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

recommended operating conditions

			SN54LS02			SN74LS02			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
VIH	High-level input voltage	2	_		2			۷	
VIL	Low-level input voltage			0.7			0.8	v	
юн	High-level output current			- 0.4			- 0.4	mA	
10L	Low-level output current			4			8	mΑ	
TA	Operating free-air temporature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

				SN54LS02			SN74LS02			
PARAMÉTER		TEST CONDITIONS T			TYP‡	MAX	MIN	TYP‡	MAX	
۷ıк	VCC = MIN,	l _j = - 18 mA				- 1.5	1		- 1.5	v
VOH	V _{CC} = MIN,	V _{IL} = MAX,	[†] OH = - 0.4 mA	2.5	3.4		2.7	3.4		v
	V _{CC} - MIN,	V _{IH} = 2 V,	l _{OL} = 4 mA	-	0.25	0.4		0.25	0.4	- v
VOL	VCC = MIN,	V _{IH} = 2 V,	IOL = 8 mA					0.35	0.5	
4	V _{CC} = MAX,	Vi = 7 V				0.1			0 .1	mΑ
lін	V _{CC} = MAX,	V ₁ = 2.7 V				20			20	μA
Ι _{ΙL}	VCC = MAX,	V) = 0.4 V	· · · · · · · · · · · · · · · · · · ·			- 0.4			- 0.4	mΑ
los§	V _{CC} - MAX			- 20		- 100	- 20		- 100	mΑ
^і ссн	V _{CC} = MAX,	V = 0 V			1.6	3.2		1.6	3.2	mΑ
ICCL	V _{CC} = MAX,	See Note 2			2.8	5.4		2.8	5.4	mА

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

 \pm All typical values are at V_C = 5 V, T_A = 25^oC § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second. NOTE 2: One input at 4.5 V, all others at GND.

switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST COND	MIN	түр	МАХ	UNIT	
t₽LH	A or B	Ý		C. = 15 pE		10	15	ńs
^t PHL	A OLD	1	R _L = 2 kΩ,	CL = 15 pF		10	15	ПS

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

SN54S02, SN74S02 QUADRUPLE 2-INPUT POSITIVE-NOR GATES

recommended operating conditions

			SN54S02			SN74S02			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
V _{CC} S	Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
V _{IH} F	ligh-level input voltage	2			2			v	
VIL L	.ow-level input voltage		-	0.8			0.8	v	
IOH F	ligh-level output current			- 1			- 1	mΑ	
IOL L	ow-level output current			20			20	mA	
T _A C	Operating free-air temperature	55		125	0		70	°C	

*

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS T	SN54S02	SN74S02	UNIT
	TEST CONDITIONS I	MIN TYP‡ MAX	MIN TYP‡ MAX	
۷ _{IK}	V _{CC} = MIN, I _I = -18 mA	-1.2	-1.2	V
V _{OH}	V _{CC} = MIN, V _{IL} = 0.8 V, I _{OH} = -1 mA	2.5 3.4	2.7 3.4	V
VOL	V _{CC} = MIN, V _{IH} = 2 V, I _{OL} = 20 mA	0.5	0.5	v
4	V _{CC} = MAX, V ₁ = 5.5 V	1	1	mA
—————————————————————————————————————	V _{CC} = MAX, V ₁ = 2.7 V	50	50	μA
ΙL	V _{CC} = MAX, V ₁ = 0.5 V	-2	-2	mA
los§	V _{CC} = MAX	-40 -100	-40 -100	mA
ссн	V _{CC} = MAX, V _I = 0 V	17 29	17 29	mA
ICCL	V _{CC} = MAX, See Note 2	26 45	26 45	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$. § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

NOTE 2: One input at 4.5 V, all others at GND,

switching characteristics, V_{CC} = 5 V, T_A = 25° C (see note 3)

PARAMETER	FROM (INPUT)	TO (QUTPUT)	TEST CONDITIONS	MIN TYP	MAX	UNIT
^t PLH	A or B	Y	$R_1 = 280 \Omega$, $C_1 = 15 \rho F$	3,5	5,5	ns
^t PHL			R _L = 280 Ω, C _L = 15 ρF	3.5	5,5	ns
^t ₽LH			D 200 0 0 0 50 50	5		ns
^t PHL			$R_{L} = 280 \ \Omega$, $C_{L} = 50 \ pF$	5		ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

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