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- Local Bus-Latch Capability
- Noninverting Logic
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

#### description

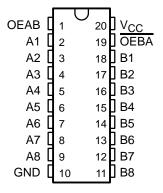
These octal bus transceivers are designed for asynchronous communication between data buses. The control function implementation allows for maximum flexibility in timing.

These devices allow data transmission from the A bus to the B bus or from the B bus to the A bus depending upon the logic levels at the output enable (OEAB and OEBA) inputs.

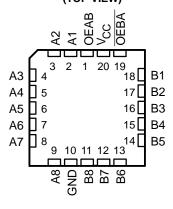
The output-enable inputs can be used to disable the device so that the buses are effectively isolated. The dual-enable configuration gives the transceivers the capability of storing data by simultaneously enabling OEAB and OEBA. Each output reinforces its input in this configuration. When both OEAB and OEBA are enabled and all other data sources to the two sets of bus lines are at high impedance, both sets of bus lines (16 in all) will remain at their last states.

The SN54F623 is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to 125°C. The SN74F623 is characterized for operation from 0°C to 70°C.

#### SN54F623 . . . J PACKAGE SN74F623 . . . DW OR N PACKAGE (TOP VIEW)



## SN54F623 . . . FK PACKAGE (TOP VIEW)



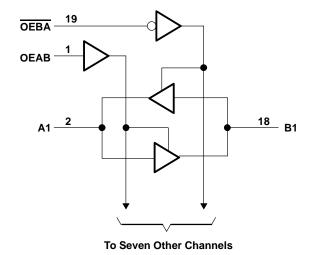
#### **FUNCTION TABLE**

INP	UTS	OPERATION					
OEBA	OEAB						
L	L	B data to A bus					
L	Н	B data to A bus, A data to B bus					
Н	L	Isolation					
Н	Н	A data to B bus					

#### logic symbol†

#### **OEBA** EN1 **OEAB** EN2 В1 **▽ 1** $\triangleright$ 17 Α2 **B2** 16 15 В4 Α4 14 Α5 **B5** 13 **B6** A6 12 **B7** Α7 11 **B8**

## logic diagram (positive logic)



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V <sub>CC</sub>		0.5 V to 7 V
Input voltage range, VI (excluding I/O p	orts) (see Note 1)	1.2 V to 7 V
Input current range, I <sub>IK</sub>		–30 mA to 5 mA
Voltage range applied to any output in	the disabled or power-off state .	0.5 V to 5.5 V
Voltage range applied to any output in	the high state	0.5 V to V <sub>CC</sub>
Current into any output in the low state	: SN54F623 (A1-A8)	40 mÅ
	SN54F623 (B1-B8)	96 mA
	SN74F623 (A1-A8)	48 mA
	SN74F623 (B1-B8)	128 mA
Operating free-air temperature range:	SN54F623	–55°C to 125°C
	SN74F623	0°C to 70°C
Storage temperature range		65°C to 150°C

<sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input-voltage ratings may be exceeded provided the input-current ratings are observed.

#### recommended operating conditions

				SN54F623			SN74F623		
				NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage		4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage		2			2			V
V <sub>IL</sub>	Low-level input voltage				0.8			0.8	V
lιΚ	Input clamp current				- 18			- 18	mA
1	High lavel output ourrent	A1-A8			- 3			-3	A
IOH High-le	High-level output current	B1-B8			- 12			- 15	mA
IOL Low-level outpu	Love lovel output ourrent	A1-A8			20			24	A
	B1-B8				48			64	mA
TA	Operating free-air temperature		- 55		125	0		70	°C



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# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		SN54F623			SN74F623			UNIT
'	ARAMETER	TEST CONDITIONS		MIN	TYP <sup>†</sup>	MAX	MIN	TYP <sup>†</sup>	MAX	UNII
VIK		V <sub>CC</sub> = 4.5 V,	I <sub>I</sub> = – 18 mA			- 1.2			- 1.2	V
	A1-A8	V <sub>CC</sub> = 4.5 V	I <sub>OH</sub> = - 1 mA	2.5	3.4		2.5	3.4		V
			$I_{OH} = -3 \text{ mA}$	2.4	3.3		2.4	3.3		
,			$I_{OH} = -3 \text{ mA}$	2.4	3.3		2.4	3.3		
VOH	B1-B8		$I_{OH} = -12 \text{ mA}$	2	3.2					
			$I_{OH} = -15 \text{ mA}$				2	3.1		
	Any output	$V_{CC} = 4.75 \text{ V},$	$I_{OH} = -1 \text{ mA to } -3 \text{ mA}$				2.7			
	A1-A8		$I_{OL} = 20 \text{ mA}$		0.3	0.5				V
\ <sub>\/</sub>		V <sub>CC</sub> = 4.5 V	I <sub>OL</sub> = 24 mA					0.35	0.5	
VOL	B1-B8		$I_{OL} = 48 \text{ mA}$		0.38	0.55				
			$I_{OL} = 64 \text{ mA}$					0.42	0.55	
Ī	A and B ports	V <sub>CC</sub> = 5.5 V	V <sub>I</sub> = 5.5 V			1			1	mA
11	OEAB or OEBA		V <sub>I</sub> = 7 V			0.1			0.1	IIIA
. +	A and B ports	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> =27.7' v			70			70	μΑ
I <sub>IH</sub> ‡	OEAB or OEBA					20			20	
. +	A and B ports	V <sub>CC</sub> = 5.5 V,	۷ <sub>I</sub> =%:5′ ۷			- 0.65			- 0.65	mA
I <sub>IL</sub> ‡	OEAB or OEBA	VCC = 3.5 V,	5 v, v = 0.5 v			- 0.6			- 0.6	ША
. 8	A1-A8	V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 0	- 60		<b>– 150</b>	- 60		<b>– 150</b>	mA
l <sub>OS</sub> §	B1-B8			- 100		- 225	- 100		- 225	IIIA
ICCH		$V_{CC} = 5.5 V,$	Any output = 4.5 V		110	140		110	140	mA
ICCL		V <sub>CC</sub> = 5.5 V	OEAB or <del>OEBA</del> = 4.5 V, A1-A8 = GND		110	140		110	140	mA
ICCZ		V <sub>CC</sub> = 5.5 V	OEBA or A1 – A8 = 4.5 V, OEAB = GND		99	130		99	130	mA

<sup>†</sup> All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C. ‡ For I/O ports, the parameters I<sub>IH</sub> and I<sub>IL</sub> include the off-state output current. § Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

## SN54F623, SN74F623 OCTAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

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## switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	то (оитрит)	$V_{CC} = 5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 500 \Omega,$ $T_A = 25^{\circ}\text{C}$			$V_{CC}$ = 4.5 V to 5.5 V, $C_L$ = 50 pF, $R_L$ = 500 $\Omega$ , $T_A$ = MIN to MAX $\dagger$				UNIT	
	, ,		′F623			SN54	F623	SN74F623			
			MIN	TYP	MAX	MIN	MAX	MIN	MAX		
<sup>t</sup> PLH	A	Δ.	В	1.2	3.6	5.5	1.1	6.8	1.2	6.5	ns
<sup>t</sup> PHL		В	2.2	4.6	7	1.6	8	1.7	7.5	115	
t <sub>PLH</sub>	В	Α	1.2	3.6	5.5	1.1	6.8	1.2	6.5	ns	
<sup>t</sup> PHL		A	1.7	4.1	6.5	1.6	8	1.7	7.5	115	
<sup>t</sup> PZH	<del></del> OEBA	А	3.1	8.1	10.5	2.7	12.4	3.1	12	ns	
t <sub>PZL</sub>		A	2.8	7.1	9.5	2.5	10.3	2.8	10	115	
<sup>t</sup> PHZ	<del>OEBA</del>	А	1.7	4.1	6.5	1.6	8.3	1.7	7.5	ns	
<sup>t</sup> PLZ	OEBA	A	1.7	4.1	6.5	1.5	7.4	1.7	7	110	
<sup>t</sup> PZH	OEAB	OFAR	В	2.8	7.6	10	2.7	12	2.8	11.5	ns
tPZL		ט	2.8	6.6	9	2.8	10	2.9	9.5	115	
<sup>t</sup> PHZ	OEAB	AB B	2.2	5.6	8.5	1.9	10	2.2	10	ns	
t <sub>PLZ</sub>		ט	3.2	6.6	9	3.1	10.7	3.2	10	115	

<sup>&</sup>lt;sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and waveforms are shown in Section 1.



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