

# SN74F260 DUAL 5-INPUT POSITIVE-NOR GATE

SDFS012A – D3214, JANUARY 1989 – REVISED OCTOBER 1993

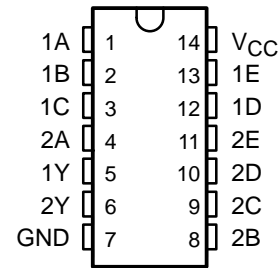
- Package Options Include Plastic Small-Outline Packages and Standard Plastic 300-mil DIPs

## description

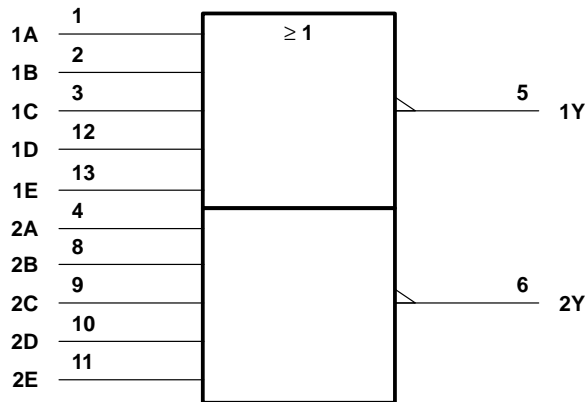
The SN74F260 contains two independent 5-input positive-NOR gates. It performs the Boolean functions  $Y = A + B + C + D + E$  in positive logic.

The SN74F260 is characterized for operation from 0°C to 70°C.

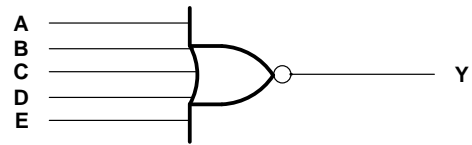
## D OR N PACKAGE (TOP VIEW)



## logic symbol†



## logic diagram, each gate (positive logic)



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, $V_{CC}$	–0.5 V to 7 V
Input voltage range, $V_I$ (see Note 1)	–1.2 V to 7 V
Input current range	–30 mA to 5 mA
Voltage range applied to any output in the high state	–0.5 V to $V_{CC}$
Current into any output in the low state	40 mA
Operating free-air temperature range	0°C to 70°C
Storage temperature range	–65°C to 150°C

‡ Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage ratings may be exceeded provided the input current ratings are observed.

# SN74F260

## DUAL 5-INPUT POSITIVE-NOR GATE

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### recommended operating conditions

	MIN	NOM	MAX	UNIT
$V_{CC}$ Supply voltage	4.5	5	5.5	V
$V_{IH}$ High-level input voltage	2			V
$V_{IL}$ Low-level input voltage			0.8	V
$I_{IK}$ Input clamp current			–18	mA
$I_{OH}$ High-level output current			–1	mA
$I_{OL}$ Low-level output current			20	mA
$T_A$ Operating free-air temperature	0		70	°C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
$V_{IK}$	$V_{CC} = 4.5$ V, $I_I = -18$ mA			–1.2	V
$V_{OH}$	$V_{CC} = 4.5$ V, $I_{OH} = -1$ mA	2.5	3.4		V
	$V_{CC} = 4.75$ V, $I_{OH} = -1$ mA	2.7			
$V_{OL}$	$V_{CC} = 4.5$ V, $I_{OL} = 20$ mA		0.3	0.5	V
$I_I$	$V_{CC} = 5.5$ V, $V_I = 7$ V			0.1	mA
$I_{IH}$	$V_{CC} = 5.5$ V, $V_I = 2.7$ V			20	µA
$I_{IL}$	$V_{CC} = 5.5$ V, $V_I = 0.5$ V			–0.6	mA
$I_{OS}‡$	$V_{CC} = 5.5$ V, $V_O = 0$	–60		–150	mA
$I_{CCH}$	$V_{CC} = 5.5$ V, $V_I = 0$		4.6	6.5	mA
$I_{CCL}$	$V_{CC} = 5.5$ V, $V_I = 4.5$ V		7.3	9.5	mA

† All typical values are at  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$ .

‡ Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

### switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5$ V, $C_L = 50$ pF, $R_L = 500\ \Omega$ , $T_A = 25^\circ\text{C}$			$V_{CC} = 4.5$ V to $5.5$ V, $C_L = 50$ pF, $R_L = 500\ \Omega$ , $T_A = \text{MIN to MAX}^\S$		UNIT
			MIN	TYP	MAX	MIN	MAX	
$t_{PLH}$	A, B, C, D, E	Y	1.7	4	5.5	1.2	6.5	ns
$t_{PHL}$			1	2.5	4	1	4.5	

§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: Load circuits and waveforms are shown in Section 1.



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