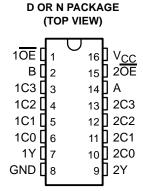
#### SN74AS353B DUAL 1-OF-4 DATA SELECTOR/MULTIPLEXER WITH 3-STATE OUTPUTS

SDAS222A - APRIL 1982 - REVISED DECEMBER 1994

- Inverting Version of 'AS253
- Permits Multiplexing From n Lines to One Line
- Performs Parallel-to-Serial Conversion
- Package Options Include Plastic Small-Outline (D) Packages and Standard Plastic (N) 300-mil DIPs

#### description

This data selector/multiplexer contains inverters and drivers to supply full binary decoding data selection to the AND-OR-invert gates. Separate output-enable (OE) inputs are provided for each of the two 4-line sections.



The 3-state outputs can interface with and drive data lines of bus-organized systems. With all but one of the common outputs disabled (at the high-impedance state), the low impedance of the single enabled output drives the bus line to a high or low logic level. Each section has its own output enable. The output is disabled when  $\overline{\text{OE}}$  is high.

The SN74ALS353B is characterized for operation from 0°C to 70°C.

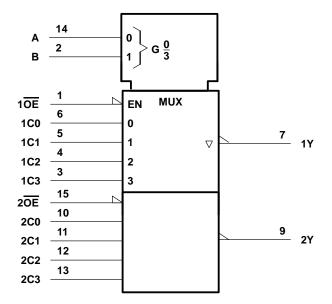
#### **FUNCTION TABLE**

INPUTS							
SELECT		DATA			ŌĒ	OUTPUT	
В	Α	C0	C1	C2	C3	OE.	•
Х	Х	Χ	Χ	Х	Χ	Н	Z
L	L	L	Χ	X	X	L	Н
L	L	Н	Χ	X	Χ	L	L
L	Н	Χ	L	X	Χ	L	Н
L	Н	Χ	Н	X	Χ	L	L
Н	L	Χ	Χ	L	Χ	L	Н
Н	L	Χ	Χ	Н	Χ	L	L
Н	Н	Χ	Χ	Χ	L	L	Н
Н	Н	Χ	Χ	Χ	Н	L	L

Select inputs A and B are common to both sections.

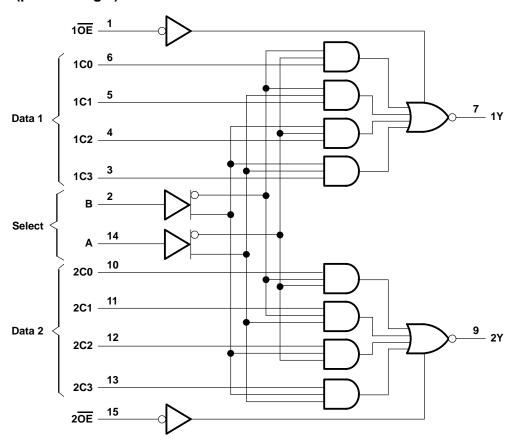
#### SDAS222A - APRIL 1982 - REVISED DECEMBER 1994

## logic symbol†



<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

#### logic diagram (positive logic)





### SN74AS353B DUAL 1-OF-4 DATA SELECTOR/MULTIPLEXER WITH 3-STATE OUTPUTS

SDAS222A - APRIL 1982 - REVISED DECEMBER 1994

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V <sub>CC</sub>	7 V
Input voltage, V <sub>I</sub>	
Voltage applied to a disabled 3-state output	5.5 V
Operating free-air temperature range, T <sub>A</sub>	0°C to 70°C
Storage temperature range	-65°C to 150°C

#### recommended operating conditions

		MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	V
VIH	High-level input voltage	2			V
V <sub>IL</sub>	Low-level input voltage			0.8	V
IOH	High-level output current			-15	mA
l <sub>OL</sub>	Low-level output current			48	mA
TA	Operating free-air temperature	0		70	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST COND	DITIONS	MIN	TYP‡	MAX	UNIT
VIK		V <sub>CC</sub> = 4.5 V,	I <sub>I</sub> = -18 mA			-1.2	V
VOH		V <sub>CC</sub> = 4.5 V to 5.5 V,	I <sub>OH</sub> = -2 mA	V <sub>CC</sub> -2			V
		V <sub>CC</sub> = 4.5 V,	$I_{OH} = -15 \text{ mA}$	2.4	3.3		V
VOL		V <sub>CC</sub> = 4.5 V,	I <sub>OL</sub> = 48 mA		0.35	0.5	V
lozh		V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 2.7 V			50	μΑ
lozL		V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 0.4 V			-50	μΑ
	A, B	V 55V	V <sub>1</sub> = 7 V				A
1 <sub>1</sub>	All others	$V_{CC} = 5.5 V,$				0.1	mA
1	A, B	V FFV	\/ 07\/			40	^
ΉΗ	All others	$V_{CC} = 5.5 \text{ V},$	V <sub>I</sub> =27.7' v			20	μΑ
1	A, B	V 55V	V. 04V			-1	A
ΊL	All others	$V_{CC} = 5.5 V,$	٧ <sub> </sub> =٠٠.٤ ٧			-0.5	mA
ΙΟ§		V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 2.25 V	-30		-112	mA
			Outputs high		15	24	
Icc		$V_{CC} = 5.5 V$	Outputs low		19	31	mA
			Outputs disabled		18	30	

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .



<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

<sup>§</sup> The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

# **SN74AS353B DUAL 1-OF-4 DATA SELECTOR/MULTIPLEXER** WITH 3-STATE OUTPUTS SDAS222A – APRIL 1982 – REVISED DECEMBER 1994

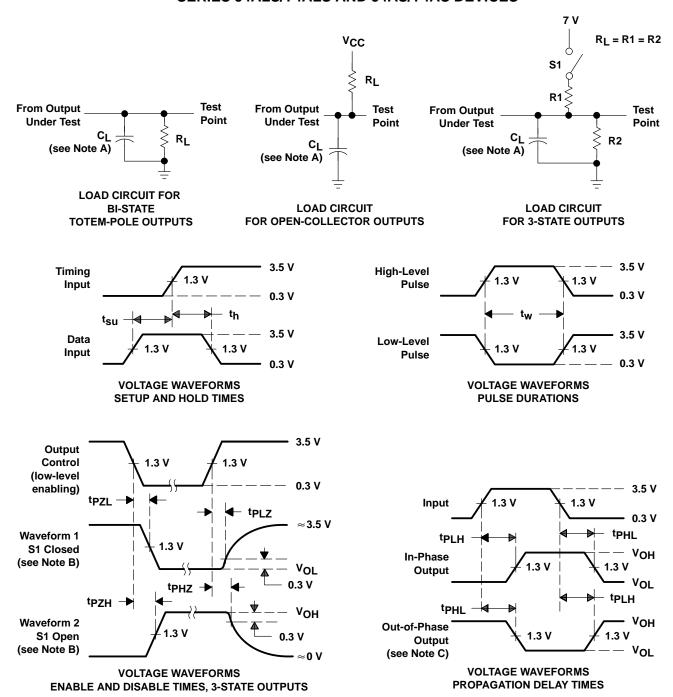
## switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$\label{eq:VCC} \begin{split} &V_{CC} = 4.5 \text{ V to } 5.5 \text{ V,} \\ &C_L = 50 \text{ pF,} \\ &R1 = 500 \Omega, \\ &R2 = 500 \Omega, \\ &T_A = \text{MIN to MAX}^{\dagger} \end{split}$		UNIT	
			MIN	MAX		
t <sub>PLH</sub>	A or B	Y	3	10	ns	
<sup>t</sup> PHL		1	2.5	9	115	
<sup>t</sup> PLH	Data (any C)	Υ	2	7		
<sup>t</sup> PHL		ī	1	6	ns	
<sup>t</sup> PZH	ŌĒ	٧	1	5	ns ns	
<sup>t</sup> PZL		Ť	2	9		
<sup>t</sup> PHZ	ŌĒ	٧	1	6		
t <sub>PLZ</sub>		1	1	5	ns	

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



# PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C<sub>L</sub> includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics: PRR  $\leq$  1 MHz,  $t_f = t_f = 2$  ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms



#### **IMPORTANT NOTICE**

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

Copyright © 1996, Texas Instruments Incorporated