SN54ALS22B, SN74ALS22B **DUAL 4-INPUT POSITIVE-NAND GATES** WITH OPEN-COLLECTOR OUTPUTS

SDAS005A - MARCH 1984 - REVISED MAY 1986

- Package Options Include Plastic Small **Outline Packages, Ceramic Chip Carriers,** and Standard Plastic and Ceramic 300-mil DIPs
- **Dependable Texas Instruments Quality and** • Reliability

description

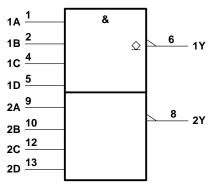
These devices contain two independent 4-input NAND gates. They perform the Boolean functions $Y = \overline{A \bullet B \bullet C \bullet D}$ or $Y = \overline{A} + \overline{B} + \overline{C} + \overline{D}$ in positive logic. The open-collector outputs require pullup resistors to perform correctly. They may be connected to other open-collector outputs to implement active-low wired-OR or active-high wired-AND functions. Open-collector devices are often used to generate higher VOH levels.

The SN54ALS22B is characterized for operation over the full military temperature range of 55°C to 125°C. The SN74ALS22B is characterized for operation from 0°C to 70°C.

FUNCTION TABLE				
FUNCTION TABLE (each gate)				

(outer gate)									
	INF	OUTPUT							
Α	В	С	D	Y					
н	Н	Н	Н	L					
L	Х	Х	Х	Н					
Х	L	Х	Х	Н					
X	Х	L	Х	Н					
х	Х	Х	L	Н					

logic symbol[†]



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

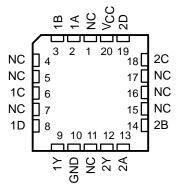
Pin numbers shown are for D, J, and N packages.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



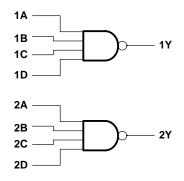
14 🛛 V_{CC} 1A 13 2D 1B 2 NC 🛛 3 12 🛛 2C 1C 11 NC 4 1DΓ 10 2B 5 9 🛛 2A 1Y 6 GND 1 2Y 8 7

SN54ALS22B ... FK PACKAGE (TOP VIEW)



NC-No internal connection

logic diagram (positive logic)



SN54ALS22B, SN74ALS22B DUAL 4-INPUT POSITIVE-NAND GATES WITH OPEN-COLLECTOR OUTPUTSWITH OPEN-COLLECTOR OUTPUTS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC}		
Off-state output voltage		
Operating free-air temperature range:		
	SN74ALS22B	 0°C to 70°C
Storage temperature range		 . −65°C to 150°C

recommended operating conditions

		SN54ALS22B		SN74ALS22B			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
ЮН	High-level output current			5.5			5.5	mA
IOL	Low-level output current			4			8	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN	SN54ALS22B			SN74ALS22B		
			MIN	TYP†	MAX	MIN	TYP†	MAX	UNIT
VIK	V _{CC} = 4.5 V,	lj = -18 mA			-1.5			-1.5	V
Ve	V _{CC} = 4.5 V,	I _{OL} = 4 mA		0.25	0.4		0.25	0.4	V
VOL	V _{CC} = 4.5 V,	I _{OL} = 8 mA					0.35	0.5	v
Iон	V _{CC} = 4.5 V,	V _{OH} = 5.5 V			0.1			0.1	mA
Ц	V _{CC =} 5.5 V,	$V_{I} = 7 V$			0.1			0.1	mA
liΗ	V _{CC} = 5.5 V,	VI = 2.7 V			20			20	μΑ
۱ _{IL}	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1			-0.1	mA
ICCH	V _{CC} = 5.5 V,	$V_{I} = 0 V$		0.22	0.4		0.22	0.4	mA
ICCL	V _{CC} = 5.5 V,	VI = 4.5 V		0.8	1.5		0.8	1.5	mA

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 V,$ $C_L = 50 pF,$ $R_L = 2 k\Omega,$ $T_A = 25°C$ 'ALS22B TYP	CL RL TA	$V_{CC} = 4.5 V \text{ to } 5.5 V,$ $C_{L} = 50 \text{ pF},$ $R_{L} = 2 \text{ k} \Omega,$ $T_{A} = \text{MIN to MAX}$ SN54ALS22B SN74ALS22B $MIN MAX MIN MAX$ 23 65 23 45		UNIT	
^t PLH	Any	Y	35	23	65	23	45	ns
^t PHL	Any	Y	8	4	32	4	18	ns

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.



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