## SN54AHCT16540, SN74AHCT16540 16-BIT BUFFERS/DRIVERS WITH 3-STATE OUTPUTS

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- Inputs Are TTL-Voltage Compatible
- Members of the Texas Instruments Widebus™ Family
- EPIC™ (Enhanced-Performance Implanted CMOS) Process
- Distributed V<sub>CC</sub> and GND Pin Configuration Minimizes High-Speed Switching Noise
- Flow-Through Architecture Optimizes PCB Layout
- Package Options Include Plastic 300-mil Shrink Small-Outline (DL) and Thin Shrink Small-Outline (DGG) Packages and 380-mil Fine-Pitch Ceramic Flat (WD) Package Using 25-mil Center-to-Center Spacings

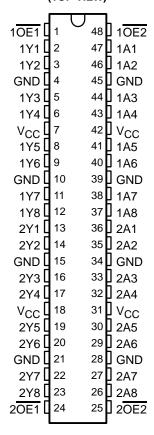
#### description

These 16-bit buffers and bus drivers provide a high-performance bus interface for wide data paths.

The 3-state control gate is a 2-input AND gate with active-low inputs so that if either output-enable (OE1 or OE2) input is high, all corresponding outputs are in the high-impedance state.

The SN54AHCT16540 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74AHCT16540 is characterized for operation from -40°C to 85°C.

#### SN54AHCT16540 ... WD PACKAGE SN74AHCT16540 ... DGG OR DL PACKAGE (TOP VIEW)



## FUNCTION TABLE (each 8-bit section)

	OUTPUT		
OE1	OE2	Α	Υ
L	L	L	Н
L	L	Н	L
Н	X	Χ	Z
Х	Н	Χ	Z



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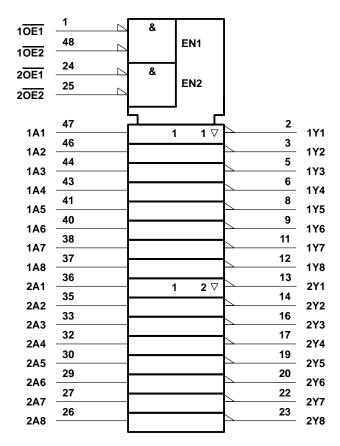
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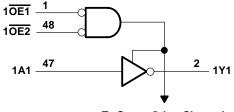
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### logic symbol<sup>†</sup>

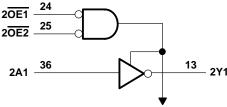


<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

## logic diagram (positive logic)



To Seven Other Channels



To Seven Other Channels

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V <sub>CC</sub>	0.5 V to 7 V
Input voltage range, V <sub>I</sub> (see Note 1)	0.5 V to 7 V
Output voltage range, VO (see Note 1)	0.5 V to $V_{CC}$ + 0.5 V
Input clamp current, $I_{ K }(V_1 < 0)$	
Output clamp current, I <sub>OK</sub> (V <sub>O</sub> < 0 or V <sub>O</sub> > V <sub>CC</sub> )	±20 mA
Continuous output current, $I_O(V_O = 0 \text{ to } V_{CC})$	±25 mA
Continuous current through each V <sub>CC</sub> or GND	±75 mA
Package thermal impedance, θ <sub>JA</sub> (see Note 2): DGG package	89°C/W
DL package	94°C/W
Storage temperature range, T <sub>stg</sub>	–65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

#### recommended operating conditions (see Note 3)

		SN54AHC	T16540	SN74AHC	T16540	UNIT
		MIN	MAX	MIN	MAX	UNIT
VCC	Supply voltage	4.5	5.5	4.5	5.5	V
VIH	High-level input voltage	2		2		V
VIL	Low-level input voltage		0.8		0.8	V
٧ <sub>I</sub>	Input voltage	0	5.5	0	5.5	V
٧o	Output voltage	0	Vcc	0	Vcc	V
ЮН	High-level output current		-8		-8	mA
loL	Low-level output current		8		8	mA
Δt/Δν	Input transition rise or fall rate		20		20	ns/V
TA	Operating free-air temperature	-55	125	-40	85	°C

NOTE 3: Unused inputs must be held high or low to prevent them from floating.

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	T <sub>A</sub> = 25°C		SN54AHC	T16540	SN74AHC	UNIT		
PARAMETER	TEST CONDITIONS	VCC	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
\/a++	I <sub>OH</sub> = -50 μA	4.5 V	4.4	4.5		4.4		4.4		V
VOH	I <sub>OH</sub> = -8 mA	4.5 V	3.94			3.8		3.8		V
V <sub>OL</sub>	I <sub>OL</sub> = 50 μA	4.5 V			0.1		0.1		0.1	V
	I <sub>OL</sub> = 8 mA	4.5 V			0.36		0.44		0.44	V
loz	$V_O = V_{CC}$ or GND	5.5 V			±0.25		±2.5		±2.5	μΑ
lį	$V_I = V_{CC}$ or GND	5.5 V			±0.1		±1		±1	μΑ
lcc	$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			4		40		40	μΑ
Δl <sub>CC</sub> ‡	One input at 3.4 V, Other inputs at V <sub>CC</sub> or GND	5.5 V			1.35		1.5		1.5	mA
Ci	$V_I = V_{CC}$ or GND	5 V		2	10				10	pF
Co	V <sub>O</sub> = V <sub>CC</sub> or GND	5 V		4						pF

<sup>&</sup>lt;sup>‡</sup>This is the increase in supply current for each input at one of the specified TTL voltage levels rather than 0 V or V<sub>CC</sub>.



NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

<sup>2.</sup> The package thermal impedance is calculated in accordance with JESD 51.

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# switching characteristics over recommended operating free-air temperature range, $V_{CC}$ = 5 V $\pm$ 0.5 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	то	LOAD	T,	λ = 25°C	;	SN54AHC	T16540	SN74AHC	T16540	UNIT		
PARAMETER	(INPUT)	(OUTPUT) CAPACITA	CAPACITANCE	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT		
tPLH*	Α	Υ	C <sub>L</sub> = 15 pF		4	6	1	7.5	1	7.5	ns		
tPHL*	] ^	ı	GL = 13 pr		4	6	1	7.5	1	7.5	115		
<sup>t</sup> PZH*	ŌĒ	Y	C <sub>I</sub> = 15 pF		5.5	8	1	9	1	9	ns		
tPZL*		ı	GL = 13 pr		5.5	8	1	9	1	9	115		
<sup>t</sup> PHZ*	ŌĒ	<del>OE</del> Y	V	C <sub>I</sub> = 15 pF		5	8	1	9	1	9	ns	
<sup>t</sup> PLZ*			ı	ı	oL = 13 pr	1 OL = 13 pi	ОС = 13 рг		5	8	1	9	1
tPLH	Α	Υ	C: - 50 pE		6	8.5	1	10	1	10	20		
t <sub>PHL</sub>		Ĭ	C <sub>L</sub> = 50 pF		6	8.5	1	10	1	10	ns		
<sup>t</sup> PZH	ŌE	Y	C: - 50 pE		7.5	11	1	12	1	12	ns		
tPZL	) OE	OE T	OE .	<sup>'</sup> '	Y $C_L = 50 \text{ pF}$		7.5	11	1	12	1	12	115
t <sub>PHZ</sub>	ŌĒ	<u> </u>	Υ	C <sub>I</sub> = 50 pF		8	11	1	12	1	12	ns	
tPLZ	) <u> </u>	ľ	GL = 50 pr		8	11	1	12	1	12	115		

<sup>\*</sup> On products compliant to MIL-PRF-38535, this parameter is ensured but not production tested.

## output-skew characteristics, $C_L = 50 pF$ (see Note 4)

PARAMETER				SN74AH			
	FROM (INPUT)	TO (OUTPUT)	Vcc	T <sub>A</sub> = 25°C	MIN N	/AX	UNIT
				MIN MAX	IVIIIV IV	IIAA	
t <sub>sk(o)</sub>	Α	Υ	$5~V\pm0.5~V$	1		1	ns

NOTE 4: Characteristics are determined during product characterization and ensured by design.

## noise characteristics, $V_{CC} = 5 \text{ V}$ , $C_L = 50 \text{ pF}$ , $T_A = 25^{\circ}\text{C}$ (see Note 5)

	PARAMETER		SN74AHCT16540			
			TYP	MAX	UNIT	
VOL(P)	Quiet output, maximum dynamic V <sub>OL</sub>			0.8	V	
V <sub>OL(V)</sub>	Quiet output, minimum dynamic V <sub>OL</sub>			-0.8	V	
V <sub>OH(V)</sub>	Quiet output, minimum dynamic V <sub>OH</sub>	4.5			V	
V <sub>IH(D)</sub>	High-level dynamic input voltage	2			V	
V <sub>IL(D)</sub>	Low-level dynamic input voltage			0.8	V	

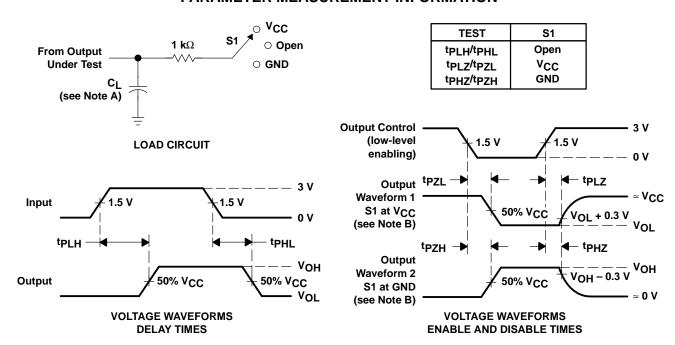
NOTE 5: Characteristics are determined during product characterization and ensured by design for surface-mount packages only.

## operating characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$

	PARAMETER		ONDITIONS	TYP	UNIT
C <sub>pd</sub>	Power dissipation capacitance	No load,	f = 1 MHz	12	pF



#### PARAMETER MEASUREMENT INFORMATION



- NOTES: A. C<sub>L</sub> includes probe and jig capacitance.
  - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
  - C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  1 MHz,  $Z_O = 50~\Omega$ ,  $t_f = 3~ns$ ,  $t_f = 3~ns$ .
  - D. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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