SN74BCT756 OCTAL BUFFER/DRIVER WITH OPEN-COLLECTOR OUTPUTS

SCBS056A - OCTOBER 1990 - REVISED DECEMBER 1993

 BiCMOS Design Significantly Reduces I_{CCZ} ESD Protection Exceeds 2000 V Per 	DW OR N PACKAGE (TOP VIEW)
MIL-STD-883C, Method 3015; Exceeds 200 V Using Machine Model (C = 200 pF, R = 0)	1 OE [1 20] V _{CC} 1A1 [2 19] 2OE 2Y4 [3 18] 1Y1
 Open-Collector Outputs Drive Bus Lines or Buffer Memory Address Registers 	1A2 4 17 2A4 2Y3 5 16 1Y2
 Comparable Speed and Improved Power Performance Relative to SN74F756 	1A3 [6 15] 2A3 2Y2 [7 14] 1Y3
 Package Options Include Small-Outline Packages (DW) and Standard Plastic 	1A4
300-mil DIPs (N)	GND [] 10 11 [] 2A1

description

This octal buffer and line driver is designed specifically to improve both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. Taken together with the SN74BCT757 and SN74BCT760, these devices provide the choice of selected combinations or inverting outputs, symmetrical \overline{OE} (active low) output control, and complementary OE and \overline{OE} inputs.

The SN74BCT756 is characterized for operation from 0°C to 70°C.

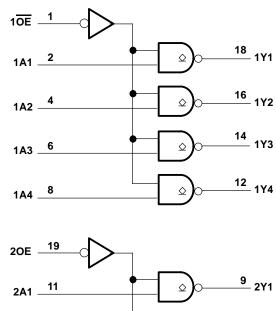
FUNCTION TABLE

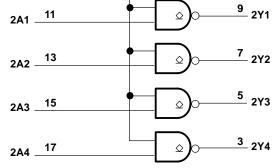
INP	JTS	OUTPUT
ŌĒ	Α	Y
Н	Χ	Н
L	L	Н
L	Н	L

logic symbol†

10E ΕN 18 \Diamond 1A1 1Y1 16 1A2 1Y2 14 1A3 1Y3 8 12 1Y4 1A4 19 20E ΕN 9 11 \triangleright \Diamond 2Y1 2A1 7 13 2Y2 2A2 5 15 2Y3 2A3 17 3 2Y4 2A4

logic diagram (positive logic)





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I	0.5 V to 7 V
Input current range, I ₁	–30 mA to 5 mA
Voltage range applied to any output in the disabled or power-off state, V _O	0.5 V to 5.5 V
Voltage range applied to any output in the high state, V _O	0.5 V to V _{CC}
Current into any output in the low state	128 mÅ
Operating free-air temperature range	0°C to 70°C
Storage temperature range	−65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

SCBS056A - OCTOBER 1990 - REVISED DECEMBER 1993

recommended operating conditions

		MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	V
٧ _{IH}	High-level input voltage	2			V
V _{IL}	Low-level input voltage			0.8	V
Vон	High-level output voltage			5.5	V
l _{IK}	Input clamp current			-18	mA
lOL	Low-level output current			64	mA
TA	Operating free-air temperature	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP†	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	I _I = -18 mA	I _I = -18 mA			-1.2	V
loн	$V_{CC} = 4.5 \text{ V},$	V _{OH} = 5.5 V				0.1	mA
V _{OL}	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 64 \text{ mA}$			0.42	0.55	V
l _l	$V_{CC} = 5.5 V$,	V _I = 7 V				0.1	mA
lіН	$V_{CC} = 5.5 V$,	V _I = 2.7 V				20	μΑ
I _{IL}	V _{CC} = 5.5 V,	V _I = 0.5 V				-1	mA
			Outputs high		21	33	
l _{CC}	$V_{CC} = 5.5 V$,	Outputs open	Outputs low	55		86	mA
			OE disable		6	10	
C _i	$V_{CC} = 5 V$,	V _I = 2.5 V or 0.5 V			6		pF
Co	V _{CC} = 5 V,	V _I = 2.5 V or 0.5 V			10		pF

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature, C_L = 50 pF (unless otherwise noted) (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L R1 R2	C = 5 V, = 50 p, = 500 Ω = 500 Ω = 25°C	<u>.</u> ,	C _L = 50 R1 = 500 R2 = 500	Ω,	UNIT
			MIN	TYP	MAX	MIN	MAX	
^t PLH	А	A V	6.2	8.5	10.5	6.2	11.3	ns
^t PHL		1	0.5	2	4.1	0.5	4.2	115
^t PLH	1OE or 2OE	V	8.2	12.5	14.8	8.2	16.5	200
^t PHL		1	3.4	6.8	9.2	3.4	10.3	ns

[‡] For conditions as MIN or MAX, use the appropriate value specified under recommended operating conditions.



NOTE 1: Load circuits and voltage waveforms are shown in Section 1.

IMPORTANT NOTICE

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

Copyright © 1996, Texas Instruments Incorporated