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- State-of-the-Art BiCMOS Design Significantly Reduces I<sub>CCZ</sub>
- P-N-P Inputs Reduce DC Loading
- ESD Protection Exceeds 2000 V Per MIL-STD-883C, Method 3015
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Package Options Include Plastic Small-Outline (DW) and Shrink Small-Outline (DB) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic and Ceramic 300-mil DIPs (J, N)

#### description

These octal buffers and line drivers are designed specifically to improve both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. Taken together with the 'BCT240 and 'BCT241, these devices provide the choice of selected combinations of inverting and noninverting outputs, symmetrical  $\overline{OE}$  (active-low output-enable) inputs, and complementary OE and  $\overline{OE}$  inputs.

The 'BCT244 is organized as two 4-bit buffers/line drivers with separate output-enable ( $\overline{OE}$ ) inputs. When  $\overline{OE}$  is low, the device passes data from the A inputs to the Y outputs. When  $\overline{OE}$  is high, the outputs are in the high-impedance state.

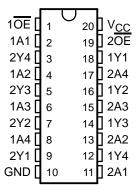
The SN54BCT244 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74BCT244 is characterized for operation from 0°C to 70°C.

## FUNCTION TABLE

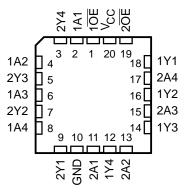
(each buffer)

INP	JTS	OUTPUT
OE	Α	Υ
L	Н	Н
L	L	L
Н	Χ	Z

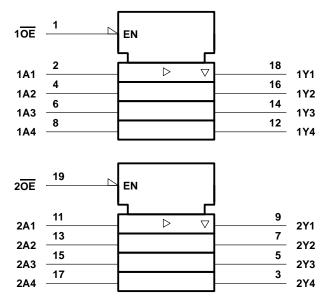
SN54BCT244 . . . J OR W PACKAGE SN74BCT244 . . . DB OR DW OR N PACKAGE (TOP VIEW)



## SN54BCT244 . . . FK PACKAGE (TOP VIEW)

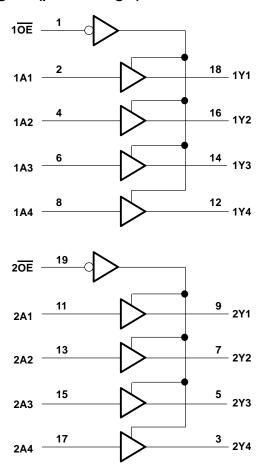


#### logic symbol†



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

#### logic diagram (positive logic)



#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

0 1 1/		0.51/4. 71/
Supply voltage range, V <sub>CC</sub>		– 0.5 V to / V
Input voltage range, V <sub>I</sub> (see Note 1)		– 0.5 V to 7 V
Voltage range applied to any output in t	he disabled or power-off state, VO	– 0.5 V to 5.5 V
Voltage range applied to any output in t	he high state, VO	– 0.5 V to V <sub>CC</sub>
Current into any output in the low state:	SN54BCT244	
• •	SN74BCT244	
Operating free-air temperature range:	SN54BCT244	– 55°C to 125°C
	SN74BCT244	0°C to 70°C
Storage temperature range		– 65°C to 150°C

<sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.



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#### recommended operating conditions

		SN54BCT244			SN74BCT244			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V <sub>IL</sub>	Low-level input voltage			0.8			0.8	V
lıK	Input clamp current			-18			-18	mA
ІОН	igh-level output current -12			-15	mA			
loL	Low-level output current	48				64	mA	
TA	Operating free-air temperature	-55		125	0		70	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN	SN54BCT244			SN74BCT244		
			MIN	TYP†	MAX	MIN	TYP†	MAX	UNIT
VIK	$V_{CC} = 4.5 V$ ,	I <sub>I</sub> = -18 mA			-1.2			-1.2	V
		$I_{OH} = -3 \text{ mA}$	2.4	3.3		2.4	3.3		V
Voн	$V_{CC} = 4.5 V$	$I_{OH} = -12 \text{ mA}$	2	3.2					
		$I_{OH} = -15 \text{ mA}$				2	3.1		
Va	Vaa = 4.5.V	I <sub>OL</sub> = 48 mA		0.38	0.55				
VOL	V <sub>CC</sub> = 4.5 V	$I_{OL} = 64 \text{ mA}$					0.42	0.55	٧
lį	$V_{CC} = 5.5 V$ ,	V <sub>I</sub> = 7 V			0.1			0.1	mA
lН	$V_{CC} = 5.5 \text{ V},$	V <sub>I</sub> = 2.7 V			20			20	μΑ
I <sub>Ι</sub> L	$V_{CC} = 5.5 \text{ V},$	V <sub>I</sub> = 0.5 V			-1			-1	mA
lozh	$V_{CC} = 5.5 V$ ,	$V_0 = 2.7 \text{ V}$			50			50	μΑ
lozL	$V_{CC} = 5.5 V$ ,	V <sub>O</sub> = 0.5 V			-50			-50	μΑ
los <sup>‡</sup>	$V_{CC} = 5.5 V$ ,	V <sub>O</sub> = 0	-100		-225	-100		-225	mA
ICCH	$V_{CC} = 5.5 \text{ V},$	Outputs open		23	40		23	40	mA
ICCL	V <sub>CC</sub> = 5.5 V,	Outputs open		53	80		53	80	mA
Iccz	V <sub>CC</sub> = 5.5 V,	Outputs open		4	10		4	10	mA

<sup>†</sup> All typical values are at V<sub>CC</sub> = 5 V. ‡ Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

### SN54BCT244, SN74BCT244 OCTAL BUFFERS/DRIVERS WITH 3-STATE OUTPUTS

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#### switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$ = 5 V, $C_{L}$ = 50 pF, R1 = 500 Ω, R2 = 500 Ω, $T_{A}$ = 25°C			$V_{CC}$ = 4.5 V to 5.5 V, $C_L$ = 50 pF, $R1$ = 500 $\Omega$ , $R2$ = 500 $\Omega$ , $T_A$ = MIN to MAX $^\dagger$				UNIT
			′BCT244		SN54BCT244		SN74BCT244			
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t <sub>PLH</sub>	А	Y	1.2	2.5	4.4	0.9	5.3	0.9	5	ns
<sup>t</sup> PHL			1.7	3.2	5	1.4	6	1.4	5.5	
<sup>t</sup> PZH	ŌĒ	Y	2	5.7	7.8	2	9	2	8.7	ns
t <sub>PZL</sub>			2	5.9	8.1	2	9.4	2	8.9	115
<sup>t</sup> PHZ	ŌĒ	Y	2	5.4	6.7	2	8	2	7.7	ns
t <sub>PLZ</sub>		1	2	6.1	7.6	2	9.8	2	8.9	110

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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