### SN74ALVC164245 16-BIT 3.3-V TO 5-V LEVEL SHIFTING TRANSCEIVER WITH 3-STATE OUTPUTS

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- Member of the Texas Instruments Widebus™ Family
- EPIC™ (Enhanced-Performance Implanted CMOS) Submicron Process
- Latch-Up Performance Exceeds 250 mA Per JEDEC Standard JESD-17
- Package Options Include Plastic 300-mil Shrink Small-Outline (DL) and Thin Shrink Small-Outline (DGG) Packages

#### description

This 16-bit (dual-octal) noninverting bus transceiver contains two separate supply rails; B port has  $V_{CCB}$ , which is set at 5 V, and A port has  $V_{CCA}$ , which is set to operate at 3.3 V. This allows for translation from a 3.3-V to a 5-V environment and vice-versa.

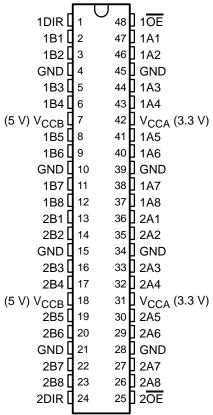
The SN74ALVC164245 is designed for asynchronous communication between data buses.

The SN74ALVC164245 is available in Tl's shrink small-outline (DL) and thin shrink small-outline (DGG) packages, which provide twice the I/O pin count and functionality of standard small-outline packages in the same printed-circuit-board area.

To ensure the high-impedance state during power up or power down,  $\overline{OE}$  should be tied to  $V_{CC}$  through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

The SN74ALVC164245 is characterized for operation from -40°C to 85°C.

#### DGG OR DL PACKAGE (TOP VIEW)



## FUNCTION TABLE (each 8-bit section)

INP	UTS	OPERATION			
OE	DIR	OPERATION			
L	L	B data to A bus			
L	Н	A data to B bus			
Н	X	Isolation			

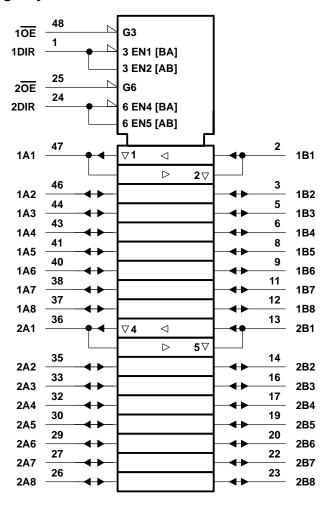


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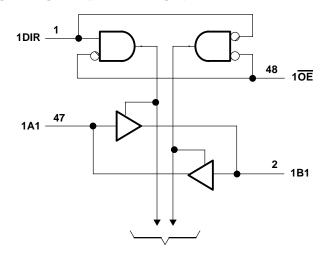


#### logic symbol<sup>†</sup>

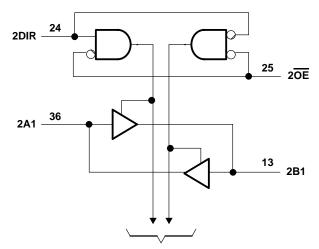


<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

#### logic diagram (positive logic)



To Seven Other Channels



To Seven Other Channels

### SN74ALVC164245 16-BIT 3.3-V TO 5-V LEVEL SHIFTING TRANSCEIVER WITH 3-STATE OUTPUTS

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# absolute maximum ratings over operating free-air temperature range for $V_{CCB}$ at 5 V (unless otherwise noted)<sup>†</sup>

Supply voltage range, V <sub>CCB</sub>	−0.5 V to 6 V
Input voltage range, V <sub>I</sub> (see Note 1)	0.5 V to 6 V
Output voltage range, V <sub>O</sub> (see Note 1)	$\cdot \cdot \cdot -0.5 \text{ V to V}_{CCB} + 0.5 \text{ V}$
Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CCB}$ )	–50 mA
Output clamp current, I <sub>OK</sub> (V <sub>O</sub> < 0 or V <sub>O</sub> > V <sub>CCB</sub> )	±50 mA
Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CCB}$ )	±50 mA
Continuous current through each V <sub>CCB</sub> or GND	±100 mA
Maximum power dissipation at T <sub>A</sub> = 55°C (in still air) (see Note 2): DGG package	0.85 W
DL package .	1.2 W
Storage temperature range, T <sub>stq</sub>	−65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

# absolute maximum ratings over operating free-air temperature range for $\rm V_{CCA}$ at 3.3 V (unless otherwise noted) $\!\!\!\!^{\dagger}$

Supply voltage range, V <sub>CCA</sub>	0.5 V to 4.6 V
Input voltage range, VI: Except I/O ports (see Note 3)	0.5 V to 4.6 V
I/O ports (see Note 3)	0.5 V to V <sub>CCA</sub> + 0.5 V
Output voltage range, V <sub>O</sub> (see Note 3)	$-0.5 \text{ V to V}_{CCA} + 0.5 \text{ V}$
Input clamp current, $I_{ K }(V_{ C } < 0)$	–50 mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CCA}$ )	±50 mA
Continuous output current, $I_O(V_O = 0 \text{ to } V_{CCA})$	±50 mA
Continuous current through each V <sub>CCA</sub> or GND	±100 mA
Maximum power dissipation at $T_A = 55^{\circ}C$ (in still air) (see Note 2): [	DGG package 0.85 W
	DL package 1.2 W
Storage temperature range, T <sub>sto</sub>	–65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.



NOTES: 1. This value is limited to 6 V maximum.

<sup>2.</sup> The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils. For more information, refer to the *Package Thermal Considerations* application note in the 1994 *ABT Advanced BiCMOS Technology Data Book*, literature number SCBD002B.

NOTES: 2. The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils. For more information, refer to the *Package Thermal Considerations* application note in the 1994 *ABT Advanced BiCMOS Technology Data Book*, literature number SCBD002B.

<sup>3.</sup> This value is limited to 4.6 V maximum.

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## recommended operating conditions for $V_{\mbox{CCB}}$ at 5 V (see Note 3)

		MIN	MAX	UNIT
VCCB	Supply voltage	4.5	5.5	V
VIH	High-level input voltage	2		V
$V_{IL}$	Low-level input voltage		0.8	V
VIA	Input voltage	0	VCCB	V
V <sub>OB</sub>	Output voltage	0	VCCB	V
IOH	High-level output current		-24	mA
IOL	Low-level output current		24	mA
$\Delta t/\Delta v$	Input transition rise or fall rate	0	10	ns/V
TA	Operating free-air temperature	-40	85	°C

NOTE 3: Unused inputs must be held high or low to prevent them from floating.

## recommended operating conditions for $V_{\mbox{CCA}}$ at 3.3 V (see Note 3)

			MIN	MAX	UNIT	
VCCA	Supply voltage		2.7	3.6	V	
VIH	High-level input voltage	V <sub>CCA</sub> = 2.7 V to 3.6 V	2		V	
V <sub>IL</sub>	Low-level input voltage	V <sub>CCA</sub> = 2.7 V to 3.6 V		0.8	V	
V <sub>IB</sub>	Input voltage		0	VCCA	V	
VOA	Output voltage		0	VCCA	V	
lo	High-level output current	V <sub>CCA</sub> = 2.7 V		-12	mA	
ЮН		V <sub>CCA</sub> = 3 V		-24	IIIA	
la.	Low lovel output ourrent	V <sub>CCA</sub> = 2.7 V		12	mA	
IOL	Low-level output current  VCCA = 3 V			24	IIIA	
$\Delta t/\Delta v$	Input transition rise or fall rate		0	10	ns/V	
TA	Operating free-air temperature		-40	85	°C	

NOTE 3: Unused inputs must be held high or low to prevent them from floating.



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## electrical characteristics over recommended operating free-air temperature range for $V_{CCB} = 5 \text{ V}$ (unless otherwise noted) (see Note 4)

PAI	RAMETER	TEST CONDITIONS	V <sub>CCB</sub>	MIN	TYP <sup>†</sup>	MAX	UNIT	
		Jan - 100 uA	4.5 V	4.3				
\\\(\Lambda)	to D)	ΙΟΗ = - 100 μΑ	5.5 V	5.3			v	
V <sub>OH</sub> (A	Ю Б)	J	4.5 V	3.7			V	
		10H = -24 IIIA	5.5 V	4.7				
		In. 400 · A	4.5 V			0.2		
\/~: (A +	to D)	ΙΟΣ = 100 μΑ	5.5 V			0.2	٧	
V <sub>OL</sub> (A t	Ю Б)	lo: - 24 mA	4.5 V			0.55	V	
		= -100 μA  = -24 mA  = 100 μA  = 24 mA  EVCCB or GND  EVCCB or GND  Other inputs at V <sub>CCB</sub> or GND  EVCCB or GND  Other inputs at V <sub>CCB</sub> or GND	5.5 V			0.55		
lį	Control inputs	$V_I = V_{CCB}$ or GND	5.5 V			±5	μΑ	
loz <sup>‡</sup>	A or B ports	$V_O = V_{CCB}$ or GND	5.5 V				μΑ	
Icc		$V_I = V_{CCB}$ or GND, $I_O = 0$	5.5 V				μΑ	
∆lcc§		One input at 3.4 V, Other inputs at V <sub>C</sub>	CB or GND 4.5 V to 5.5 V				μΑ	
Ci	Control inputs	V <sub>I</sub> = V <sub>CCB</sub> or GND	5 V		6.5		pF	
Cio	A or B ports	$V_O = V_{CCB}$ or GND	5 V		6.5		pF	

<sup>&</sup>lt;sup>†</sup> Typical values are measured at  $V_{CC} = 3.3 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

# electrical characteristics over recommended operating free-air temperature range for $V_{CCA} = 3.3 \text{ V}$ (unless otherwise noted) (see Note 5)

PA	RAMETER	TEST CONDITIONS	V <sub>CCA</sub> ¶	MIN T	YP† MAX	UNIT	
		$I_{OH} = -100 \mu A$	MIN to MAX	V <sub>CC</sub> -0.2			
V <sub>OH</sub> (B	to A)	I <sub>OH</sub> = -12 mA	2.7 V	2.2		V	
VOH (D	10 A)	10H = - 12 IIIA	3 V	2.4		v	
		$I_{OH} = -24 \text{ mA}$	3 V	2			
		$I_{OL} = 100 \mu\text{A}$	MIN to MAX		0.2	<b>—</b>	
V <sub>OL</sub> (B t	to A)	$I_{OL} = 12 \text{ mA}$	2.7 V		0.4		
		$I_{OL} = 24 \text{ mA}$	3 V		0.55		
lį	Control inputs	$V_I = V_{CCA}$ or GND	3.6 V		±5	μΑ	
l <sub>OZ</sub> ‡		$V_O = V_{CCA}$ or GND	3.6 V		±10	μΑ	
ICC		$V_I = V_{CCA}$ or GND, $I_O = 0$	3.6 V		40	μΑ	
∆ICC§		One input at V <sub>CCA</sub> – 0.6 V, Other inputs at V <sub>CCA</sub> or GND	3 V to 3.6 V		750	μΑ	
Ci	Control inputs	$V_I = V_{CCA}$ or GND	3.3 V		6.5	pF	
C <sub>io</sub>	A or B ports	$V_O = V_{CCA}$ or GND	3.3 V		8.5	pF	

<sup>&</sup>lt;sup>†</sup> Typical values are measured at V<sub>CC</sub> = 3.3 V, T<sub>A</sub> = 25°C.

NOTE 5:  $V_{CCB} = 5 V \pm .05 V$ 



<sup>‡</sup> For I/O ports, the parameter IOZ includes the input leakage current.

<sup>§</sup> This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather then at 0 V or V<sub>CCA</sub>. NOTE 4: V<sub>CCA</sub> = 2.7 V to 3.6 V

<sup>‡</sup> For I/O ports, the parameter I<sub>OZ</sub> includes the input leakage current.

<sup>§</sup> This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather then at 0 V or VCCA.

<sup>¶</sup> For conditions shown as MIN or MAX, use the appropriate values under recommended operating conditions.

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# switching characteristics over recommended operating free-air temperature range, $C_L$ = 50 pF (unless otherwise noted) (see Figures 1 and 2)

			VCCB = 5	$V \pm 0.5$	٧	
PARAMETER	FROM (INPUT)		V <sub>CCA</sub> = 2.7 V	V <sub>CCA</sub> = 3.3 V ± 0.3 V		UNIT
			MIN MAXT	MIN†	MAX†	
+ ,	А	В	5.9	1	5.8	ns
<sup>t</sup> pd	В	А	6.7	1.2	5.8	ns
t <sub>en</sub>	<del> </del> OE	В	9.3	1	8.9	ns
<sup>t</sup> dis	ŌĒ	В	9.2	2.1	9.5	ns
t <sub>en</sub>	ŌĒ	Α	10.2	2	9.1	ns
<sup>t</sup> dis	ŌĒ	А	9	2.9	8.6	ns

<sup>†</sup> This datasheet limit can vary among suppliers.

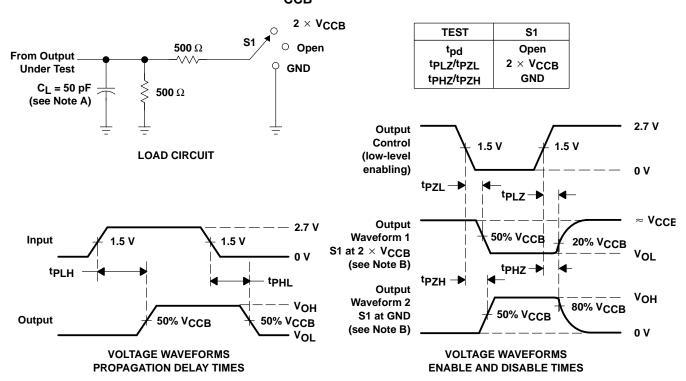
## operating characteristics, $T_A = 25^{\circ}C$

	PARAMETER		TEST CONDITIONS		V <sub>CCA</sub> = 3.3 V V <sub>CCB</sub> = 5 V	UNIT	
C .	Dower discipation canonitance	Outputs enabled (A or B)	C 50 pE	f _ 10 MH-	56	рF	
Cpd	C <sub>nd</sub> Power dissipation capacitance	Outputs disabled (A or B)	$C_L = 50 \text{ pr},$	$C_L = 50 pF$ ,	f = 10 MHz	6	μΓ



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# PARAMETER MEASUREMENT INFORMATION $V_{CCB} = 5 V \pm 0.5 V$



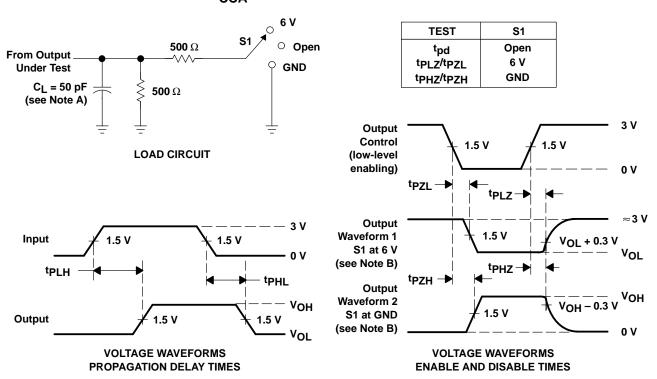
NOTES: A. C<sub>I</sub> includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz,  $Z_O = 50 \Omega$ ,  $t_f \leq 2.5 \text{ ns.}$   $t_f \leq 2.5 \text{ ns.}$
- D. The outputs are measured one at a time with one transition per measurement.
- E. tpLZ and tpHZ are the same as tdis.
- F. tpzL and tpzH are the same as ten.
- G.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

Figure 1. Load Circuit and Voltage Waveforms

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# PARAMETER MEASUREMENT INFORMATION $V_{CCA}$ = 2.7 V AND 3.3 V $\pm$ 0.3 V



- NOTES: A. C<sub>L</sub> includes probe and jig capacitance.
  - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
  - C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz,  $Z_O = 50 \Omega$ ,  $t_f \leq 2.5 \text{ ns.}$   $t_f \leq 2.5 \text{ ns.}$
  - D. The outputs are measured one at a time with one transition per measurement.
  - E. tpLz and tpHz are the same as tdis.
  - F. tpzL and tpzH are the same as ten.
  - G.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

Figure 2. Load Circuit and Voltage Waveforms



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