

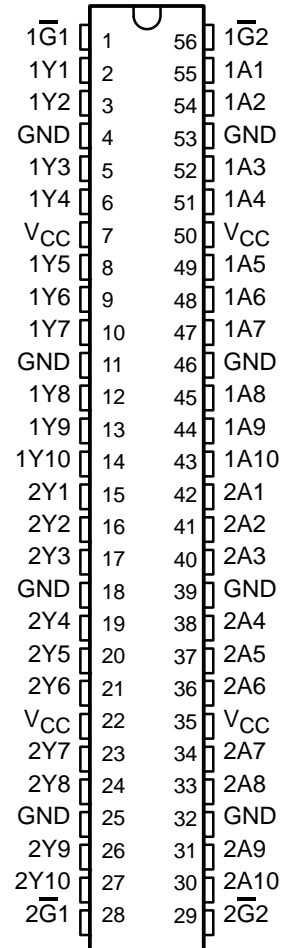
54AC16828, 54ACT16828 74AC16828, 74ACT16828 20-BIT BUFFERS AND LINE DRIVERS WITH 3-STATE OUTPUTS

SCAS404 – JUNE 1990

- **Members of Texas Instruments Widebus™ Family**
- **Packaged in Shrink Small-Outline 300-mil Packages (SSOP) and 380-mil Fine-Pitch Ceramic Flat Packages Using 25-mil Center-to-Center Pin Spacings**
- **Inputs are TTL- or CMOS-Voltage Compatible**
- **3-State Outputs Drive Bus Lines Directly**
- **Flow-Through Architecture Optimizes PCB Layout**
- **Distributed V_{CC} and GND Pin Configuration Minimizes High-Speed Switching Noise**
- **EPIC™ (Enhanced-Performance Implanted CMOS) 1-μm Process**
- **500-mA Typical Latch-Up Immunity at 125°C**

16828, 74ACT16828 . . . DL PACKAGE
16828, 54ACT16828 . . . WD PACKAGE

(TOP VIEW)



description

The 'AC16828 and 'ACT16828 are inverting 20-bit buffers composed of two 10-bit sections with separate output-enable signals. For either 10-bit buffer section, the two output enable inputs (1G1 and 1G2 or 2G1 and 2G2) must both be low for the corresponding Y outputs to be active. If either output-enable input is high, the outputs of that 10-bit buffer section are in the high-impedance state.

The 74AC16828 and 74ACT16828 are packaged in TI's shrink small-outline package (SSOP) with 25-mil center-to-center pin spacings. This package provides twice the I/O pin count and functionality of a standard small-outline package in the same printed-circuit-board area.

The 'AC16828 has CMOS-compatible input thresholds. The 'ACT16828 has TTL-compatible input thresholds.

The SN54AC16828 and SN54ACT16828 are characterized over the full military temperature range of -55°C to 125°C. The SN74AC16828 and SN74ACT16828 are characterized for operation from -40°C to 85°C.

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PRODUCT PREVIEW

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