- **Flow-Through Architecture Optimizes PCB Layout**
- **Center-Pin V_{CC} and GND Configurations Minimize High-Speed Switching Noise**
- *EPIC*™ **(Enhanced-Performance Implanted CMOS) 1-μm Process**
- **500-mA Typical Latch-Up Immunity at 125°C**
- **Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic (N) and Ceramic (J) DIPs**
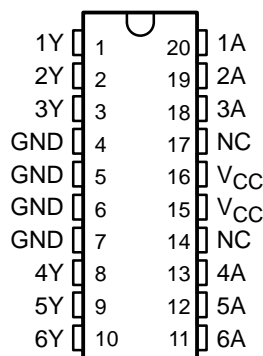
54AC11014 . . . J OR W PACKAGE
74AC11014 . . . DW OR N PACKAGE
(TOP VIEW)

1Y	1		20	1A
2Y	2		19	2A
3Y	3		18	3A
GND	4		17	NC
GND	5		16	V_{CC}
GND	6		15	V_{CC}
GND	7		14	NC
4Y	8		13	4A
5Y	9		12	5A
6Y	10		11	6A

description

The 'AC11014 contains six independent inverters. They perform the Boolean function $Y = \overline{A}$. Because of the Schmitt action, the devices have different input threshold levels for positive-going (V_{T+}) and for negative-going (V_{T-}) signals.
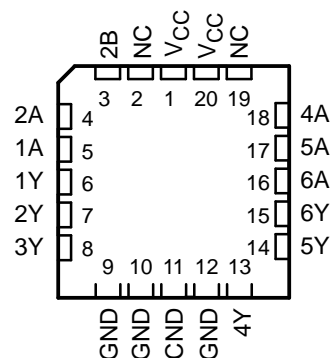
These circuits are temperature compensated. They can be triggered from the slowest of input ramps and still give clean, jitter-free output signals and greater noise margin than conventional inverters.

The 54AC11014 is characterized for operation over the full military temperature range of −55°C to 125°C. The 74AC11014 is characterized for operation from −40°C to 85°C.

54AC11014 . . . FK PACKAGE
(TOP VIEW)

Top pins: 2B, NC, V_{CC}, V_{CC}, NC (pins 3, 2, 1, 20, 19)

2A	4	18 4A
1A	5	17 5A
1Y	6	16 6A
2Y	7	15 6Y
3Y	8	14 5Y

Bottom pins: GND, GND, CND, GND, 4Y (pins 9, 10, 11, 12, 13)

NC – No internal connection

FUNCTION TABLE
(each inverter)

INPUT A	OUTPUT Y
H	L
L	H

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54AC11014, 74AC11014
HEX SCHMITT-TRIGGER INVERTERS

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logic symbol†
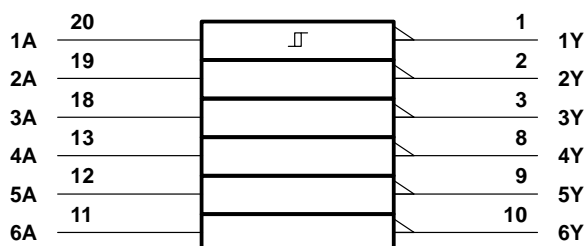
† This symbol is in accordance with ANSI/IEEE Std 91-1984 and
 IEC Publication 617-12.
Pin numbers shown are for the DW, J, N, and W packages.

logic diagram (positive logic)

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

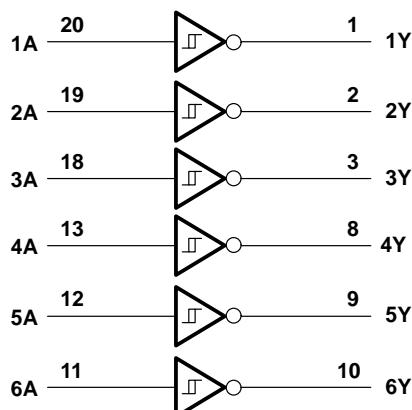Supply voltage range, V_{CC} .... −0.5 V to 7 V
Input voltage range, V_I (see Note 1) .... − 0.5 V to V_{CC} + 0.5 V
Output voltage range, V_O (see Note 1) .... − 0.5 V to V_{CC} + 0.5 V
Input clamp current, I_{IK} (V_I < 0 or V_I > V_{CC}) .... ±20 mA
Output clamp current, I_{OK} (V_O < 0 or V_O > V_{CC}) .... ±50 mA
Continuous output current, I_O (V_O = 0 to V_{CC}) .... ±50 mA
Continuous current through V_{CC} or GND .... ±150 mA
Storage temperature range, T_{stg} .... −65°C to 150°C

‡ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and
 functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not
 implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTE 1:   The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

recommended operating conditions

			54AC11014			74AC11014			UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC}	Supply voltage		3	5	5.5	3	5	5.5	V
V_{IH}	High-level input voltage	V_{CC} = 3 V	2.1			2.1			V
		V_{CC} = 4.5 V	3.15			3.15			
		V_{CC} = 5.5 V	3.85			3.85			
V_{IL}	Low-level input voltage	V_{CC} = 3 V			0.9			0.9	V
		V_{CC} = 4.5 V			1.35			1.35	
		V_{CC} = 5.5 V			1.65			1.65	
V_I	Input voltage		0		V_{CC}	0		V_{CC}	V
V_O	Output voltage		0		V_{CC}	0		V_{CC}	V
I_{OH}	High-level output current	V_{CC} = 3 V			−4			−4	mA
		VCC = 4.5 V			−24			−24	
		V_{CC} = 5.5 V			−24			−24	
I_{OL}	Low-level output current	V_{CC} = 3 V			12			12	mA
		V_{CC} = 3 V			24			24	
		V_{CC} = 5.5 V			24			24	
T_A	Operating free-air temperature		−55		125	−40		85	°C

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	V_{CC}	$T_A = 25°C$ MIN	TYP	MAX	54AC11014 MIN	MAX	74AC11014 MIN	MAX	UNIT
V_{T+} Positive-going threshold		3 V			2.2		2.2		2.2	V
		4.5 V			3.2		3.2		3.2	
		5.5 V			3.9		3.9		3.9	
V_{T-} Negative-going threshold		3 V	0.5			0.5		0.5		V
		4.5 V	0.9			0.9		0.9		
		5.5 V	1.1			1.1		1.1		
ΔV_T Hysteresis $(V_{T+} - V_{T-})$		3 V	0.3		1.2	0.3	1.2	0.3	1.2	V
		4.5 V	0.4		1.4	0.4	1.4	0.4	1.4	
		5.5 V	0.5		1.6	0.5	1.6	0.5	1.6	
V_{OH}	$I_{OH} = -50$ µA	3 V	2.9			2.9		2.9		V
		4.5 V	4.4			4.4		4.4		
		5.5 V	5.4			5.4		5.4		
	$I_{OH} = -4$ mA	3 V	2.58			2.4		2.48		
	$I_{OH} = -24$ mA	4.5 V	3.94			3.7		3.8		
		5.5 V	4.94			4.7		4.8		
	$I_{OH} = -50$ mA†	5.5 V				3.85				
	$I_{OH} = -75$ mA†	5.5 V						3.85		
V_{OL}	$I_{OL} = 50$ µA	3 V			0.1		0.1		0.1	V
		4.5 V			0.1		0.1		0.1	
		5.5 V			0.1		0.1		0.1	
	$I_{OL} = 12$ mA	3 V			0.36		0.5		0.44	
	$I_{OL} = 24$ mA	4.5 V			0.36		0.5		0.44	
		5.5 V			0.36		0.5		0.44	
	$I_{OL} = 50$ mA†	5.5 V					1.65			
	$I_{OL} = 75$ mA†	5.5 V							1.65	
I_I	$V_I = V_{CC}$ or GND	5.5 V			±0.1		±1		±1	µA
I_{CC}	$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			4		80		40	µA
C_i	$V_I = V_{CC}$ or GND	5 V		3.5						pF

† Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

switching characteristics over recommended operating free-air temperature range, V_{CC} = 3.3 V ± 0.3 V (unless otherwise noted) (see Figure 1)

| PARAMETER	FROM (INPUT)	TO (OUTPUT)	$T_A = 25°C$ MIN	TYP	MAX	54AC11014 MIN	MAX	74AC11014 MIN	MAX	UNIT
t_{PLH}	A	Y	1.2	5.4	9.2	1.2	10.3	1.2	9.8	ns
t_{PHL}			1.7	6	8.5	1.7	9.9	1.7	9.3	

54AC11014, 74AC11014
HEX SCHMITT-TRIGGER INVERTERS

switching characteristics over recommended operating free-air temperature range, V_{CC} = 5 V $\pm$ 0.5 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	T_A = 25°C			54AC11014		74AC11014		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t_{PLH}	A	Y	1.1	3.6	6.8	1.1	7.6	1.1	7.1	ns
t_{PHL}			1.5	4.1	6.7	1.5	7.6	1.5	7.4	

operating characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER	TEST CONDITIONS	TYP	UNIT
C_{pd} Power dissipation capacitance	C_L = 50 pF, f = 1 MHz	27	pF

PARAMETER MEASUREMENT INFORMATION

LOAD CIRCUIT

VOLTAGE WAVEFORMS
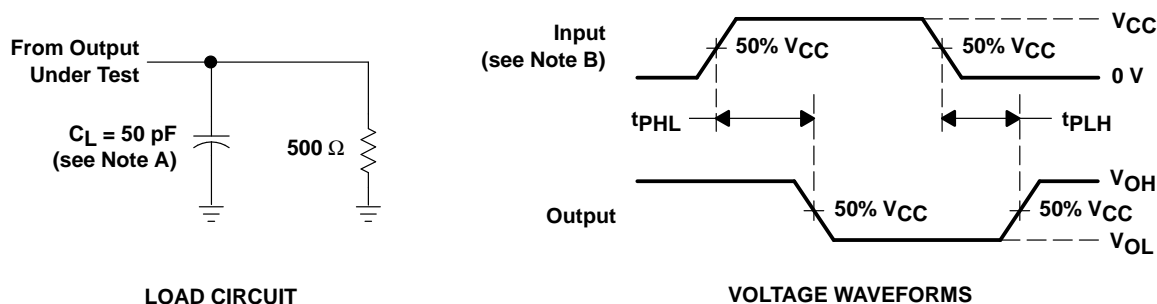
NOTES: A. C_L includes probe and jig capacitance.
B. Input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z_O = 50 Ω, t_r = 3 ns, t_f = 3 ns.
C. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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