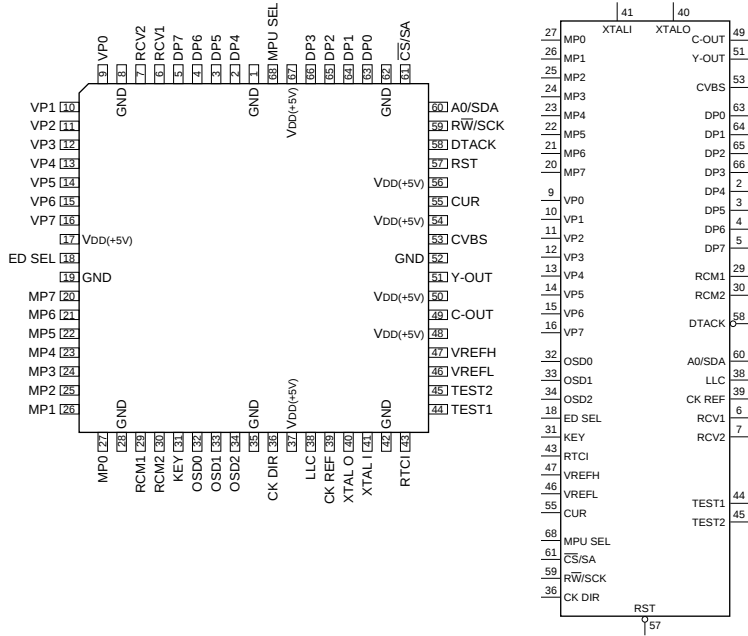

C-MOS DIGITAL VIDEO ENCODER -TOP VIEW-



INPUT

CK DIR ; CLOCK DIRECTION
CS/SA ; CHIP SELECT INPUT IF PIN 68 IS HIGH, OTHERWISE IT IS CONTROLLED ADDRESS SELECT
CUR ; OUTPUT AMPLIFIER CURRENT
ED SEL ; ENCODER DATA SELECT
KEY ; KEY SIGNAL FOR ON-SCREEN DISPLAY DATA
MP0 - MP7 ; MPEG PORT
MPU SEL ; MPU INTERFACE SELECT
OSD0 - OSD2 ; ON-SCREEN DISPLAY DATA
RST ; RESET
RTCI ; REAL TIME CONTROL
RW/SCK ; READ/WRITE INPUT IF PIN 68 IS HIGH, OTHERWISE IT IS SERIAL CLOCK
VP0 - VP7 ; VIDEO PORT
VREFH ; UPPER REFERENCE VOLTAGE
VREFL ; LOWER REFERENCE VOLTAGE
XTALI ; CRYSTAL OSCILLATOR

OUTPUT

C-OUT ; ANALOG CHROMA SIGNAL
CVBS ; ANALOG COMPOSITE VBS SIGNAL
DTACK ; DATA ACKNOWLEDGE OF PARALLEL MPU INTERFACE
RCM1, RCM2 ; RASTER CONTROL FOR MPEG PORT
XTALO ; CRYSTAL OSCILLATOR
Y-OUT ; ANALOG LUMINANCE SIGNAL

I/O

A0/SDA ; ADDRESS INPUT IF PIN 68 IS HIGH, OTHERWISE IT IS SERIAL DATA
CK REF ; CLOCK REFERENCE SIGNAL
DP0 - DP7 ; DATA PORT (PARALLEL MPU INTERFACE)
LLC ; LINE-LOCKED CLOCK
RCV1, RCV2 ; VIDEO PORT RASTER CONTROL

