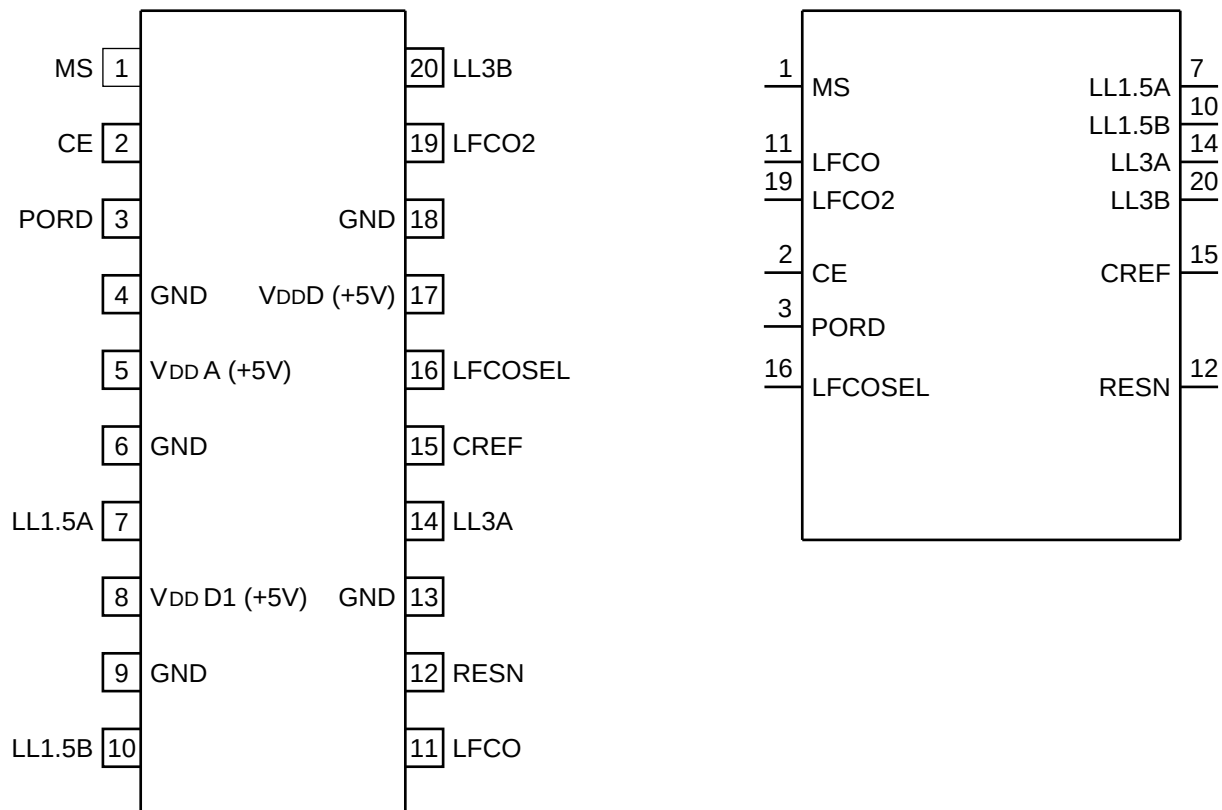

CLOCK SIGNAL GENERATOR CIRCUIT FOR DIGITAL TV SYSTEM

- TOP VIEW -



INPUT

CE ; CHIP ENABLE/RESET
 LFCO ; LINE-LOCKED FREQUENCY CONTROL 1
 LFCOSEL ; LFCO SOURCE SELECT
 LFCO2 ; LINE-LOCKED FREQUENCY CONTROL 2
 MS ; MODE SELECT
 PORD ; POWER-ON RESET DELAY

OUTPUT

CREF ; CLOCK REFERENCE
 LL1.5A ; LINE LOCKED CLOCK 1.5A
 LL1.5B ; LINE LOCKED CLOCK 1.5B
 LL3A ; LINE LOCKED CLOCK 3A
 LL3B ; LINE LOCKED CLOCK 3B
 RESN ; RESET

