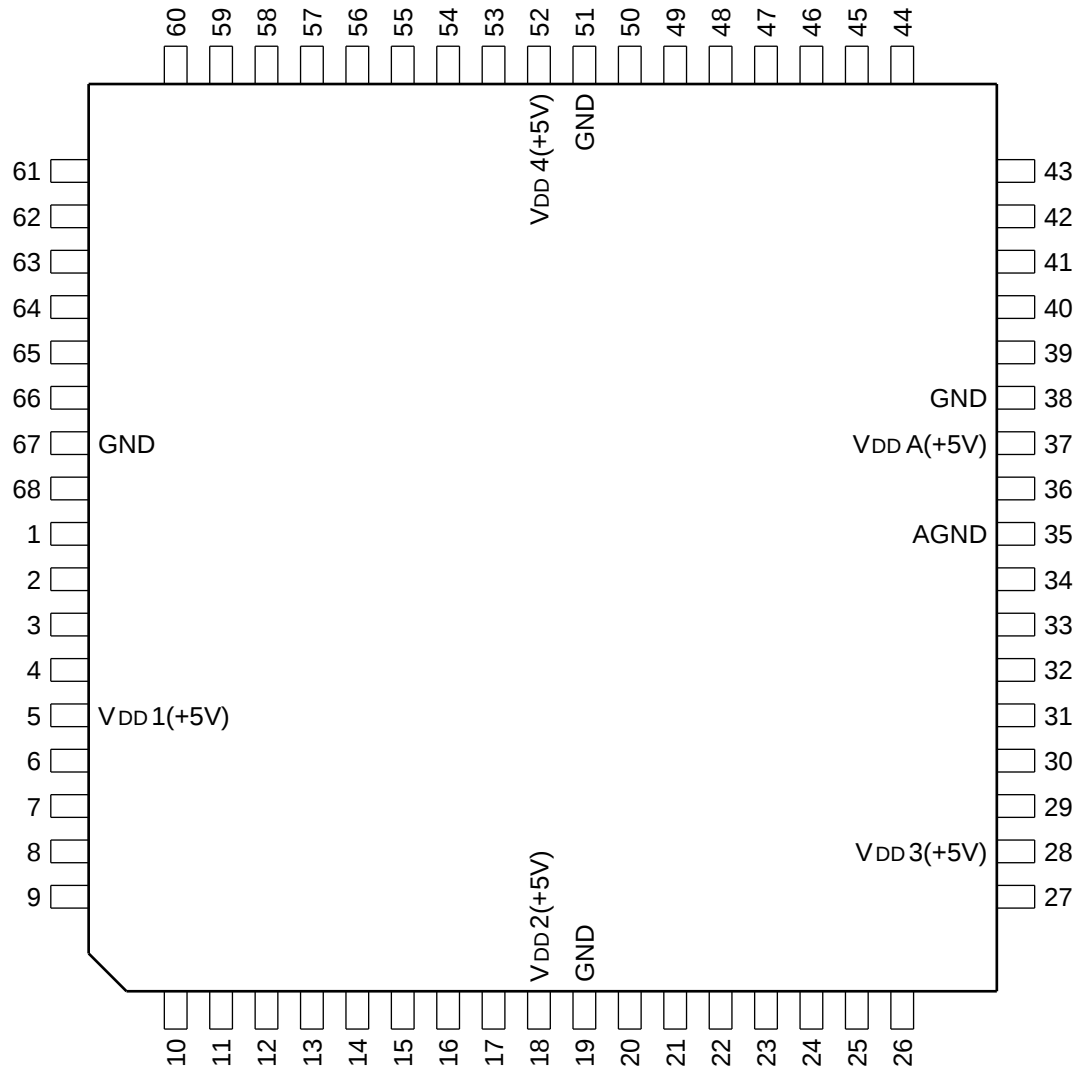


- TOP VIEW -



| PIN No. | I/O | SIGNAL           | PIN No. | I/O | SIGNAL           | PIN No. | I/O | SIGNAL           | PIN No. | I/O | SIGNAL           |
|---------|-----|------------------|---------|-----|------------------|---------|-----|------------------|---------|-----|------------------|
| 1       | –   | SP               | 18      | –   | V <sub>DD2</sub> | 35      | –   | V <sub>ssA</sub> | 52      | –   | V <sub>DD4</sub> |
| 2       | –   | AP               | 19      | –   | V <sub>ss1</sub> | 36      | O   | LFC0             | 53      | O   | Y1               |
| 3       | I   | RESN             | 20      | I   | CVBS4            | 37      | –   | V <sub>DDA</sub> | 54      | O   | Y0               |
| 4       | I   | CREF             | 21      | I   | CVBS5            | 38      | –   | V <sub>ss2</sub> | 55      | O   | UV7              |
| 5       | –   | V <sub>DD1</sub> | 22      | I   | CVBS6            | 39      | O   | ODD              | 56      | O   | UV6              |
| 6       | I   | CUV0             | 23      | I   | CVBS7            | 40      | I/O | SDA              | 57      | O   | UV5              |
| 7       | I   | CUV1             | 24      | O   | GPSW1            | 41      | I   | SCL              | 58      | O   | UV4              |
| 8       | I   | CUV2             | 25      | O   | GPSW2            | 42      | O   | HREF             | 59      | O   | UV3              |
| 9       | I   | CUV3             | 26      | O   | HCL              | 43      | I   | IICSA            | 60      | O   | UV2              |
| 10      | I   | CUV4             | 27      | I   | LL27             | 44      | I   | CP1              | 61      | O   | UV1              |
| 11      | I   | CUV5             | 28      | –   | V <sub>DD3</sub> | 45      | O   | Y7               | 62      | O   | UV0              |
| 12      | I   | CUV6             | 29      | O   | HSY              | 46      | O   | Y6               | 63      | O   | GPSW0            |
| 13      | I   | CUV7             | 30      | O   | VS               | 47      | O   | Y5               | 64      | I   | FEIN             |
| 14      | I   | CVBS0            | 31      | O   | HS               | 48      | O   | Y4               | 65      | O   | MUXC             |
| 15      | I   | CVBS1            | 32      | O   | RTCO             | 49      | O   | Y3               | 66      | O   | FS0              |
| 16      | I   | CVBS2            | 33      | O   | XTAL             | 50      | O   | Y2               | 67      | –   | V <sub>ss4</sub> |
| 17      | I   | CVBS3            | 34      | I   | XTAL1            | 51      | –   | V <sub>ss3</sub> | 68      | I   | FS1              |

**INPUT**

CREF ; CLOCK REFERENCE  
 CP1 ; CLAMPING PULSE  
 CUV0-7 ; CHROMINANCE DATA  
 CVBS0-3 ; CVBS LOWER DATA  
 CVBS4-7 ; CVBS UPPER DATA  
 FEIN ; FAST ENABLE  
 FS1 ; FAST SWITCH  
 IICSA ; SET MODULE ADDRESS  
 LL27 ; LINE-LOCKED SYSTEM CLOCK  
 RESN ; RESET  
 SCL ; I<sup>2</sup>C-BUS CLOCK LINE  
 XTAL1 ; 24.576MHz CONNECTION FOR CRYSTAL

**OUTPUT**

FSO ; FAST SWITCH AND SYNC-INSERTION  
 GPSW0 ; PORT OUTPUT FOR GENERAL PURPOSE  
 GPSW1 ; STATUS BIT FSST0 OR PORT1  
 GPSW2 ; STATUS BIT FSST1 OR PORT2  
 HREF ; HORIZONTAL REFERENCE FOR YUV DATA  
 HCL ; BLACK LEVEL CLAMP PULSE  
 HS ; HORIZONTAL SYNC  
 HSY ; HORIZONTAL SYNC PULSE REFERENCE  
 LFC0 ; LINE FREQUENCY CONTROL  
 MUXC ; MULTIPLEXER CONTROL  
 ODD ; ODD/EVEN FIELD IDENTIFICATION  
 RTCO ; REAL TIME CONTROL  
 UV0-7 ; UV SIGNAL  
 VS ; VERTICAL SYNC  
 XTAL ; 24.576MHz CLOCK  
 Y0-Y7 ; Y SIGNAL

**INPUT/OUTPUT**

SDA ; I<sup>2</sup>C-BUS DATA LINE

