



**SAA1042, A**

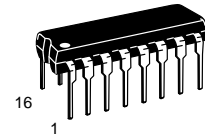
## Advance Information

# Stepper Motor Driver

The SAA1042 drives a two-phase stepper motor in the bipolar mode. The device contains three input stages, a logic section and two output stages. The IC is contained in a 16 pin dual-in-line heat tab plastic package for improved heatsinking capability. The center four ground pins are connected to the copper alloy heat tab and improve thermal conduction from the die to the circuit board.

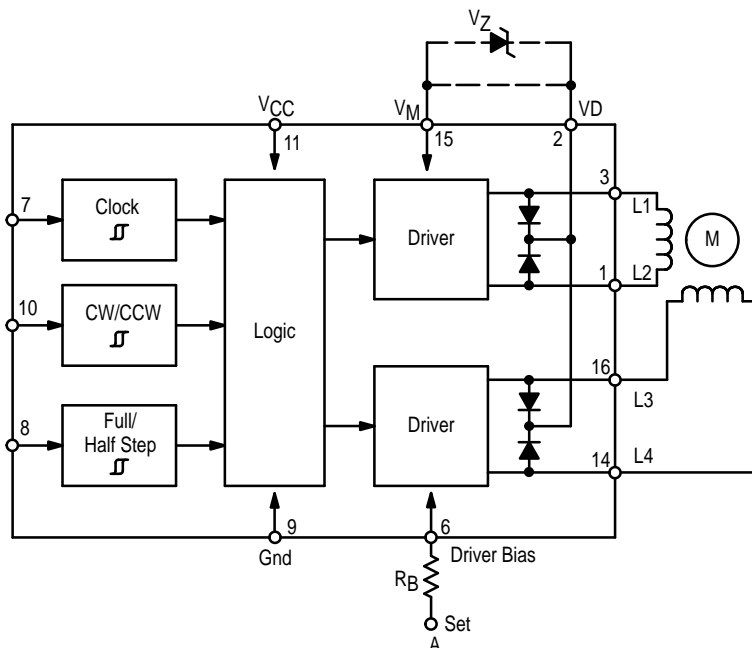
- Drive Stages Designed for Motors: 6.0 V and 12 V: SAA1042V  
24 V: SAA1042AV
- 500 mA/Coil Drive Capability
- Built-In Clamp Diodes for Overvoltage Suppression
- Wide Logic Supply Voltage Range
- Accepts Commands for CW/CCW and Half/Full Step Operation
- Inputs Compatible with Popular Logic Families: MOS, TTL, DTL
- Set Input Defined Output State
- Drive Stage Bias Adaptable to Motor Power Dissipation for Optimum Efficiency

# STEPPER MOTOR DRIVER

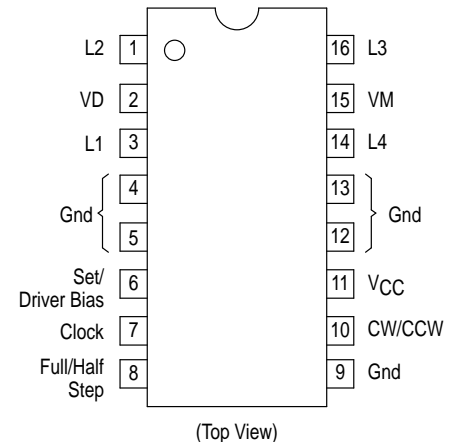
SEMICONDUCTOR  
TECHNICAL DATA

**V SUFFIX**  
PLASTIC PACKAGE  
CASE 648C

**Figure 1. Representative Block Diagram**



## PIN CONNECTIONS



## ORDERING INFORMATION

Device	Operating Temperature Range	Package
SAA1042V	T <sub>J</sub> = - 30° to +125°C	Plastic DIP
SAA1042AV		Plastic DIP

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## MAXIMUM RATINGS (T<sub>A</sub> = 25°C, unless otherwise noted.)

Rating	Symbol	SAA1042V	SAA1042AV	Unit
Clamping Voltage (Pins 1, 3, 14, 16)	V <sub>clamp</sub>	20	30	V
Over Voltage (V <sub>OV</sub> = V <sub>clamp</sub> – V <sub>M</sub> )	V <sub>OV</sub>	6.0	6.0	V
Supply Voltage	V <sub>CC</sub>	20	30	V
Switching or Motor Current/Coil	I <sub>M</sub>	500	500	mA
Input Voltage (Pins 7, 8, 10)	V <sub>in</sub> clock V <sub>in</sub> Full/Half V <sub>in</sub> CW/ CCW	V <sub>CC</sub>		V
Power Dissipation (Note 1)	P <sub>D</sub>	2.0		W
Thermal Resistance, Junction-to-Air	θ <sub>JA</sub>	80		°C/W
Thermal Resistance, Junction-to-Case	θ <sub>JC</sub>	15		
Operating Junction Temperature Range	T <sub>J</sub>	–30 to +125		°C
Storage Temperature Range	T <sub>stg</sub>	–65 to +150		°C

**NOTE:** 1. The power dissipation (P<sub>D</sub>) of the circuit is given by the supply voltage (V<sub>M</sub> and V<sub>CC</sub>) and the motor current (I<sub>M</sub>), and can be determined from Figures 3 and 5. P<sub>D</sub> = P<sub>drive</sub> – P<sub>logic</sub>.

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise noted.)

Characteristics	Pin(s)	Symbol	V <sub>CC</sub>	Min	Typ	Max	Unit
Supply Current	11	I <sub>CC</sub>	5.0 V 20 V	— —	— —	3.5 8.5	mA
Motor Supply Current (I <sub>Pin 6</sub> = –400 μA, Pins 1, 3, 14, 16 Open) V <sub>M</sub> = 6.0 V V <sub>M</sub> = 12 V V <sub>M</sub> = 24 V	15	I <sub>M</sub>	5.0 V 5.0 V 5.0 V	— — —	25 30 40	— — —	mA
Input Voltage, High State	7, 8, 10	V <sub>IH</sub>	5.0 V 10 V 15 V 20 V	2.0 7.0 10 14	— — — —	— — — —	V
Input Voltage, Low State			5.0 V 10 V 15 V 20 V	— — — —	— — — —	0.8 1.5 2.5 3.5	
Input Reverse Current, High State (V <sub>in</sub> = V <sub>CC</sub> )		I <sub>IR</sub>	5.0 V 10 V 15 V 20 V	— — — —	— — — —	2.0 2.0 3.0 5.0	
Input Forward Current, Low State (V <sub>in</sub> = Gnd)			5.0 V 10 V 15 V 20 V	–10 –25 –40 –50	— — — —	— — — —	
Output Voltage, High State (V <sub>M</sub> = 12 V) I <sub>out</sub> = –500 mA I <sub>out</sub> = –50 mA	1, 3, 14, 16	V <sub>OH</sub>	5.0 – 20 V	— —	V <sub>M</sub> – 2.0 V <sub>M</sub> – 1.2	— —	V
Output Voltage, Low State I <sub>out</sub> = 500 mA I <sub>out</sub> = 50 mA		V <sub>OL</sub>	5.0 – 20 V	— —	0.7 0.2	— —	
Output Leakage Current, Pin 6 = Open (V <sub>M</sub> = V <sub>D</sub> = V <sub>clamp</sub> max)	1, 3, 14, 16	I <sub>DR</sub>	5.0 – 20 V	–100	—	—	μA
Clamp Diode Forward Voltage (Drop at I <sub>M</sub> = 500 mA)	2	V <sub>F</sub>	—	—	2.5	3.5	V
Clock Frequency	7	f <sub>c</sub>	5.0 – 20 V	0	—	50	kHz
Clock Pulse Width	7	t <sub>w</sub>	5.0 – 20 V	10	—	—	μs
Set Pulse Width	6	t <sub>s</sub>	—	10	—	—	μs
Set Control Voltage, High State Low State	6	—	—	V <sub>M</sub> —	— —	— 0.5	V

## INPUT/OUTPUT FUNCTIONS

**Clock — (Pin 7)** This input is active on the positive edge of the clock pulse and accepts Logic '1' input levels dependent on the supply voltage and includes hysteresis for noise immunity.

**CW/CCW — (Pin 10)** This input determines the motor's rotational direction. When the input is held low, (OV, see the electrical characteristics) the motor's direction is nominally clockwise (CW). When the input is in the high state, Logic '1', the motor direction is nominally counter clockwise (CCW), depending on the motor connections.

**Full/Half Step — (Pin 8)** This input determines the angular rotation of the motor for each clock pulse. In the low state, the motor will make a full step for each applied clock pulse, while in the high state, the motor will make half a step.

**V<sub>D</sub> — (Pin 2)** This pin is used to protect the outputs (1, 3, 14, 16) where large positive spikes occur due to switching the motor coils. The maximum allowable voltage on these pins is the clamp voltage ( $V_{\text{clamp}}$ ). Motor performance is improved if a zener diode is connected between Pin 2 and 15, as shown in Figure 1.

The following conditions have to be considered when selecting the zener diode:

$$V_{\text{clamp}} = V_M + 6.0 \text{ V}$$

$$V_Z = V_{\text{clamp}} - V_M - V_F$$

where:  $V_F$  = clamp diodes forward voltage drop (see Figure 4)

$$V_{\text{clamp}}: \leq 20 \text{ V for SAA1042V} \leq 30 \text{ V for SAA1042AV}$$

Pins 2 and 15 can be linked, in this case  $V_Z = 0 \text{ V}$ .

**Set/Bias Input — (Pin 6)** This input has two functions:

- 1) The resistor  $R_B$  adapts the drivers to the motor current.
- 2) A pulse via the resistor  $R_B$  sets the outputs (1, 3, 14, 16) to a defined state.

The resistor  $R_B$  can be determined from the graph of Figure 2 according to the motor current and voltage. Smaller values of  $R_B$  will increase the power dissipation of the circuit and larger values of  $R_B$  may increase the saturation voltage of the driver transistors.

When the "set" function is not used, terminal A of the resistor  $R_B$  must be grounded. When the set function is used, terminal A has to be connected to an open-collector (buffer) circuit. Figure 7 shows this configuration. The buffer circuit (off-state) has to sustain the motor voltage ( $V_M$ ). When a pulse is applied via the buffer and the bias resistor ( $R_B$ ), the

motor driver transistors are turned off during the pulse and after the pulse has ended, the outputs will be in defined states. Figure 6 shows the Timing Diagram.

Figure 7 illustrates a typical application in which the SAA1042 drives a 12 V stepper motor with a current consumption of 200 mA/coil. A bias resistor ( $R_B$ ) of 56 k $\Omega$  is chosen according to Figure 2.

The maximum voltage permitted at the output pin is  $V_M + 6.0 \text{ V}$  (see Maximum Ratings table), in this application  $V_M = 12 \text{ V}$ , therefore the maximum voltage is 18 V. The outputs are protected by the internal diodes and an external zener connected between Pins 2 and 15.

From Figure 4, it can be seen that the voltage drop across the internal diodes is about 1.7 V at 200 mA. This results in a zener voltage between Pins 2 and 15 of:

$$V_Z = 6.0 \text{ V} - 1.7 \text{ V} = 4.3 \text{ V}.$$

To allow for production tolerances and a safety margin, a 3.9 V zener has been chosen for this example.

The clock is derived from the line frequency which is phase-locked by the MC14046B and the MC14024. The voltage on the clock input is normally low (Logic '0'). The motor steps on the positive going transition of the clock pulse.

The Logic '0' applied to the Full/Half input (Pin 8) operates the motor in Full Step mode. A Logic '1' at this input will result in Half Step mode. The logic level state on the CW/CCW input (Pin 10), and the connection of the motor coils to the outputs determines the rotational direction of the motor.

These two inputs should be biased to a Logic '0' or '1' and not left floating. In the event of non-use, they should be tied to ground or the logic supply line,  $V_{CC}$ .

The output drivers can be set to a fixed operating point by use of the Set input and a bias resistor,  $R_B$ . A positive pulse to this input turns the drivers off and sets the logic state of the outputs.

After the negative going transition of the Set pulse, and until the first positive going transition of the clock, the outputs will be:

$$L1 = L3 = \text{high and } L2 = L4 = \text{low, (see Figure 6)}.$$

The Set input can be driven by a MC14007B or a transistor whose collector resistor is  $R_B$ . **If the input is not used, the bottom of  $R_B$  must be grounded.**

The total power dissipation of the circuit can be determined from Figures 3 and 5:

$$P_D = 0.9 \text{ W} + 0.08 \text{ W} = 0.98 \text{ W}.$$

The junction temperature can then be computed using Figure 8.

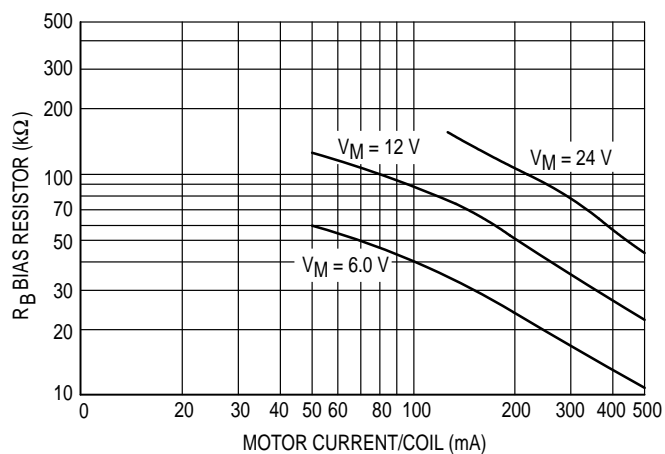
Figure 2. Bias Resistor  $R_B$  versus Motor Current

Figure 3. Drive Stage Power Dissipation

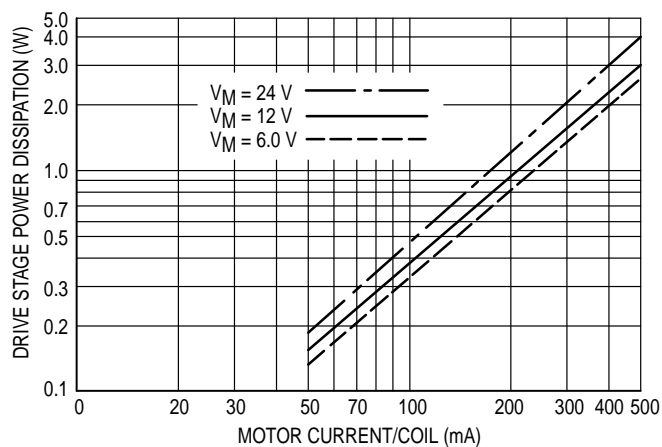


Figure 4. Clamp Diode Forward Current versus Forward Voltage

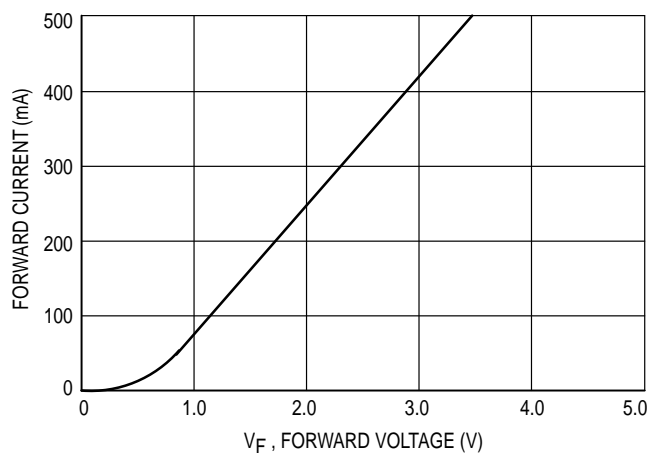


Figure 5. Power Dissipation versus Logic Supply Voltage

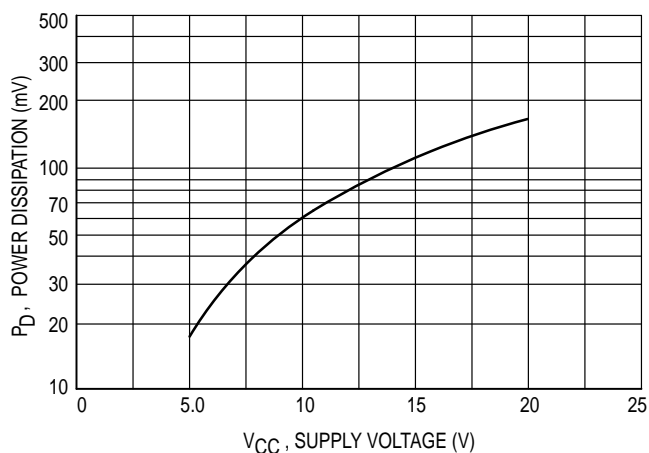
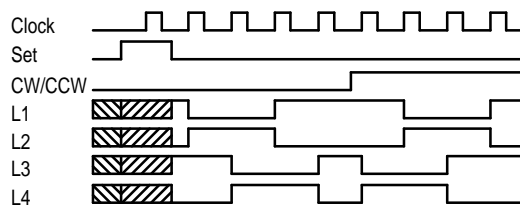
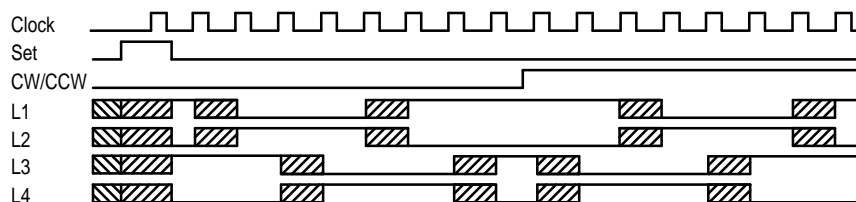


Figure 6. Timing Diagram

Full Step Motor Drive Mode. Full/Half Step Input = 0

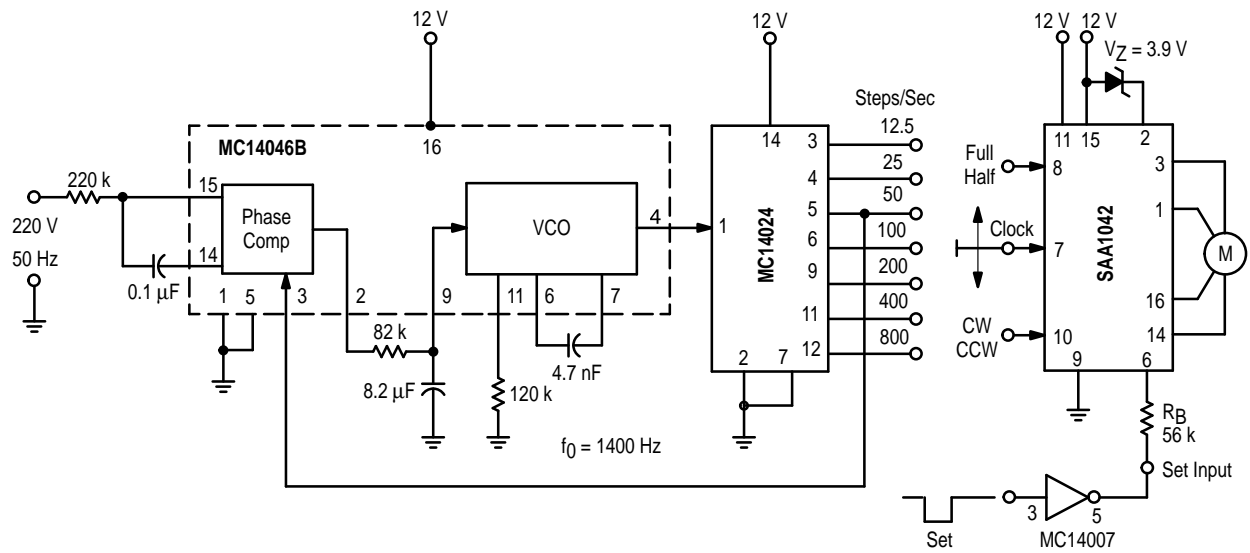


Half Step Motor Drive Mode. Full/Half Step Input = 1

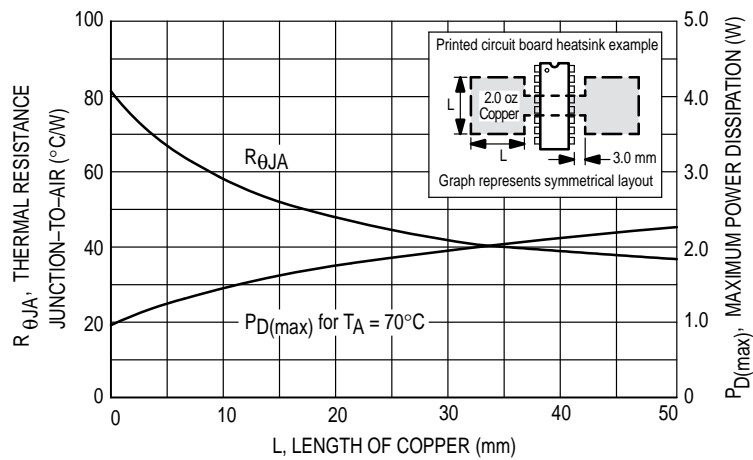


## SAA1042, A

**Figure 7. Typical Application**  
Selectable Step Rates with the Time Base Derived from the Line Frequency

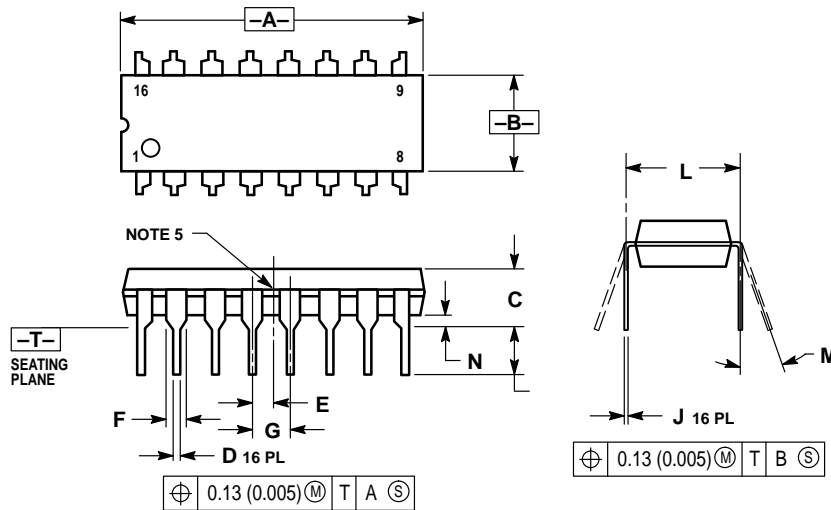


**Figure 8. Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length**



## OUTLINE DIMENSIONS


V SUFFIX  
PLASTIC PACKAGE  
CASE 648C-03  
ISSUE C



## NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.
5. INTERNAL LEAD CONNECTION BETWEEN 4 AND 5, 12 AND 13.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.740	0.840	18.80	21.34
B	0.240	0.260	6.10	6.60
C	0.145	0.185	3.69	4.69
D	0.015	0.021	0.38	0.53
E	0.050 BSC		1.27 BSC	
F	0.040	0.70	1.02	1.78
G	0.100 BSC		2.54 BSC	
J	0.008	0.015	0.20	0.38
K	0.115	0.135	2.92	3.43
L	0.300 BSC		7.62 BSC	
M	0°	10°	0°	10°
N	0.015	0.040	0.39	1.01

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