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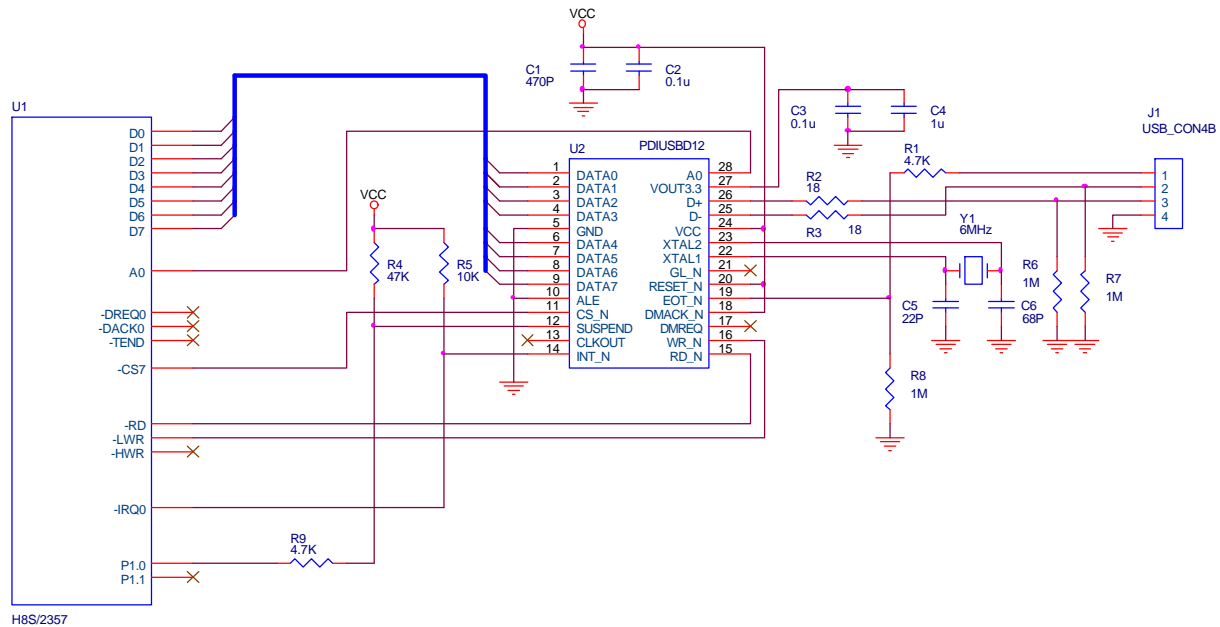
Interconnectivity

8 June 1998

Application Notes

Interfacing PDIUSBD12 to Hitachi H8S/2357

Example Interfacing Circuit in Non-DMA Mode



1. Interrupt Handling

2. Address Mapping

PDIUSBD12 can be mapped to any address area, where is simple for interfacing requirements. In above example circuit, PDISUBD12's base address is mapped to H'FFFF08 in CS7 area. Assuming that it is the only external device within this area, CS7 can be directly connected to CS_N.

3. Using DMA

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[illegible]

4. I/O Ports

The 4.7K resistor (R9) is added as protection against the faulty situation when P1.0 output high and PDIUSB12 pulls SUSPEND pin low.

There are 3 registers to configure port 1: *Port 1 Data Direction Register (P1DDR)*, *Port 1 Data Register (P1DR)* and *Port 1 Register (PORT1)*.