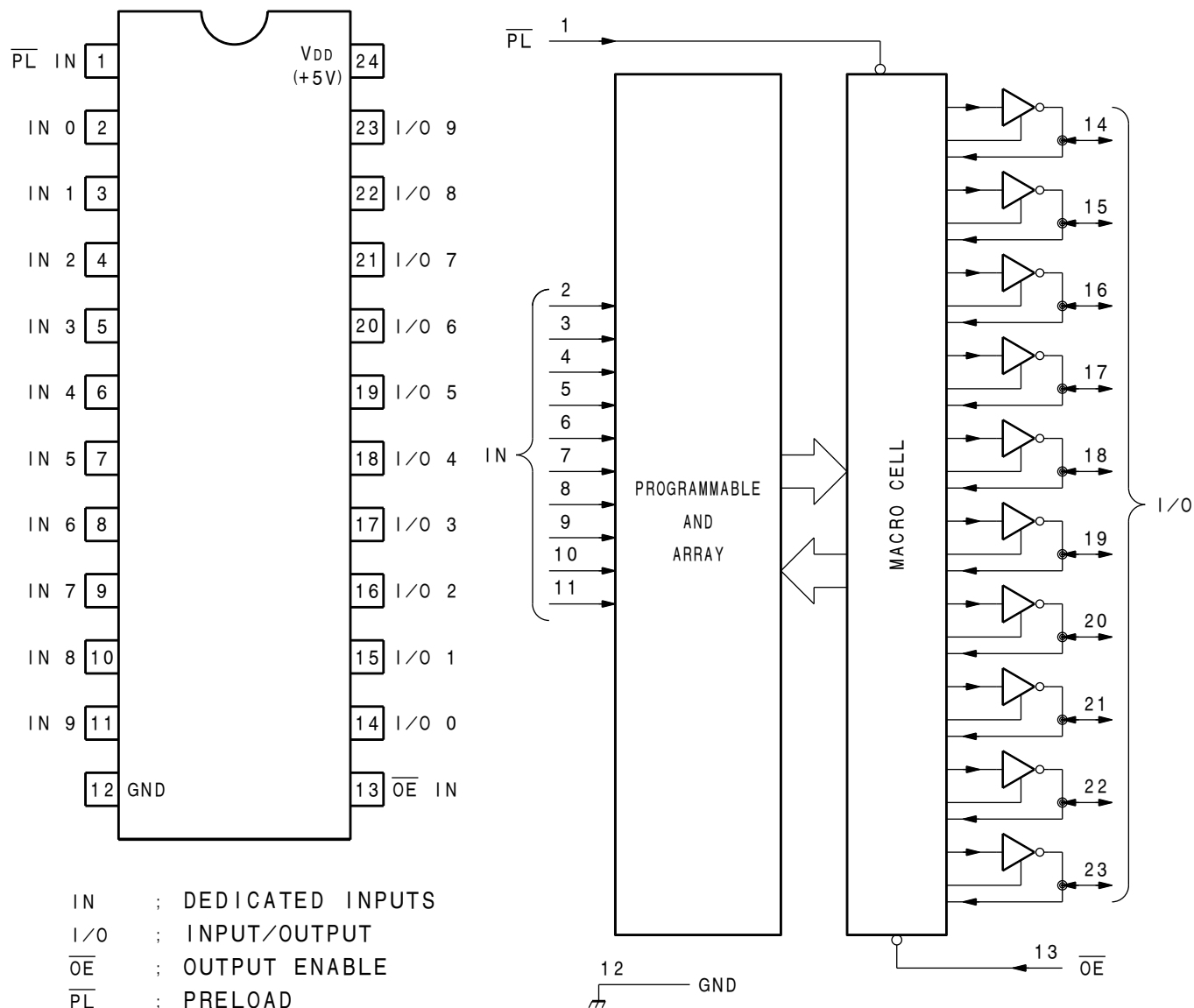


C-MOS ELECTRICALLY ERASABLE PROGRAMMABLE ARRAY LOGIC -TOP VIEW-



*ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.