## **APPLICATION NOTE**

# The BLF246 as an H.F.-S.S.B. amplifier

**NCO8801** 





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#### 1 SUMMARY

This report gives information on the BLF246 as a linear amplifier at 28 MHz for S.S.B. signals.

Typically the device produces an output power of 80 W P.E.P. at an I.M. distortion of –34 dB. At a supply voltage of 28 V the power gain is 20 dB and the 2-tone efficiency 40%.

For the design of wideband amplifiers in the range of 1.5 - 30 MHz additional information is presented showing that the power gain drops to appr. 19 dB with a variation of  $\pm 0.4$  dB.

#### 2 INTRODUCTION

The BLF246 is an R.F. power MOS-transistor in SOT121 package specified at a frequency of 108 MHz and a supply voltage of 28 V for an output power of 80 W C.W.

During the development period this device has also been tested in a class-AB amplifier at 28 MHz to investigate its behaviour as a linear amplifier for possible application in the H.F. band (1.5 - 30 MHz) with S.S.B. modulation. In that case the 3rd and 5th order intermodulation products are of special interest.

#### 3 NARROW BAND TEST AT 28 MHz

The R.F. circuit used for this purpose is depicted in Fig.1. The optimum drain quiescent current for linear operation of this device in class-AB in 0.6 A.

This is achieved with a Vgs which is appr. 0.48 V higher than the specified Vgs(th).

The output circuit has been aligned with a dummy load consisting of the parallel connection of a 3.5  $\Omega$  resistor and a 400 pF capacitor. This results in a load seen by the transistor of appr. 3.3  $\Omega$  with a negligible reactive part.

The capacitors C9 and C10 are used to reduce the second harmonic voltage at the drain.

In a wideband amplifier this is not necessary because the same effect is obtained by other means like a centre-tapped drain choke in a push-pull amplifier.

The input circuit is aligned for minimum reflection at 50% of the maximum output power.

The gate-source damping resistors R1 and R2 are needed for 2 reasons:

- 1. Stability i.e. to prevent oscillation when the output circuit is detuned
- 2. Low intermodulation distortion. In general it can be said that I.M. distortion is improved by lowering the value of these resistors. Of course they will influence the power gain of the amplifier.

The average performance of the BLF246 in this amplifier is shown in Figs 2, 3, 4 and 5.

At an output power of 80 W P.E.P. the average performance is:

```
Gp = 20 \text{ dB}

Eff. = 40\% \text{ (2-tone)}

d3 = -34 \text{ dB}

d5 = -42 \text{ dB}.
```

The distortion products have been measured with respect to one tone.

It must be mentioned that the information given here is typical i.e. it is not guaranteed by measurements on individual transistors.

#### 4 WIDEBAND OPERATION

For operation in the H.F. range (1.5 – 30 MHz) it will be necessary to reduce the value of Rgs from 18  $\Omega$  as used in the narrow band amplifier down to 12  $\Omega$  to obtain a more constant power gain over the band and to achieve a smaller variation in the input impedance to allow an easier matching to a driver stage or a 50  $\Omega$  source.

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Table 2 shows the results of a computer calculation on this basis. It appears that the power gain is reduced by appr. 1 dB while the gain variation is then  $\pm 0.4$  dB.

The optimum load impedance is still appr. 3.3  $\Omega$  with a negligible reactive part.

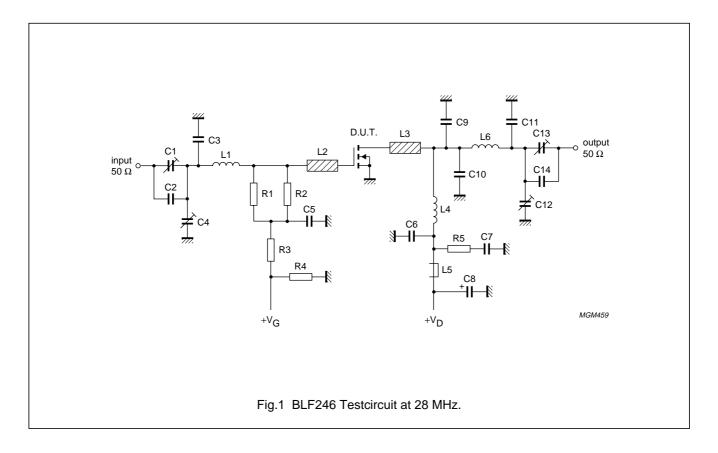
It must be mentioned that the figures given for power gain and input impedance include already the effects of an Rgs of  $12 \Omega$ .

As mentioned earlier the lowering of Rgs has a positive effect on the intermodulation behaviour.

For the design of a suitable wideband input matching network one is referred to application report nr. NCO8703.

#### **5 ACKNOWLEDGEMENT**

The 28 MHz amplifier described in this report was designed and constructed by Mr. G. Lukkassen of our laboratory. He also made the various measurements.



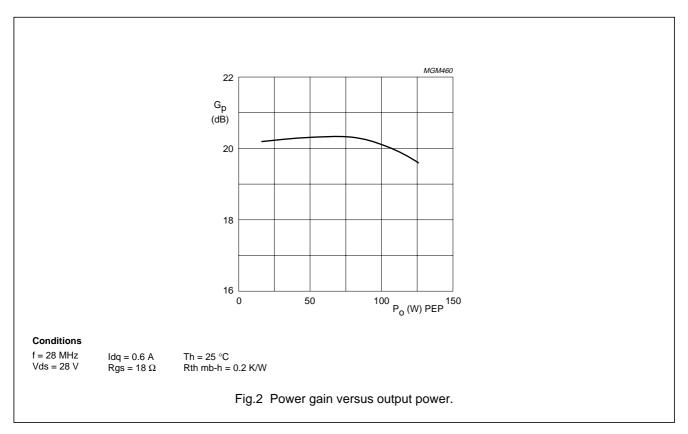
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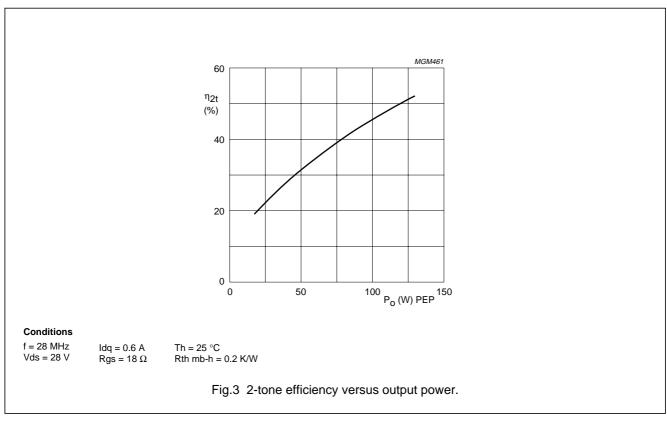
**Table 1** PC-board: double Cu-clad, 1.6 mm PTFE fibre-glas dielectric ( $\epsilon_r$  = 2.2)

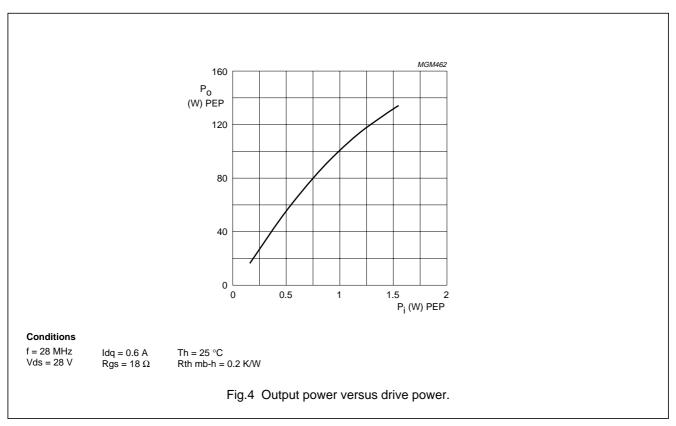
C1 = C4	6 to 80 pF	film dielectric trimmer (cat.nr. 2222 809 07013)		
C2	62 pF	multilayer ceramic chip capacitor; note 1		
C3	150 pF	multilayer ceramic chip capacitor; note 1		
C5 = C6	100 nF	multilayer ceramic chip capacitor (cat.nr. 2222 852 47104)		
C7	3 × 100 nF	multilayer ceramic chip capacitor (cat.nr. 2222 852 47104)		
C8	2.2 μF	electrolytic capacitor		
C9 = C10 = C14	75 pF	multilayer ceramic chip capacitor; note 1		
C11	100 pF	multilayer ceramic chip capacitor; note 1		
C12 = C13	7 to 100 pF	film dielectric trimmer (cat.nr. 2222 809 07015)		
L1	184 nH	6 turns enamelled Cu-wire (0.7 mm), int.dia: 6 mm		
L2 = L3	41.1 Ω	stripline (10 × 6 mm)		
L4	278 nH	7 turns enamelled Cu-wire (1.5 mm), int.dia: 8 mm		
L5		Ferroxcube h.f. coke, grade 3B (cat.nr. 4312 020 36642)		
L6	131 nH	4 turns enamelled Cu-wire (1.5 mm), int.dia: 8 mm		
R1 = R2	34.8 Ω	metal film resistor (cat.nr. 2322 153 53489)		
R3	1 kΩ	metal film resistor (cat.nr. 2322 151 71002)		
R4	1 ΜΩ	metal film resistor (cat.nr. 2322 151 71005)		
R5	10 Ω	metal film resistor (cat.nr. 2322 153 51009)		

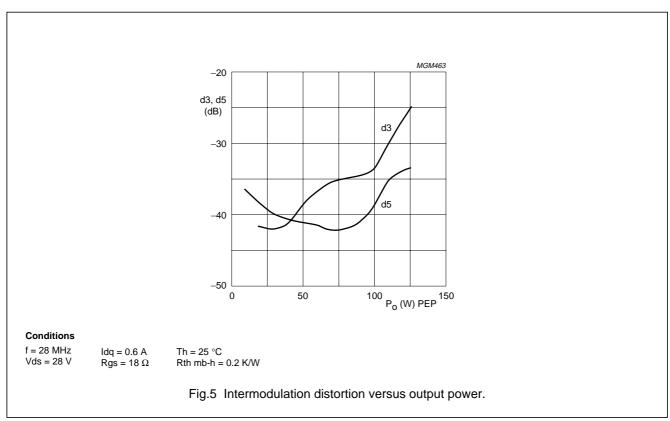
### Note

1. American Technical Ceramics type 100B or capacitor of same quality.









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**Table 2** Power gain, input and load impedance versus frequency; Idq = 0.6 A;  $Rgs = 12 \Omega$ 

BLF246	V <sub>DS</sub> = 28 V	P <sub>O</sub> = 80 W P.E.P.	Class-AB
f	G	Inp.Imp.	Load Imp.
MHz	dB	Ω	Ω
1.5	19.57	11.97 – j0.51	3.34 + j0.01
5.0	19.54	11.71 – j1.67	3.34 + j0.02
10.0	19.47	10.92 – j3.06	3.33 + j0.04
15.0	19.35	9.86 – j4.03	3.32 + j0.07
20.0	19.20	8.75 – j4.59	3.31 + j0.09
25.0	19.00	7.72 – j4.84	3.30 + j0.11
30.0	18.77	6.83 – j4.86	3.28 + j0.13

## Philips Semiconductors – a worldwide company

Argentina: see South America

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Tel. +61 2 9805 4455, Fax. +61 2 9805 4466

Austria: Computerstr. 6, A-1101 WIEN, P.O. Box 213, Tel. +43 160 1010,

Fax. +43 160 101 1210

Belarus: Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,

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China/Hong Kong: 501 Hong Kong Industrial Technology Centre,

72 Tat Chee Avenue, Kowloon Tong, HONG KONG,

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Colombia: see South America Czech Republic: see Austria

Denmark: Prags Boulevard 80, PB 1919, DK-2300 COPENHAGEN S,

Tel. +45 32 88 2636, Fax. +45 31 57 0044 Finland: Sinikalliontie 3, FIN-02630 ESPOO, Tel. +358 9 615800, Fax. +358 9 61580920

France: 51 Rue Carnot, BP317, 92156 SURESNES Cedex,

Tel. +33 1 40 99 6161, Fax. +33 1 40 99 6427

Germany: Hammerbrookstraße 69, D-20097 HAMBURG,

Tel. +49 40 23 53 60, Fax. +49 40 23 536 300

Greece: No. 15, 25th March Street, GR 17778 TAVROS/ATHENS,

Tel. +30 1 4894 339/239, Fax. +30 1 4814 240

Hungary: see Austria

India: Philips INDIA Ltd, Band Box Building, 2nd floor, 254-D, Dr. Annie Besant Road, Worli, MUMBAI 400 025,

Tel. +91 22 493 8541, Fax. +91 22 493 0966

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Israel: RAPAC Electronics, 7 Kehilat Saloniki St, PO Box 18053, TEL AVIV 61180, Tel. +972 3 645 0444, Fax. +972 3 649 1007

Italy: PHILIPS SEMICONDUCTORS, Piazza IV Novembre 3, 20124 MILANO, Tel. +39 2 6752 2531, Fax. +39 2 6752 2557

Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku, TOKYO 108,

Tel. +81 3 3740 5130, Fax. +81 3 3740 5077

Korea: Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL,

Tel. +82 2 709 1412, Fax. +82 2 709 1415 Malaysia: No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,

Tel. +60 3 750 5214, Fax. +60 3 757 4880

Mexico: 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905, Tel. +9-5 800 234 7381

Middle East: see Italy

Netherlands: Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,

Tel. +31 40 27 82785, Fax. +31 40 27 88399

New Zealand: 2 Wagener Place, C.P.O. Box 1041, AUCKLAND,

Tel. +64 9 849 4160, Fax. +64 9 849 7811 Norway: Box 1, Manglerud 0612, OSLO, Tel. +47 22 74 8000, Fax. +47 22 74 8341

Philippines: Philips Semiconductors Philippines Inc., 106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI, Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

Poland: UI. Lukiska 10, PL 04-123 WARSZAWA, Tel. +48 22 612 2831, Fax. +48 22 612 2327

Portugal: see Spain Romania: see Italy

Russia: Philips Russia, UI. Usatcheva 35A, 119048 MOSCOW,

Tel. +7 095 755 6918, Fax. +7 095 755 6919

Singapore: Lorong 1, Toa Payoh, SINGAPORE 1231,

Tel. +65 350 2538, Fax. +65 251 6500

Slovakia: see Austria Slovenia: see Italy

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2092 JOHANNESBURG, P.O. Box 7430 Johannesburg 2000,

Tel. +27 11 470 5911, Fax. +27 11 470 5494 South America: Al. Vicente Pinzon, 173, 6th floor,

04547-130 SÃO PAULO, SP, Brazil, Tel. +55 11 821 2333, Fax. +55 11 821 2382

Spain: Balmes 22 08007 BARCELONA Tel. +34 3 301 6312, Fax. +34 3 301 4107

Sweden: Kottbygatan 7, Akalla, S-16485 STOCKHOLM,

Tel. +46 8 632 2000, Fax. +46 8 632 2745

Switzerland: Allmendstrasse 140, CH-8027 ZÜRICH,

Tel. +41 1 488 2686, Fax. +41 1 488 3263

Taiwan: Philips Semiconductors, 6F, No. 96, Chien Kuo N. Rd., Sec. 1, TAIPEI, Taiwan Tel. +886 2 2134 2865, Fax. +886 2 2134 2874

Thailand: PHILIPS ELECTRONICS (THAILAND) Ltd.

209/2 Sanpavuth-Bangna Road Prakanong, BANGKOK 10260,

Tel. +66 2 745 4090, Fax. +66 2 398 0793

Turkey: Talatpasa Cad. No. 5, 80640 GÜLTEPE/ISTANBUL,

Tel. +90 212 279 2770, Fax. +90 212 282 6707

Ukraine: PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,

252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

United Kingdom: Philips Semiconductors Ltd., 276 Bath Road, Haves. MIDDLESEX UB3 5BX, Tel. +44 181 730 5000, Fax. +44 181 754 8421

United States: 811 East Arques Avenue, SUNNYVALE, CA 94088-3409,

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