

MPC2604GA MPC2605

Integrated Secondary Cache for PowerPC™ Microprocessor Applications

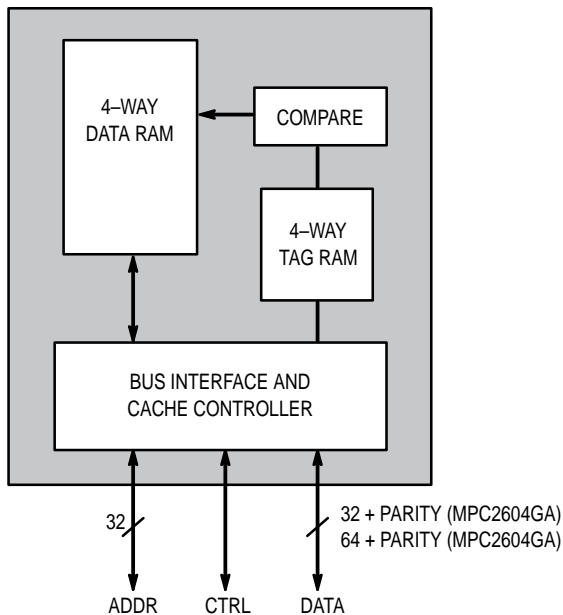
The Secondary Cache Solution For PowerPC Microprocessors:

The MPC2604GA and MPC2605 are the highest performing secondary cache solutions available for use in PowerPC 60x buses. The MPC2604GA and MPC2605 are four-way set associative integrated look-aside caches with copy-back or optional write-through capability. An increase in system performance is achieved by integrating the data, tag, host interface and least recently used (LRU) memory with a cache controller in a single chip.

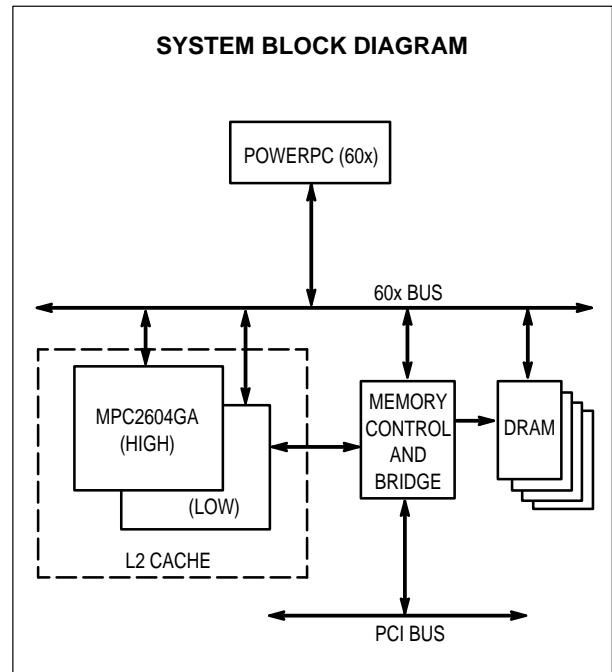
These solutions offer minimum latency and zero wait state burst performance – burst reads occur at 2-1-1-1 clock rates.

The multi-way set associativity feature increases cache hit rates which makes the cache behave like a direct-mapped cache twice its size. This yields a higher performance system solution at 256KB prices.

MPC2604GA/MPC2605 BLOCK DIAGRAM



SYSTEM BLOCK DIAGRAM



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