

MC78M00 Series

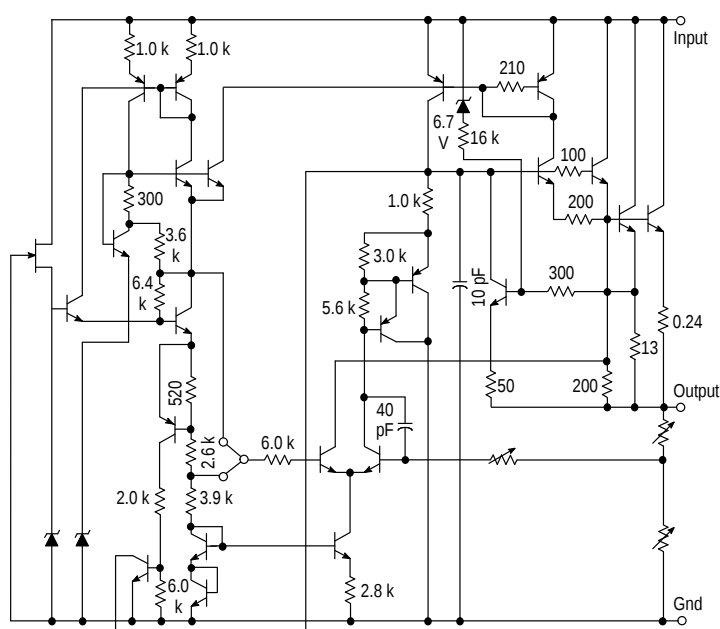
Three-Terminal Medium Current Positive Voltage Regulators

The MC78M00 Series positive voltage regulators are identical to the popular MC7800 Series devices, except that they are specified for only half the output current. Like the MC7800 devices, the MC78M00 three-terminal regulators are intended for local, on-card voltage regulation.

Internal current limiting, thermal shutdown circuitry and safe-area compensation for the internal pass transistor combine to make these devices remarkably rugged under most operating conditions. Maximum output current, with adequate heatsinking is 500 mA.

- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe-Area Compensation

Representative Schematic Diagram



This device contains 28 active transistors.

DEVICE TYPE/NOMINAL OUTPUT VOLTAGE

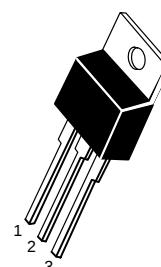
MC78M05B,C	5.0 V	MC78M09B,C	9.0 V	MC78M18B,C	18 V
MC78M06B,C	6.0 V	MC78M12B,C	12 V	MC78M20B,C	20 V
MC78M08B,C	8.0 V	MC78M15B,C	15 V	MC78M24B,C	24 V

THREE-TERMINAL MEDIUM CURRENT POSITIVE FIXED VOLTAGE REGULATORS

SEMICONDUCTOR TECHNICAL DATA

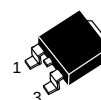
T SUFFIX
PLASTIC PACKAGE
CASE 221A
(TO-220)

Heatsink surface
connected to Pin 2.

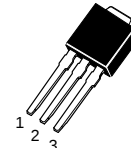


Pin 1. Input
2. Ground
3. Output

DT SUFFIX
PLASTIC PACKAGE
CASE 369A
(DPAK)



DT-1 SUFFIX
PLASTIC PACKAGE
CASE 369
(DPAK)



Heatsink surface (shown as terminal 4 in
case outline drawing) is connected to Pin 2.

ORDERING INFORMATION

Device	Tested Operating Temperature Range	Package
MC78MXXCDT* MC78MXXCDT-1*	$T_J = 0^\circ \text{ to } +125^\circ\text{C}$	DPAK
MC78MXXCT		TO-220
MC78MXXBT#	$T_J = -40^\circ \text{ to } +125^\circ\text{C}$	TO-220
MC78MXXBDT#		DPAK

XX Indicates nominal voltage.

* Available in 5, 8, 12 and 15 V devices.

Automotive temperature range selections are available with special test conditions and additional tests. Contact your local Motorola sales office for information.

MC78M00 Series

MAXIMUM RATINGS (T_A = 25°C, unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage (5.0 V–18 V) (20 V–24V)	V _I	35 40	Vdc
Power Dissipation (Package Limitation) Plastic Package, T Suffix T _A = 25°C Thermal Resistance, Junction-to-Air Thermal Resistance, Junction-to-Case Plastic Package, DT Suffix T _A = 25°C Thermal Resistance, Junction-to-Air Thermal Resistance, Junction-to-Case	P _D θ _{JA} θ _{JC} P _D θ _{JA} θ _{JC}	Internally Limited 70 5.0 Internally Limited 92 5.0	°C/W °C/W °C/W °C/W
Operating Junction Temperature Range	T _J	+150	°C
Storage Temperature Range	T _{stg}	–65 to +150	°C

MC78M05B,C ELECTRICAL CHARACTERISTICS (V_I = 10 V, I_O = 350 mA, 0°C < T_J < +125°C, P_D ≤ 5.0 W, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage (T _J = 25°C)	V _O	4.8	5.0	5.2	Vdc
Line Regulation (T _J = 25°C, 7.0 Vdc ≤ V _I ≤ 25 Vdc, I _O = 200 mA)	Reg _{line}	–	3.0	50	mV
Load Regulation (T _J = 25°C, 5.0 mA ≤ I _O ≤ 500 mA) (T _J = 25°C, 5.0 mA ≤ I _O ≤ 200 mA)	Reg _{load}	– –	20 10	100 50	mV
Output Voltage (7.0 Vdc ≤ V _I ≤ 25 Vdc, 5.0 mA ≤ I _O ≤ 200 mA) (7.0 Vdc ≤ V _I ≤ 20 Vdc, 5.0 mA ≤ I _O ≤ 350 mA)	V _O	4.75	–	5.25	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	–	3.2	6.0	mA
Quiescent Current Change (8.0 Vdc ≤ V _I ≤ 25 Vdc, I _O = 200 mA) (5.0 mA ≤ I _O ≤ 350 mA)	ΔI _{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage (T _A = 25°C, 10 Hz ≤ f ≤ 100 kHz)	V _n	–	40	–	μV
Ripple Rejection (I _O = 100 mA, f = 120 Hz, 8.0 V ≤ V _I ≤ 18 V) (I _O = 300 mA, f = 120 Hz, 8.0 V ≤ V _I ≤ 18 V, T _J = 25°C)	RR	62 62	– 80	– –	dB
Dropout Voltage (T _J = 25°C)	V _I –V _O	–	2.0	–	Vdc
Short Circuit Current Limit (T _J = 25°C, V _I = 35 V)	I _{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage (I _O = 5.0 mA)	ΔV _O /ΔT	–	±0.2	–	mV/°C
Peak Output Current (T _J = 25°C)	I _O	–	700	–	mA

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MC78M06B,C ELECTRICAL CHARACTERISTICS ($V_I = 11\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	5.75	6.0	6.25	Vdc
Line Regulation ($T_J = 25^\circ\text{C}$, $8.0\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	5.0	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	20 10	120 60	mV
Output Voltage ($8.0\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$) ($8.0\text{ Vdc} \leq V_I \leq 21\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	5.7	–	6.3	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	6.0	mA
Quiescent Current Change ($9.0\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	45	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $9.0\text{ V} \leq V_I \leq 19\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $9.0\text{ V} \leq V_I \leq 19\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	59 59	– 80	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$, $V_I = 35\text{ V}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.2	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

MC78M08B,C ELECTRICAL CHARACTERISTICS ($V_I = 14\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	7.7	8.0	8.3	Vdc
Line Regulation ($T_J = 25^\circ\text{C}$, $10.5\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	6.0	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	25 10	160 80	mV
Output Voltage ($10.5\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$) ($10.5\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	7.6	–	8.4	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	6.0	mA
Quiescent Current Change ($10.5\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	52	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $11.5\text{ V} \leq V_I \leq 21.5\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $11.5\text{ V} \leq V_I \leq 21.5\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	56 56	– 80	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$, $V_I = 35\text{ V}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.2	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

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MC78M09B,C ELECTRICAL CHARACTERISTICS ($V_I = 15\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	8.64	9.0	9.45	Vdc
Line Regulation ($T_J = 25^\circ\text{C}$, $11.5\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	6.0	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	25 10	180 90	mV
Output Voltage ($11.5\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$) ($11.5\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	8.55	–	9.45	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	6.0	mA
Quiescent Current Change ($11.5\text{ Vdc} \leq V_I \leq 25\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	52	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $12.5\text{ V} \leq V_I \leq 22.5\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $12.5\text{ V} \leq V_I \leq 22.5\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	56 56	– 80	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$, $V_I = 35\text{ V}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.2	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

MC78M12B,C ELECTRICAL CHARACTERISTICS ($V_I = 19\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	11.5	12	12.5	Vdc
Line Regulation ($T_J = 25^\circ\text{C}$, $14.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	8.0	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	25 10	240 120	mV
Output Voltage ($14.5\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	11.4	–	12.6	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	6.0	mA
Quiescent Current Change ($14.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	75	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $15\text{ V} \leq V_I \leq 25\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $15\text{ V} \leq V_I \leq 25\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	55 55	– 80	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$, $V_I = 35\text{ V}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.3	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

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MC78M15B,C ELECTRICAL CHARACTERISTICS ($V_I = 23\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	14.4	15	15.6	Vdc
Input Regulation ($T_J = 25^\circ\text{C}$, $17.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	10	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	25 10	300 150	mV
Output Voltage ($17.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	14.25	–	15.75	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	6.0	mA
Quiescent Current Change ($17.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	90	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $18.5\text{ V} \leq V_I \leq 28.5\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $18.5\text{ V} \leq V_I \leq 28.5\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	54 54	– 70	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$, $V_I = 35\text{ V}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.3	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

MC78M18B,C ELECTRICAL CHARACTERISTICS ($V_I = 27\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	17.3	18	18.7	Vdc
Line Regulation ($T_J = 25^\circ\text{C}$, $21\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	10	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	30 10	360 180	mV
Output Voltage ($21\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	17.1	–	18.9	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	6.5	mA
Quiescent Current Change ($21\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	100	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $22\text{ V} \leq V_I \leq 32\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $22\text{ V} \leq V_I \leq 32\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	53 53	– 70	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$, $V_I = 35\text{ V}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.3	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

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MC78M20B,C ELECTRICAL CHARACTERISTICS ($V_I = 29\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	19.2	20	20.8	Vdc
Line Regulation ($T_J = 25^\circ\text{C}$, $23\text{ Vdc} \leq V_I \leq 35\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	10	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	30 10	400 200	mV
Output Voltage ($23\text{ Vdc} \leq V_I \leq 35\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	19	–	21	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	6.5	mA
Quiescent Current Change ($23\text{ Vdc} \leq V_I \leq 35\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	110	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $24\text{ V} \leq V_I \leq 34\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $24\text{ V} \leq V_I \leq 34\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	52 52	– 70	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$, $V_I = 35\text{ V}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.5	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

MC78M24B,C ELECTRICAL CHARACTERISTICS ($V_I = 33\text{ V}$, $I_O = 350\text{ mA}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, $P_D \leq 5.0\text{ W}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = 25^\circ\text{C}$)	V_O	23	24	25	Vdc
Line Regulation ($T_J = 25^\circ\text{C}$, $27\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$, $I_O = 200\text{ mA}$)	Reg _{line}	–	10	50	mV
Load Regulation ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$) ($T_J = 25^\circ\text{C}$, $5.0\text{ mA} \leq I_O \leq 200\text{ mA}$)	Reg _{load}	– –	30 10	480 240	mV
Output Voltage ($27\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$, $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	V_O	22.8	–	25.2	Vdc
Input Bias Current ($T_J = 25^\circ\text{C}$)	I_{IB}	–	3.2	7.0	mA
Quiescent Current Change ($27\text{ Vdc} \leq V_I \leq 38\text{ Vdc}$, $I_O = 200\text{ mA}$) ($5.0\text{ mA} \leq I_O \leq 350\text{ mA}$)	ΔI_{IB}	– –	– –	0.8 0.5	mA
Output Noise Voltage ($T_A = 25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	170	–	μV
Ripple Rejection ($I_O = 100\text{ mA}$, $f = 120\text{ Hz}$, $28\text{ V} \leq V_I \leq 38\text{ V}$) ($I_O = 300\text{ mA}$, $f = 120\text{ Hz}$, $28\text{ V} \leq V_I \leq 38\text{ V}$, $T_J = 25^\circ\text{C}$)	RR	50 50	– 70	– –	dB
Dropout Voltage ($T_J = 25^\circ\text{C}$)	$V_I - V_O$	–	2.0	–	Vdc
Short Circuit Current Limit ($T_J = 25^\circ\text{C}$)	I_{OS}	–	50	–	mA
Average Temperature Coefficient of Output Voltage ($I_O = 5.0\text{ mA}$)	$\Delta V_O / \Delta T$	–	± 0.5	–	$\text{mV}/^\circ\text{C}$
Peak Output Current ($T_J = 25^\circ\text{C}$)	I_O	–	700	–	mA

DEFINITIONS

Line Regulation – The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation – The change in output voltage for a change in load current at constant chip temperature.

Maximum Power Dissipation – The maximum total device dissipation for which the regulator will operate within specifications.

Input Bias Current – That part of the input current that is not delivered to the load.

Output Noise Voltage – The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

Long Term Stability – Output voltage stability under accelerated life test conditions with the maximum rated voltage listed in the devices' electrical characteristics and maximum power dissipation.

Figure 1. DPAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

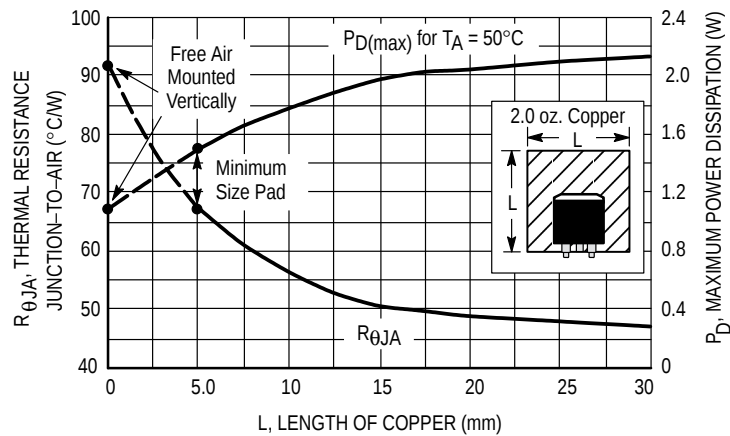


Figure 2. Worst Case Power Dissipation versus Ambient Temperature (TO-220)

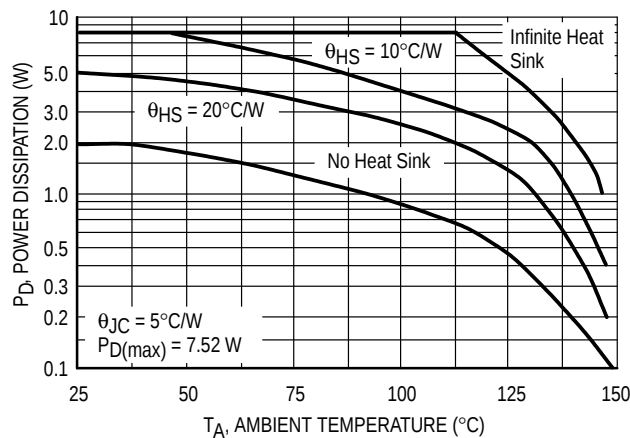


Figure 3. Peak Output Current versus Dropout Voltage

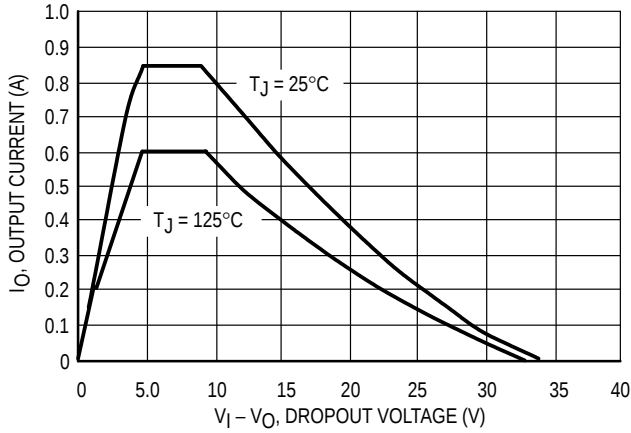


Figure 4. Dropout Voltage versus Junction Temperature

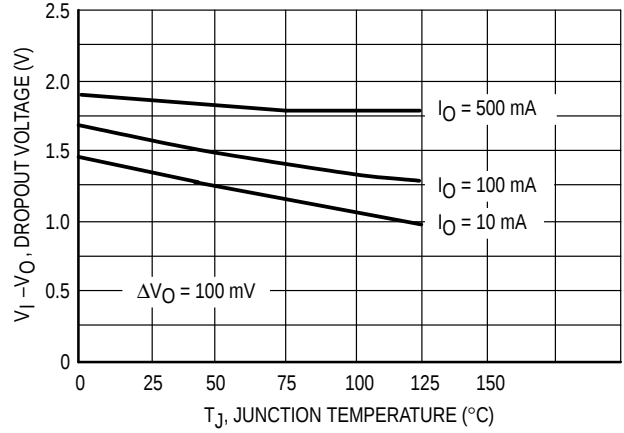


Figure 5. Ripple Rejection versus Frequency

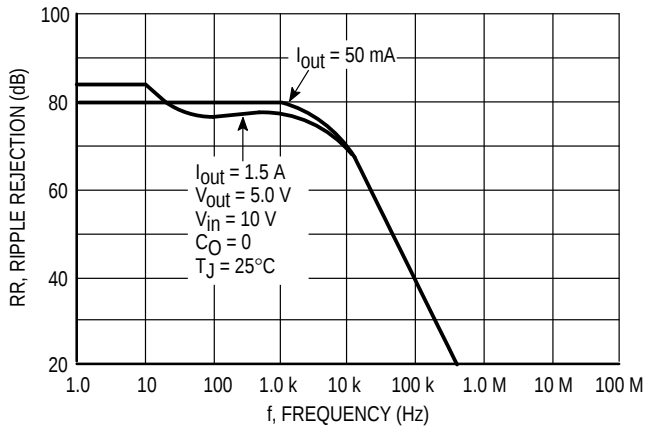


Figure 6. Ripple Rejection versus Output Current

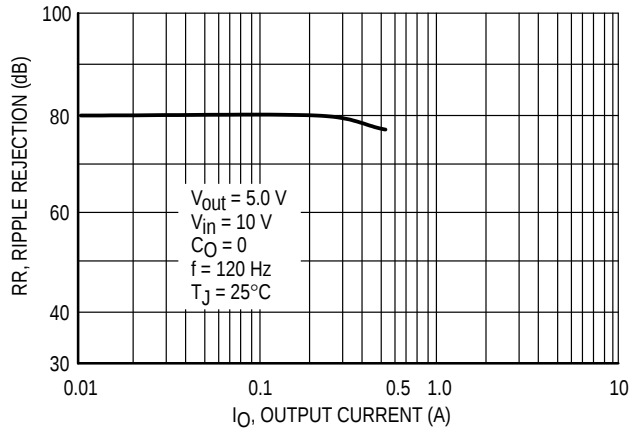


Figure 7. Bias Current versus Input Voltage

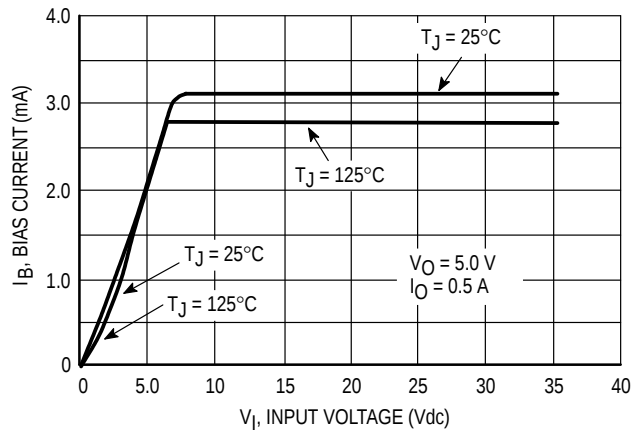
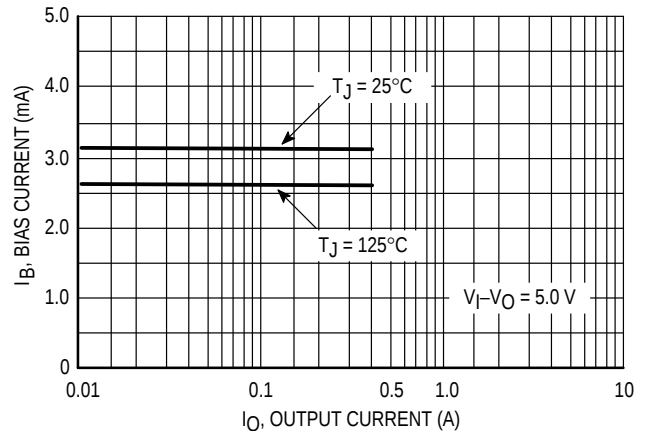


Figure 8. Bias Current versus Output Current



APPLICATIONS INFORMATION

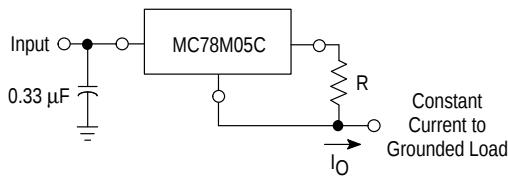
Design Considerations

The MC78M00 Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition, Internal Short Circuit Protection that limits the maximum current the circuit will pass, and Output Transistor Safe-Area Compensation that reduces the output short circuit current as the voltage across the pass transistor is increased.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the

regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high frequency characteristics to insure stable operation under all load conditions. A 0.33 μF or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulator's input terminals. Normally good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead.

Figure 9. Current Regulator



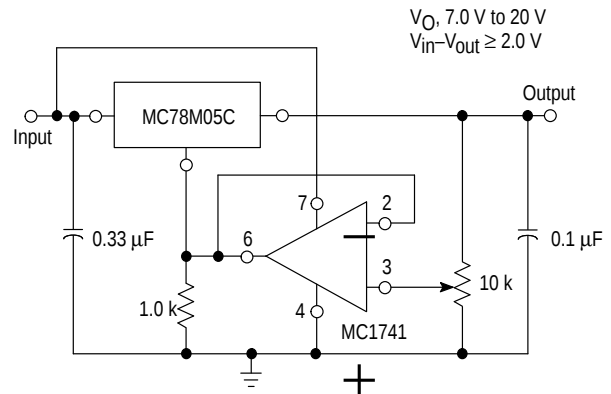
The MC78M00 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC78M05C is chosen in this application. Resistor R determines the current as follows:

$$I_O = \frac{5.0 \text{ V}}{R} + I_{IB}$$

$$I_{IB} = 1.5 \text{ mA over line and load changes.}$$

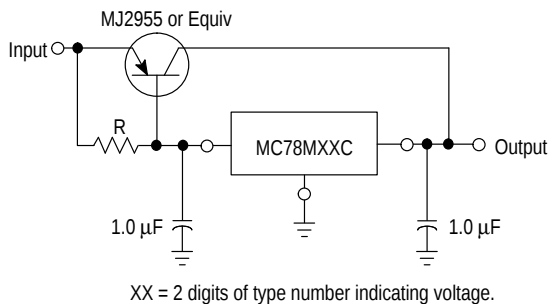
For example, a 500 mA current source would require R to be a 5.0 Ω , 10 W resistor and the output voltage compliance would be the input voltage less 7 V.

Figure 10. Adjustable Output Regulator



The addition of an operational amplifier allows adjustment to higher or intermediate values while retaining regulation characteristics. The minimum voltage obtainable with this arrangement is 2.0 V greater than the regulator voltage.

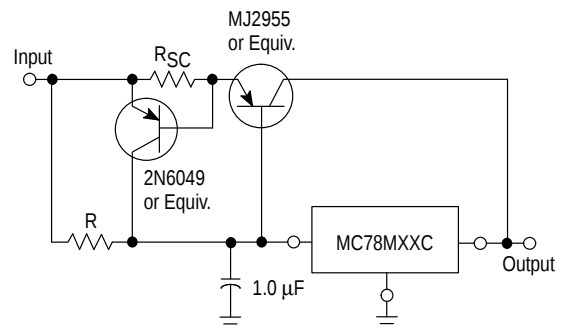
Figure 11. Current Boost Regulator



XX = 2 digits of type number indicating voltage.

The MC78M00 series can be current boosted with a PNP transistor. The MJ2955 provides current to 5.0 A. Resistor R in conjunction with the V_{BE} of the PNP determines when the pass transistor begins conducting; this circuit is not short circuit proof. Input-output differential voltage minimum is increased by V_{BE} of the pass transistor.

Figure 12. Current Boost with Short Circuit Protection



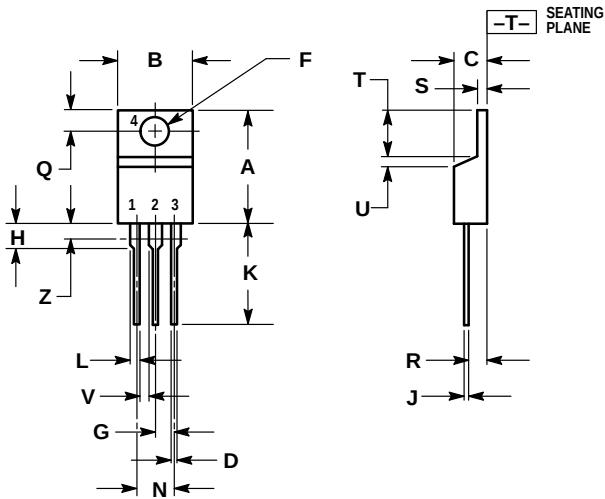
XX = 2 digits of type number indicating voltage.

The circuit of Figure 10 can be modified to provide supply protection against short circuits by adding a short circuit sense resistor, R_{SC} , and an additional PNP transistor. The current sensing PNP must be able to handle the short circuit current of the three-terminal regulator. Therefore, a 4 A plastic power transistor is specified.

MC78M00 Series

OUTLINE DIMENSIONS

T SUFFIX PLASTIC PACKAGE CASE 221A-06 (TO-220)

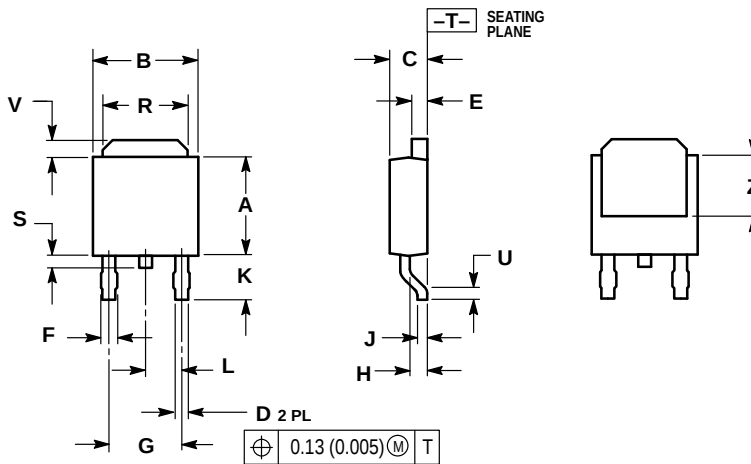


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
H	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	—	1.15	—
Z	—	0.080	—	2.04

DT SUFFIX PLASTIC PACKAGE CASE 369A-13 (DPAK)



NOTES:

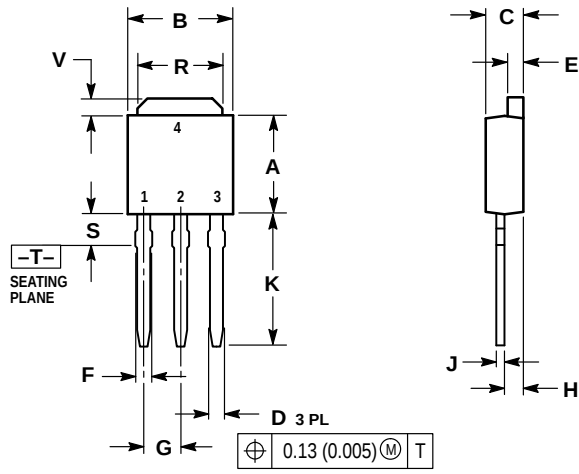
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.235	0.250	5.97	6.35
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.033	0.040	0.84	1.01
F	0.037	0.047	0.94	1.19
G	0.180	BSC	4.58	BSC
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090	BSC	2.29	BSC
R	0.175	0.215	4.45	5.46
S	0.020	0.050	0.51	1.27
U	0.020	—	0.51	—
V	0.030	0.050	0.77	1.27
Z	0.138	—	3.51	—

MC78M00 Series

OUTLINE DIMENSIONS

DT-1 SUFFIX
PLASTIC PACKAGE
CASE 369-07
(DPAK)



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.235	0.250	5.97	6.35
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.033	0.040	0.84	1.01
F	0.037	0.047	0.94	1.19
G	0.090 BSC		2.29 BSC	
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.350	0.380	8.89	9.65
R	0.175	0.215	4.45	5.46
S	0.050	0.090	1.27	2.28
V	0.030	0.050	0.77	1.27

MC78M00 Series

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