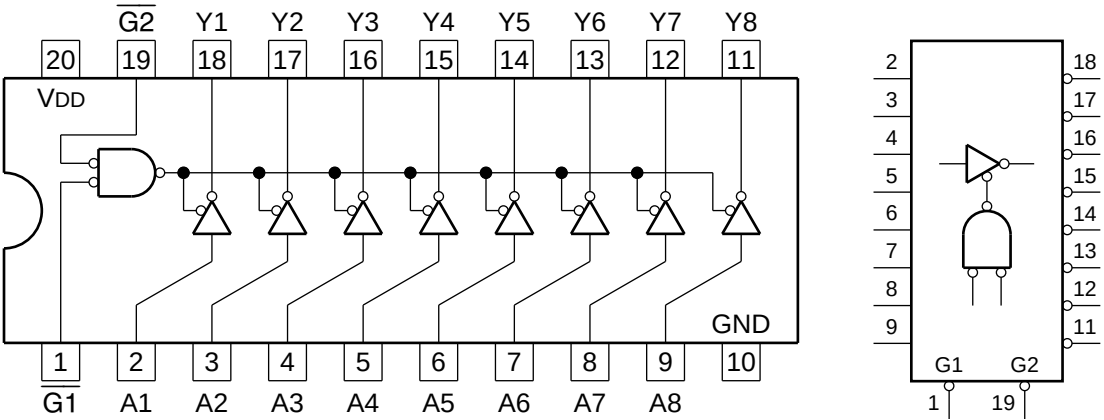


C-MOS 3-STATE OCTAL INVERTING BUFFERS/DRIVERS

—TOP VIEW—



Logic symbol for the 3-state octal inverting buffer. The symbol shows an AND gate with inputs G1 and G2, and an inverter with input A and output Y.

G1	G2	A	Y
0	0	0	1
0	0	1	0
1	x	x	HI-Z
x	1	x	HI-Z

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE