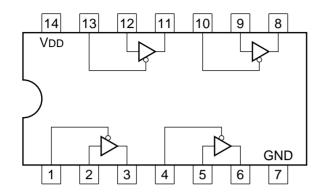
## C-MOS BUS BUFFER GATES WITH 3-STATE OUTPUT —TOP VIEW—





G	Α	Υ
0	0	0
0	1	1
1	×	HI-Z

0 : LOW LEVEL 1 : HIGH LEVEL × : DON'T CARE

HI-Z: HIGH IMPEDANCE