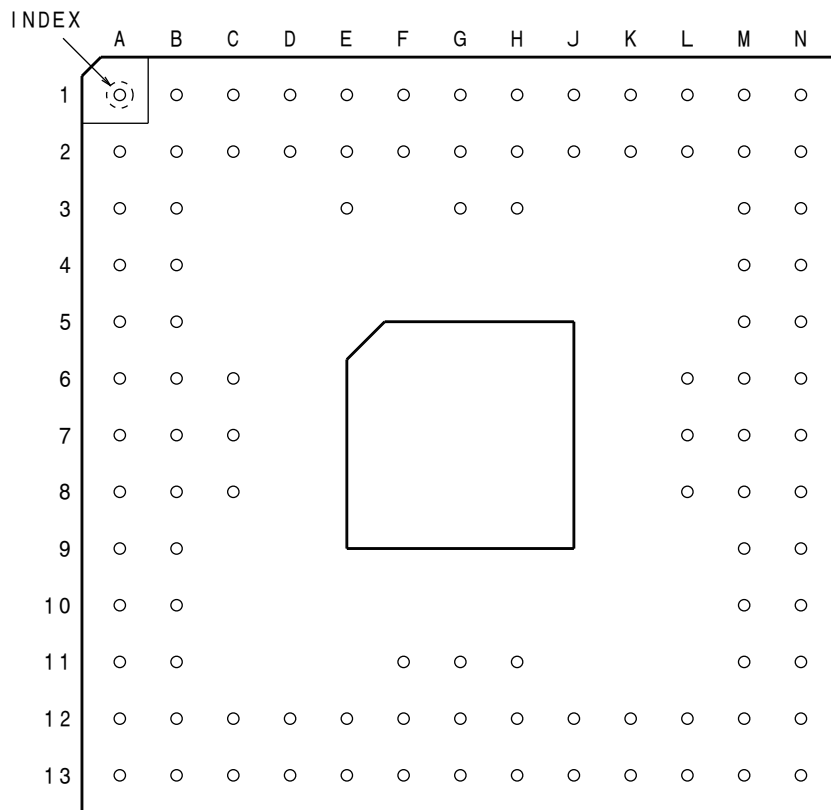


C-MOS 32-BIT MICROPROCESSOR
-BOTTOM VIEW-



$V_{CC} = -0.3 \text{ to } +7\text{V}$

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1A	–	GND	3A	0	A1	7N	I/O	D19	12E	–	V _{CC}
1B	–	GND	3B	0	A0	8A	0	A15	12F	–	GND
1C	I	CLK	3E	0	FC0	8B	0	A16	12G	I	$\overline{\text{IPL0}}$
1D	–	V _{CC}	3G	I	$\overline{\text{AVEC}}$	8C	–	GND	12H	I/O	D2
1E	0	$\overline{\text{RMC}}$	3H	I	$\overline{\text{BERR}}$	8L	–	GND	12J	–	D4
1F	0	FC2	3M	I/O	D27	8M	–	GND	12K	I/O	V _{CC}
1G	0	SIZ1	3N	I/O	D28	8N	I/O	D16	12L	I/O	D6
1H	I	$\overline{\text{DSACK0}}$	4A	0	A22	9A	0	A13	12M	I/O	D9
1J	–	GND	4B	0	A23	9B	0	A14	12N	I/O	D11
1K	I/O	$\overline{\text{HALT}}$	4M	I/O	D25	9M	–	V _{CC}	13A	0	A6
1L	0	$\overline{\text{DS}}$	4N	I/O	D26	9N	–	V _{CC}	13B	0	A5
1M	–	GND	5A	0	A20	10A	0	A11	13C	0	A3
1N	I/O	D31	5B	0	A21	10B	0	A12	13D	–	GND
2A	I	$\overline{\text{BR}}$	5M	I/O	D23	10M	I/O	D14	13E	–	V _{CC}
2B	0	BG	5N	I/O	D24	10N	I/O	D15	13F	I	$\overline{\text{IPL2}}$
2C	I/O	$\overline{\text{RESET}}$	6A	0	A17	11A	0	A9	13G	I/O	D0
2D	–	V _{CC}	6B	0	A18	11B	0	A10	13H	I/O	D3
2E	0	FC1	6C	0	A19	11F	–	GND	13J	–	GND
2F	0	SIZ0	6L	I/O	D20	11G	I	$\overline{\text{IPL1}}$	13K	I/O	D5
2G	I	$\overline{\text{CDIS}}$	6M	I/O	D21	11H	I/O	D1	13L	I/O	D7
2H	I	$\overline{\text{DSACK1}}$	6N	I/O	D22	11M	I/O	D12	13M	I/O	D8
2J	–	GND	7A	–	GND	11N	I/O	D13	13N	I/O	D10
2K	0	$\overline{\text{AS}}$	7B	–	V _{CC}	12A	0	A8			
2L	0	R/ $\overline{\text{W}}$	7C	–	V _{CC}	12B	0	A7			
2M	I/O	D30	7L	I/O	D17	12C	0	A4			
2N	I/O	D29	7M	I/O	D18	12D	0	A2			

