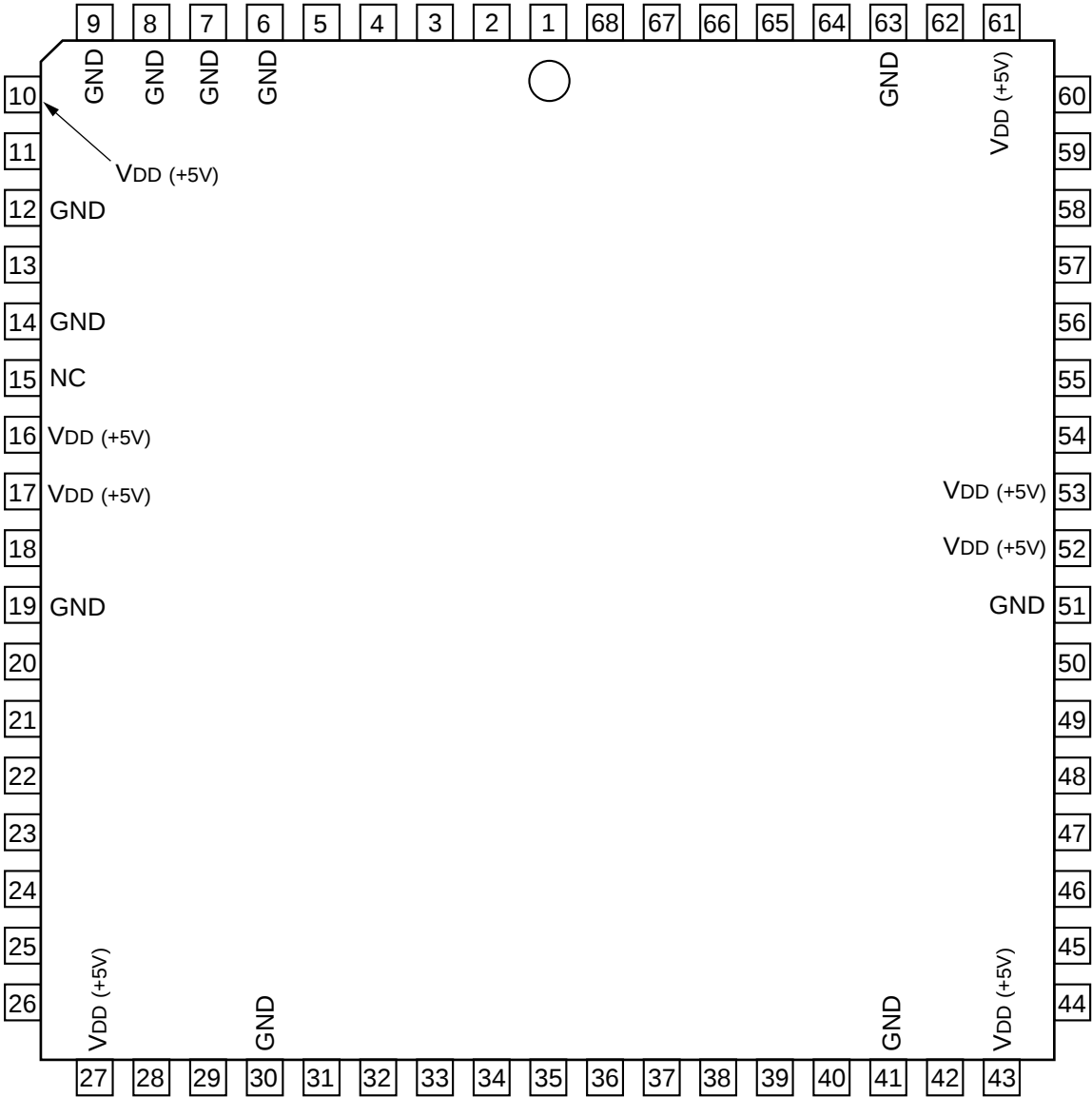
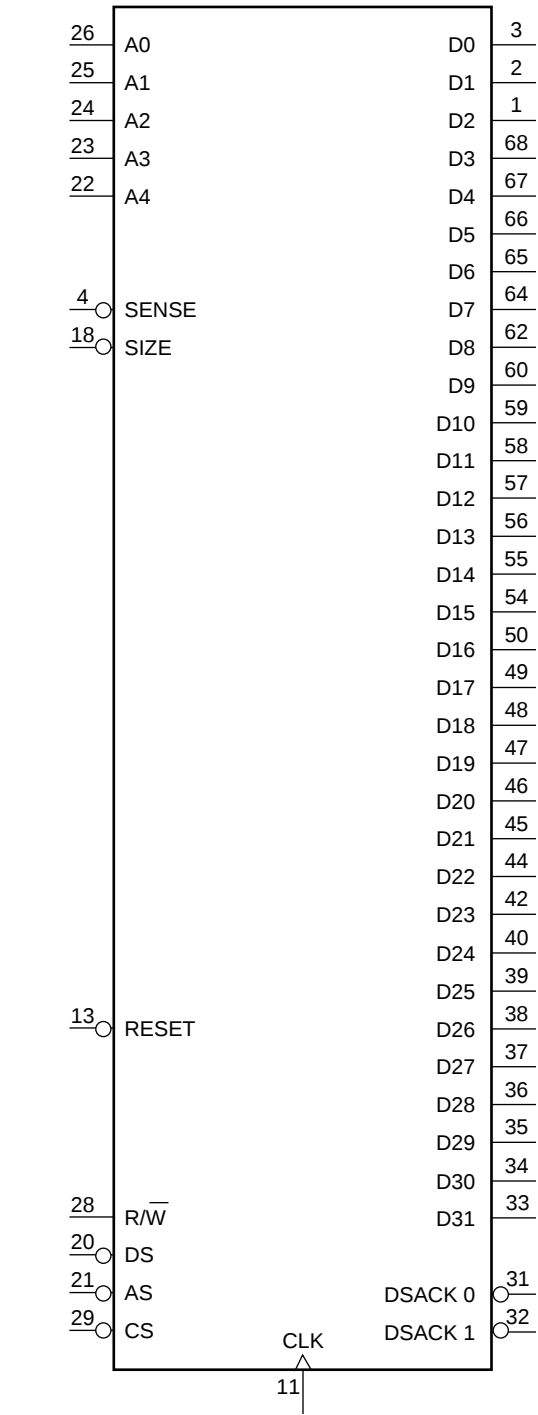

C-MOS FLOATING-POINT COPROCESSOR
—TOP VIEW—



(VDD =+5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	D2	35	I/O	D29
2	I/O	D1	36	I/O	D28
3	I/O	D0	37	I/O	D27
4	I	$\overline{\text{SENSE}}$	38	I/O	D26
5	—	GND	39	I/O	D25
6	—	GND	40	I/O	D24
7	—	GND	41	—	GND
8	—	GND	42	I/O	D23
9	—	GND	43	—	VDD
10	—	VDD	44	I/O	D22
11	I	CLK	45	I/O	D21
12	—	GND	46	I/O	D20
13	I	$\overline{\text{RESET}}$	47	I/O	D19
14	—	GND	48	I/O	D18
15	—	NC	49	I/O	D17
16	—	VDD	50	I/O	D16
17	—	VDD	51	—	GND
18	I	$\overline{\text{SIZE}}$	52	—	VDD
19	—	GND	53	—	VDD
20	I	$\overline{\text{DS}}$	54	I/O	D15
21	I	$\overline{\text{AS}}$	55	I/O	D14
22	I	A4	56	I/O	D13
23	I	A3	57	I/O	D12
24	I	A2	58	I/O	D11
25	I	A1	59	I/O	D10
26	I	A0	60	I/O	D9
27	—	VDD	61	—	VDD
28	I	$\overline{\text{R/W}}$	62	I/O	D8
29	I	$\overline{\text{CS}}$	63	—	GND
30	—	GND	64	I/O	D7
31	O	$\overline{\text{DSACK0}}$	65	I/O	D6
32	O	$\overline{\text{DSACK1}}$	66	I/O	D5
33	I/O	D31	67	I/O	D4
34	I/O	D30	68	I/O	D3



INPUT
A0- A4 ; ADDRESS
 $\overline{\text{AS}}$; ADDRESS STROBE
CLK ; CLOCK
 $\overline{\text{CS}}$; CHIP SELECT
 $\overline{\text{DS}}$; DATA STROBE
 $\overline{\text{R/W}}$; READ/WRITE
 $\overline{\text{RESET}}$; RESET
 $\overline{\text{SENSE}}$; SENSE DEVICE
SIZE ; SIZE

OUTPUT
 $\overline{\text{DSACK0}}$, ; DATA TRANSFER AND SIZE
 $\overline{\text{DSACK1}}$; ACKNOWLEDGE

INPUT/OUTPUT
D0-D31 ; DATA INPUTS/OUTPUTS

