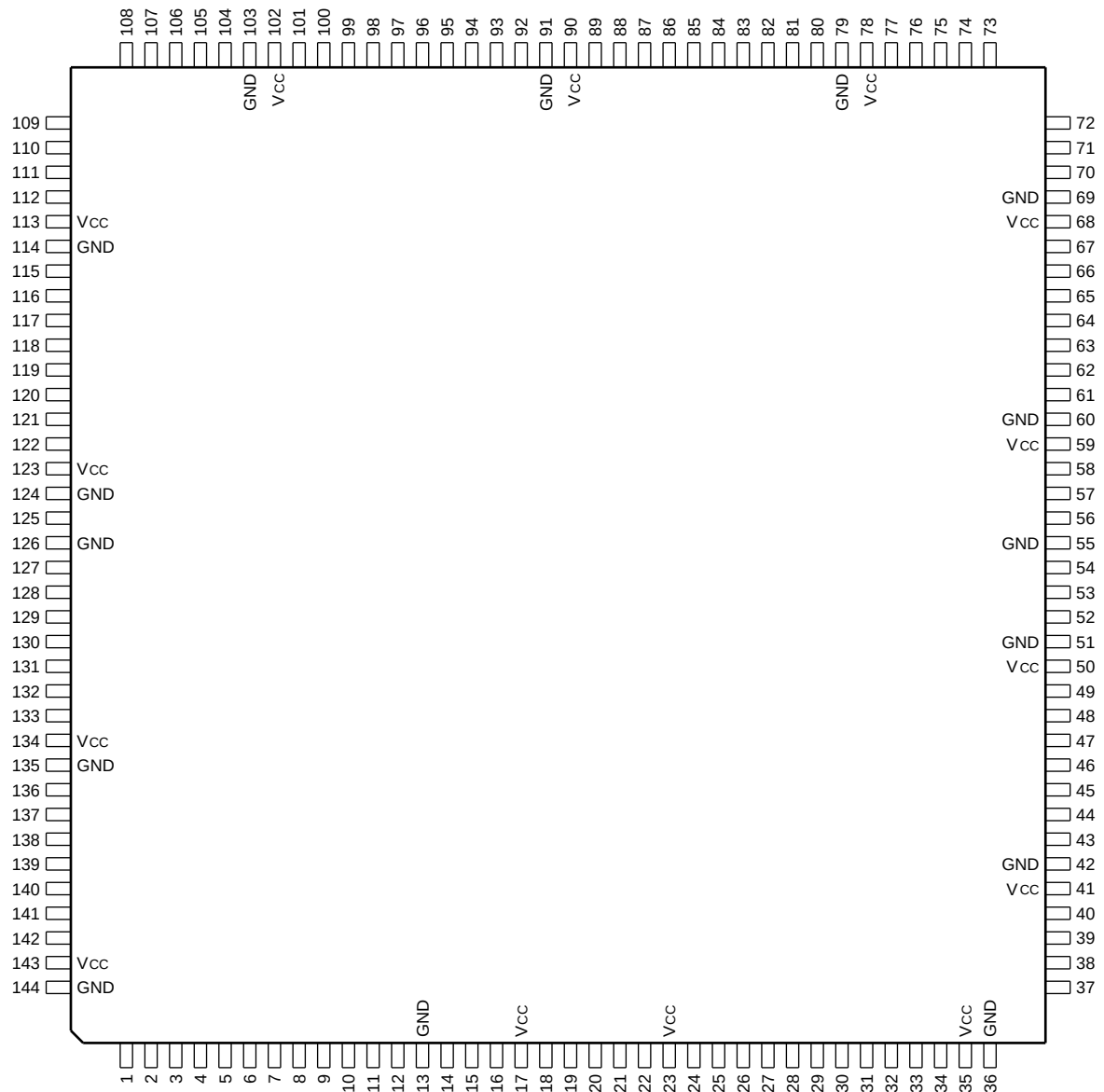

32-BIT MICROPROCESSOR

- TOP VIEW -



PIN No.	I/O	SIGNAL	SIGNAL NAME	PIN No.	I/O	SIGNAL	SIGNAL NAME	PIN No.	I/O	SIGNAL	SIGNAL NAME	PIN No.	I/O	SIGNAL	SIGNAL NAME
1	O	RMC	READ-MODIFY-WRITE CYCLE	41	–	Vcc		81	I	CTSA	CLEAR-TO-SEND	121	I/O	D10	DATA BUS
2	O	R/W	READ/WRITE	42	–	GND		82	O	RxRDYA	RECEIVER READY/FIFO FULL/OP0	122	I/O	D11	DATA BUS
3	O	SIZ1	SIZE	43	O	A23	ADDRESS BUS	83	O	TxRDYA	TRANSMITTER READY/OP6	123	–	Vcc	
4	O	SIZ0	SIZE	44	O	A22	ADDRESS BUS	84	I	RxDB	RECEIVE DATA	124	–	GND	
5	O	DS	DATA STROBE	45	O	A21	ADDRESS BUS	85	O	TxDB	TRANSMIT DATA	125	I/O	D12	DATA BUS
6	O	AS	ADDRESS STROBE	46	O	A20	ADDRESS BUS	86	O	RTSB	REQUEST-TO-SEND/OP1,OP0	126	–	GND	
7	I	BGACK	BUS GRANT ACKNOWLEDGE	47	O	A19	ADDRESS BUS	87	I	CTSB	CLEAR-TO-SEND	127	I/O	D13	DATA BUS
8	O	BG	BAS GRANT	48	O	A18	ADDRESS BUS	88	I	SCLK	SERIAL CLOCK	128	I/O	D14	DATA BUS
9	I	BR	BUS REQUEST	49	O	A17	ADDRESS BUS	89	O	X2	SERIAL CRYSTAL OSCILLATOR	129	I/O	D15	DATA BUS
10	I	BERR	BUS ERROR	50	–	Vcc		90	–	Vcc		130	I/O	A24	ADDRESS BUS/PORT A0
11	I/O	HALT	HALT	51	–	GND		91	–	GND		131	I/O	A25	ADDRESS BUS/PORT A1
12	I/O	RESET	RESET	52	O	A16	ADDRESS BUS	92	I	X1	SERIAL CRYSTAL OSCILLATOR	132	I/O	A26	ADDRESS BUS/PORT A2
13	–	GND		53	O	A15	ADDRESS BUS	93	I	DREQ1	DMA REQUEST	133	I/O	A27	ADDRESS BUS/PORT A3
14	O	CLKOUT	SYSTEM CLOCK	54	O	A14	ADDRESS BUS	94	O	DACK1	DMA ACKNOWLEDGE	134	–	Vcc	
15	–	Vcc		55	–	GND		95	I/O	DONE1	DMA DONE	135	–	GND	
16	I	XFC	EXTERNAL FILTER CAPACITOR	56	O	A13	ADDRESS BUS	96	I	DREQ2	DMA REQUEST	136	I/O	A28	ADDRESS BUS/PORT A4
17	–	Vcc		57	O	A12	ADDRESS BUS	97	O	DACK2	DMA ACKNOWLEDGE	137	I/O	A29	ADDRESS BUS/PORT A5
18	I	EXTAL	CRYSTAL OSCILLATOR	58	O	A11	ADDRESS BUS	98	I/O	DONE2	DMA DONE	138	I/O	A30	ADDRESS BUS/PORT A6
19	–	Vcc SYN	SYNCHRONIZER POWER	59	–	Vcc		99	I	IRQ7	INTERRUPT REQUEST LEVEL/ PORT B7,B6,B5,B3	139	I/O	A31	ADDRESS BUS/PORT A7
20	O	XTAL	CRYSTAL OSCILLATOR	60	–	GND		100	I	IRQ6	INTERRUPT REQUEST LEVEL/ PORT B7,B6,B5,B3	140	O	A0	ADDRESS BUS
21	–	GND		61	O	A10	ADDRESS BUS	101	I	IRQ5	INTERRUPT REQUEST LEVEL/ PORT B7,B6,B5,B3	141	I	DSACK0	DATA AND SIZE ACKNOWLEDGE
22	I	MODCK	CLOCK MODE SELECT/PORT B0	62	O	A9	ADDRESS BUS	102	–	Vcc		142	I	DSACK1	DATA AND SIZE ACKNOWLEDGE
23	–	Vcc		63	O	A8	ADDRESS BUS	103	–	GND		143	–	Vcc	
24	O	IPIPE	INSTRUCTION PIPE/ DEVELOPMENT SERIAL OUT	64	O	A7	ADDRESS BUS	104	O	CS3	CHIP SELECT 3-1/ INTERRUPT REQUEST LEVEL/PORT B4,B2,B1	144	–	GND	
25	I/O	IFETCH	INSTRUCTION FETCH/ DEVELOPMENT SERIAL IN	65	O	A6	ADDRESS BUS	105	I	IRQ3	INTERRUPT REQUEST LEVEL/ PORT B7,B6,B5,B3				
26	I	BKPT	BREAKPOINT DEVELOPMENT SERIAL CLOCK	66	O	A5	ADDRESS BUS	106	O	CS2	CHIP SELECT 2-1/ INTERRUPT REQUEST LEVEL/PORT B4,B2,B1				
27	O	FREEZE	FREEZE	67	O	A4	ADDRESS BUS	107	O	CS1	CHIP SELECT 1-1/ INTERRUPT REQUEST LEVEL/PORT B4,B2,B1				
28	I	TIN1	TIMER INPUT	68	–	Vcc		108	O	CS0	CHIP SELECT 0/AUTOVECTOR				
29	O	TOUT1	TIMER OUTPUT	69	–	GND		109	I/O	D0	DATA BUS				
30	I	TGATE1	TIMER GATE	70	O	A3	ADDRESS BUS	110	I/O	D1	DATA BUS				
31	I	TCK	TEST CLOCK	71	O	A2	ADDRESS BUS	111	I/O	D2	DATA BUS				
32	I	TMS	TEST MODE SELECT	72	O	A1	ADDRESS BUS	112	I/O	D3	DATA BUS				
33	I	TDI	TEST DATA IN	73	I	TGATE2	TIMER GATE	113	–	Vcc					
34	O	TDO	TEST DATA OUT	74	O	TOUT2	TIMER OUTPUT	114	–	GND					
35	–	Vcc		75	I	TIN2	TIMER INPUT	115	I/O	D4	DATA BUS				
36	–	GND		76	I	RxDA	RECEIVE DATA	116	I/O	D5	DATA BUS				
37	O	FC3	FUNCTION CODE	77	O	TxDA	TRANSMIT DATA	117	I/O	D6	DATA BUS				
38	O	FC2	FUNCTION CODE	78	–	Vcc		118	I/O	D7	DATA BUS				
39	O	FC1	FUNCTION CODE	79	–	GND		119	I/O	D8	DATA BUS				
40	O	FC0	FUNCTION CODE	80	O	RTSA	REQUEST-TO-SEND/OP1,OP0	120	I/O	D9	DATA BUS				

7	<u>BACK</u>	140
9	BR	A0 72
10	BERR	A1 71
11	HALT	A2 70
12	<u>RESET</u>	A3 69
		A4 68
16	XFC	A5 65
18	EXTAL	A6 64
22	MODCK	A7 63
26	<u>BKPT</u>	A8 62
88	SCLK	A9 61
30	<u>TGATE1</u>	A10 58
73	<u>TGATE2</u>	A11 57
		A12 56
31	TCK	A13 54
32	TMS	A14 53
33	TDI	A15 52
		A16 49
28	TIN1	A17 48
75	TIN2	A18 47
		A19 46
76	RxDA	A20 45
84	RxDB	A21 44
		A22 43
81	<u>CTSA</u>	A23 42
87	<u>CTSB</u>	
		PORTA0 130
93	<u>DREQ1</u>	PORTA1 131
96	<u>DREQ2</u>	PORTA2 132
		PORTA3 133
95	<u>DONE1</u>	PORTA4 136
98	<u>DONE2</u>	PORTA5 137
		PORTA6 138
		PORTA7 139
105	<u>IRQ3</u>	
101	<u>IRQ5</u>	
100	<u>IRQ6</u>	82
99	<u>IRQ7</u>	<u>R&RDYA</u> 83
		<u>T&RDYA</u>
108	<u>CS0</u>	89
107	<u>CS1</u>	X2
106	<u>CS2</u>	
104	<u>CS3</u>	94
		<u>DACK1</u> 97
		<u>DACK2</u>
141	<u>DSACK0</u>	37
142	<u>DSACK1</u>	FC3 38
		FC2 39
109	D0	FC1 40
110	D1	FC0
111	D2	
112	D3	<u>RMC</u> 1
115	D4	R/W 2
116	D5	SI21 3
117	D6	SI21 4
118	D7	SI20 5
119	D8	<u>DS</u> 6
120	D9	AS 6
121	D10	BG 8
122	D11	
125	D12	CLK OUT 14
127	D13	XTAL 20
128	D14	<u>PIPE</u> 24
129	D15	<u>IFETCH</u> 25
		FREEZE 27
		TOUT1 29
92	X1	TOUT2 74
		TDO 34
		<u>TXDA</u> 77
		TXDB 85
		<u>RTSA</u> 80
		<u>RTSB</u> 86

