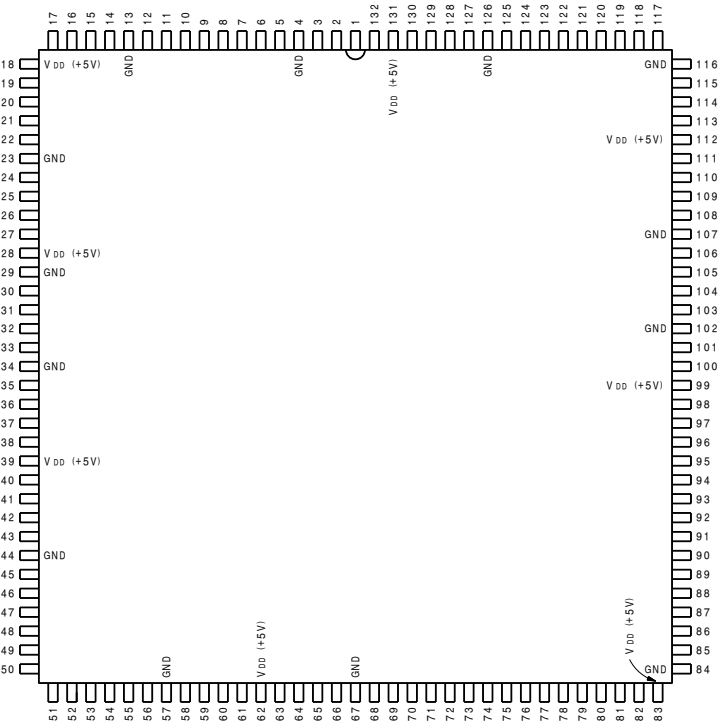
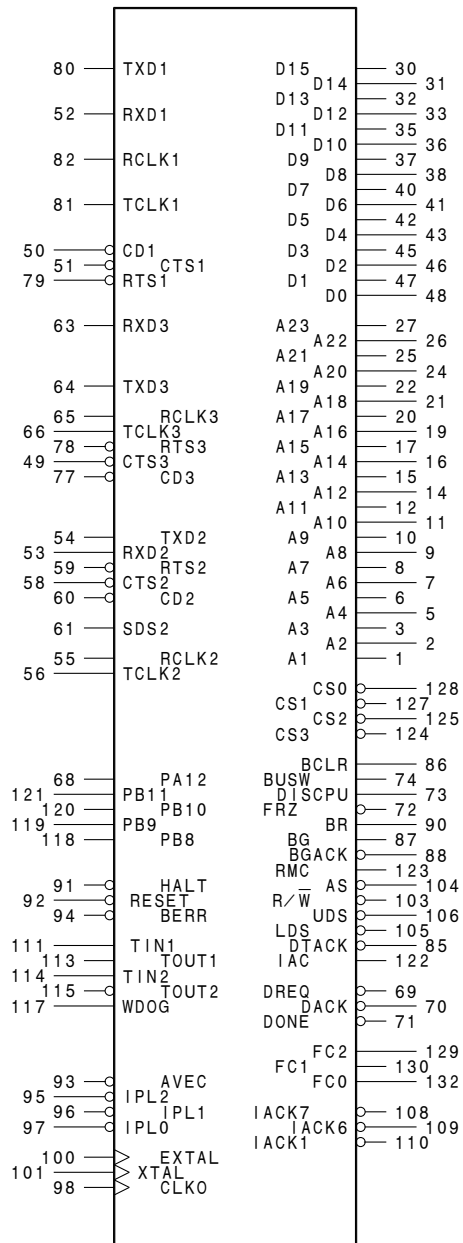

C-MOS INTEGRATED MULTIPROTOCOL PROCESSOR
-TOP VIEW-



PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL	PIN NO.	I/O	SYMBOL
1	I/O	A1	45	I/O	D3	89	-	NC1
2	I/O	A2	46	I/O	D2	90	I/O	BR
3	I/O	A3	47	I/O	D1	91	I/O	HALT
4	-	GND	48	I/O	D0	92	I/O	RESET
5	I/O	A4	49	I	CTS3	93	I/O	AVEC
6	I/O	A5	50	I	CD1	94	I/O	BERR
7	I/O	A6	51	I	CTS1	95	I	IPL2
8	I/O	A7	52	I	RXD1	96	I	IPL1
9	I/O	A8	53	I/O	RXD2	97	I	IPL0
10	I/O	A9	54	I/O	TXD2	98	0	CLK0
11	I/O	A10	55	I/O	RCLK2	99	-	VDD (+5V)
12	I/O	A11	56	I/O	TCLK2	100	I	EXTAL
13	-	GND	57	-	GND	101	0	XTAL
14	I/O	A12	58	I/O	CTS2	102	-	GND
15	I/O	A13	59	I/O	RTS2	103	I/O	R/W
16	I/O	A14	60	I/O	CD2	104	I/O	AS
17	I/O	A15	61	I/O	SDS2	105	I/O	LDS
18	-	VDD (+5V)	62	-	VDD (+5V)	106	I/O	UDS
19	I/O	A16	63	I/O	RXD3	107	-	GND
20	I/O	A17	64	I/O	TXD3	108	I/O	TACK7
21	I/O	A18	65	I/O	RCLK3	109	I/O	TACK6
22	I/O	A19	66	I/O	TCLK3	110	I/O	TACK1
23	-	GND	67	-	GND	111	I/O	TIN1
24	I/O	A20	68	I/O	PA12	112	-	VDD (+5V)
25	I/O	A21	69	I/O	DREQ	113	I/O	TOUT1
26	I/O	A22	70	I/O	DACK	114	I/O	TIN2
27	I/O	A23	71	I/O	DONE	115	I/O	TOUT2
28	-	VDD (+5V)	72	I	FRZ	116	-	GND
29	-	GND	73	I	DISCPU	117	I/O	WDOG
30	I/O	D15	74	I	BUSW	118	I/O	PB8
31	I/O	D14	75	-	NC3	119	I/O	PB9
32	I/O	D13	76	-	NC2	120	I/O	PB10
33	I/O	D12	77	I/O	CD3	121	I/O	PB11
34	-	GND	78	0	RTS3	122	0	IAC
35	I/O	D11	79	0	RTS1	123	0	RMC
36	I/O	D10	80	0	TXD1	124	0	CS3
37	I/O	D9	81	I/O	TCLK1	125	0	CS2
38	I/O	D8	82	I/O	RCLK1	126	-	GND
39	-	VDD (+5V)	83	-	VDD (+5V)	127	0	CS1
40	I/O	D7	84	-	GND	128	0	CS0
41	I/O	D6	85	I/O	DTACK	129	I/O	FC2
42	I/O	D5	86	I/O	BCLR	130	I/O	FC1
43	I/O	D4	87	I/O	BG	131	-	VDD (+5V)
44	-	GND	88	I/O	BGACK	132	I/O	FC0



INPUT

BUSW	:BUS WIDTH SELECT
CD1	:CARRIER DETECT 1
CTS1 , CTS3	:CLEAR-TO-SEND 1-3
DISCPU	:DISABLE CPU
EXTAL	:EXTERNAL CLOCK/CRYSTAL INPUT
FRZ	:FREEZE ACTIVITY
IPL0 - IPL2	:INTERRUPT CONTROL
RXD1	:RECEIVE DATA

OUTPUT

CLKO	:CLOCK OUT
CS0 - CS3	:CHIP SELECT 0-3
IAC	:INTERNAL ACCESS
RMC	:READ-MODIFY-WRITE
RTS1 , RTS3	:REQUFST-TO-SEND 1, 3
TXD1	:TRANSMIT DATA
XTAL	:CRYSTAL OUTPUT

INPUT/OUTPUT

A1-A23	:ADDRESS BUS
AS	:ADDRESS STROBE
AVEC	:AUTO VECTOR
BCLR	:BUS CLEAR
BERR	:BUS ERROR
BG	:BUS GRANT
BGACK	:BUS GRANT ACKNOWLEDGE
BR	:BUS REQUEST
CD2 , CD3	:CARRIER DETECT 2, 3
CTS2	:CLEAR-TO-SEND 2
D0-D15	:DATA BUS
DACK	:DMA ACKNOWLEDGE
DONE	:DONE
DREQ	:DMA REQUEST
DTACK	:DATA TRANSFER ACKNOWLEDGE
FC0-FC2	:FUNCTION CODES 0-2
HALT	:HALT
IACK1 , IACK6 , IACK7	:INTERRUPT ACKNOWLEDGE 1, 6, 7
LDS	:LOWER DATA STROBE
PA12	:BIT 12 OF PORT A
PB8-PB11	:PARALLEL INPUT/OUTPUT
RCLK1-RCLK3	:RECEIVE CLOCK 1-3
RESET	:RESET
R/W	:READ/WRITE
RXD2, RXD3	:RECEIVE DATA
RTS2	:REQUEST-TO-SEND 2
SDS2	:SERIAL DATA STROBE 2
TCLK1-TCLK3	:TRANSMIT CLOCK
TIN1, TIN2	:TIMER 1, 2 INPUT
TOUT1 , TOUT2	:TIMER 1, 2 OUTPUT
TXD2, TXD3	:TRANSMIT 2, 3 DATA
UDS	:UPPER DATA STROBE
WDOG	:WATCH DOG OUTPUT