



MC4558AC MC4558C

Dual Wide Bandwidth Operational Amplifiers

The MC4558AC, C combine all the outstanding features of the MC1458 and, in addition offer three times the unity gain bandwidth of the industry standard.

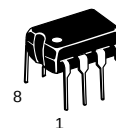
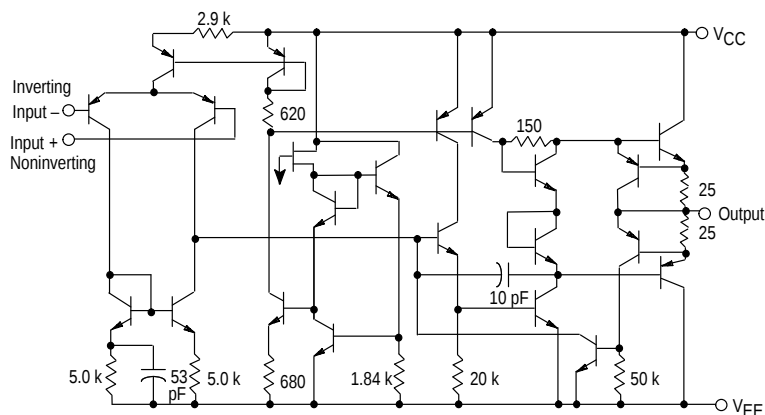
- 2.5 MHz Unity Gain Bandwidth Guaranteed (MC4558AC)
- 2.0 MHz Unity Gain Bandwidth Guaranteed (MC4558C)
- Internally Compensated
- Short Circuit Protection
- Gain and Phase Match between Amplifiers
- Low Power Consumption

MAXIMUM RATINGS ($T_A = +25^\circ\text{C}$, unless otherwise noted.)

Rating	Symbol	MC4558AC	MC4558C	Unit
Power Supply Voltage	V_{CC} V_{EE}	+22 -22	+18 -18	Vdc
Input Differential Voltage	V_{ID}	± 30		V
Input Common Mode Voltage (Note 1)	V_{ICM}	± 15		V
Output Short Circuit Duration (Note 2)	t_{SC}	Continuous		
Ambient Temperature Range	T_A	0 to $+70$		$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to $+125$		$^\circ\text{C}$
Junction Temperature	T_J	150		$^\circ\text{C}$

NOTES: 1. For supply voltages less than ± 15 V, the absolute maximum input voltage is equal to the supply voltage.
2. Short circuit may be to ground or either supply.

Representative Schematic Diagram
(1/2 of Circuit Shown)

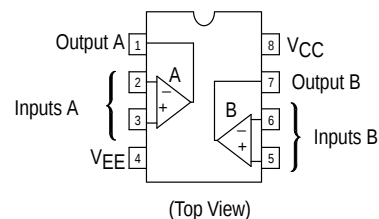


P1 SUFFIX
PLASTIC PACKAGE
CASE 626



D SUFFIX
PLASTIC PACKAGE
CASE 751
(SO-8)

PIN CONNECTIONS



ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC4558CD	$T_A = 0^\circ$ to $+70^\circ\text{C}$	SO-8
MC4558ACP1,CP1		Plastic DIP

MC4558AC MC4558C

FREQUENCY CHARACTERISTICS ($V_{CC} = +15\text{ V}$, $V_{EE} = -15\text{ V}$, $T_A = 25^\circ\text{C}$)

Characteristic	Symbol	MC4558AC			MC4558C			Unit
		Min	Typ	Max	Min	Typ	Max	
Unity Gain Bandwidth	BW	2.5	2.8	–	2.0	2.8	–	MHz

ELECTRICAL CHARACTERISTICS ($V_{CC} = 15\text{ V}$, $V_{EE} = -15\text{ V}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

Input Offset Voltage ($R_S \leq 10\text{ k}\Omega$)	V_{IO}	–	1.0	5.0	–	2.0	6.0	mV
Input Offset Current	I_{IO}	–	20	200	–	20	200	nA
Input Bias Current (Note 1)	I_{IB}	–	80	500	–	80	500	nA
Input Resistance	r_i	0.3	2.0	–	0.3	2.0	–	M Ω
Input Capacitance	C_i	–	1.4	–	–	1.4	–	pF
Common Mode Input Voltage Range	V_{ICR}	± 12	± 13	–	± 12	± 13	–	V
Large Signal Voltage Gain ($V_O = \pm 10\text{ V}$, $R_L = 2.0\text{ k}\Omega$)	A_{VOL}	50	200	–	20	200	–	V/mV
Output Resistance	r_o	–	75	–	–	75	–	Ω
Common Mode Rejection ($R_S \leq 10\text{ k}\Omega$)	CMR	70	90	–	70	90	–	dB
Supply Voltage Rejection Ratio ($R_S \leq 10\text{ k}\Omega$)	PSRR	–	30	150	–	30	150	$\mu\text{V/V}$
Output Voltage Swing ($R_L \geq 10\text{ k}\Omega$) ($R_L \geq 2.0\text{ k}\Omega$)	V_O	± 12 ± 10	± 14 ± 13	– –	± 12 ± 10	± 14 ± 13	– –	V
Output Short Circuit Current	I_{SC}	10	20	40	10	20	40	mA
Supply Currents (Both Amplifiers)	I_D	–	2.3	5.0	–	2.3	5.6	mA
Power Consumption (Both Amplifiers)	P_C	–	70	150	–	70	170	mW
Transient Response (Unity Gain) ($V_i = 20\text{ mV}$, $R_L \geq 2.0\text{ k}\Omega$, $C_L \leq 100\text{ pF}$) Rise Time ($V_i = 20\text{ mV}$, $R_L \geq 2.0\text{ k}\Omega$, $C_L \leq 100\text{ pF}$) Overshoot ($V_i = 10\text{ V}$, $R_L \geq 2.0\text{ k}\Omega$, $C_L \leq 100\text{ pF}$) Slew Rate	t_{TLH} t_{os} SR	– – 1.5	0.3 15 1.6	– – –	– – 1.0	0.3 15 1.6	– – –	μs % V/ μs

ELECTRICAL CHARACTERISTICS ($V_{CC} = +15\text{ V}$, $V_{EE} = -15\text{ V}$, $T_A = T_{high}$ to T_{low} , unless otherwise noted. See Note 2.)

Input Offset Voltage ($R_S \leq 10\text{ k}\Omega$)	V_{IO}	–	1.0	6.0	–	–	7.5	mV
Input Offset Current ($T_A = T_{high}$) ($T_A = T_{low}$) ($T_A = 0^\circ$ to $+70^\circ\text{C}$)	I_{IO}	– – –	7.0 85 –	200 500 –	– – –	– – –	– – 300	nA
Input Bias Current ($T_A = T_{high}$) ($T_A = T_{low}$) ($T_A = 0^\circ$ to $+70^\circ\text{C}$)	I_{IB}	– – –	30 300 –	500 1500 –	– – –	– – –	– – 800	nA
Common Mode Input Voltage Range	V_{ICR}	± 12	± 13	–	–	–	–	V
Large Signal Voltage Gain ($V_O = \pm 10\text{ V}$, $R_L = 2.0\text{ k}\Omega$)	A_{VOL}	25	–	–	15	–	–	V/mV
Common Mode Rejection ($R_S \leq 10\text{ k}\Omega$)	CMR	70	90	–	–	–	–	dB
Supply Voltage Rejection Ratio ($R_S \leq 10\text{ k}\Omega$)	PSRR	–	30	150	–	–	–	$\mu\text{V/V}$
Output Voltage Swing ($R_L \geq 10\text{ k}\Omega$) ($R_L \geq 2.0\text{ k}\Omega$)	V_O	± 12 ± 10	± 14 ± 13	– –	± 12 ± 10	± 14 ± 13	– –	V
Supply Currents (Both Amplifiers) ($T_A = T_{high}$) ($T_A = T_{low}$)	I_D	– –	– –	4.5 6.0	– –	– –	5.0 6.7	mA
Power Consumption (Both Amplifiers) ($T_A = T_{high}$) ($T_A = T_{low}$)	P_C	– –	– –	135 180	– –	– –	150 200	mW

NOTES: 1. I_{IB} is out of the amplifier due to PNP input transistors.
2. $T_{high} = +70^\circ\text{C}$, $T_{low} = 0^\circ\text{C}$.

Figure 1. Burst Noise versus Source Resistance

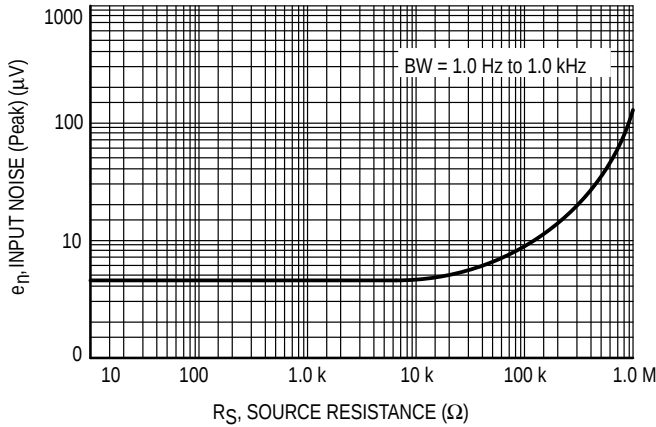


Figure 2. RMS Noise versus Source Resistance

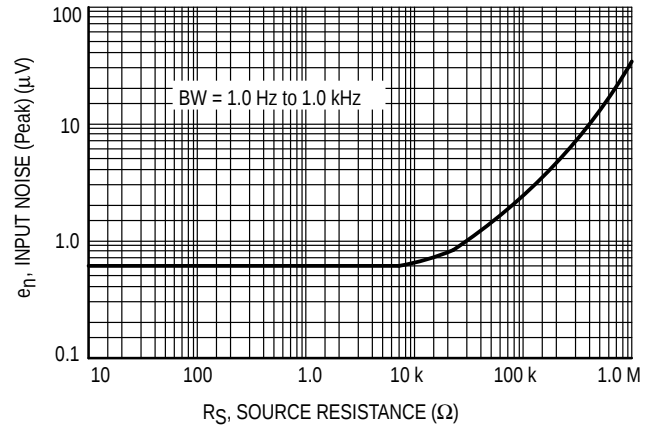


Figure 3. Output Noise versus Source Resistance

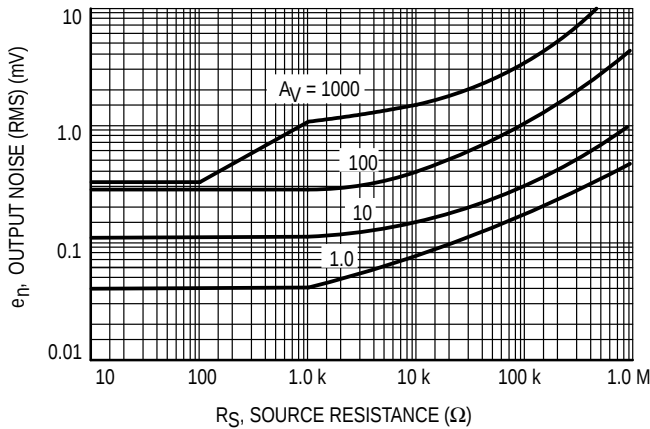


Figure 4. Spectral Noise Density

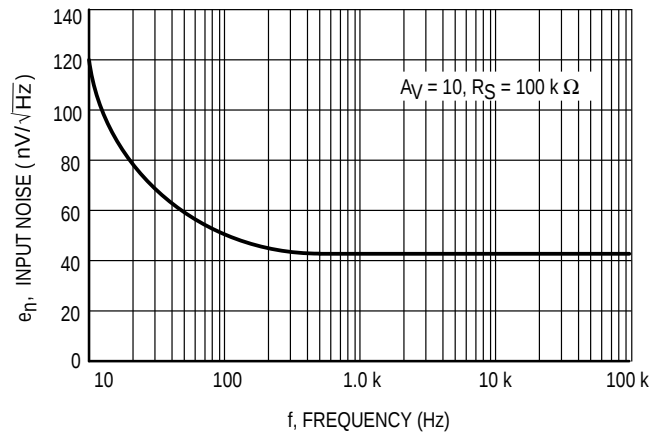
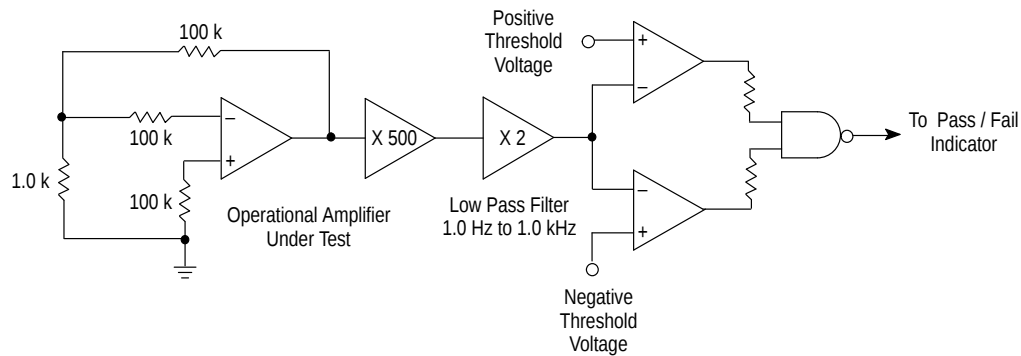


Figure 5. Burst Noise Test Circuit



Unlike conventional peak reading or RMS meters, this system was especially designed to provide the quick response time essential to burst (popcorn) noise testing.

The test time employed is 10 sec and the 20 μV peak limit refers to the operational amplifier input thus eliminating errors in the closed loop gain factor of the operational amplifier.

Figure 6. Open Loop Frequency Response

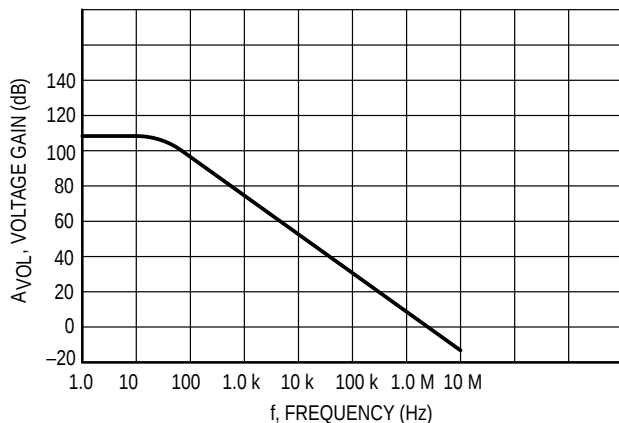


Figure 7. Phase Margin versus Frequency

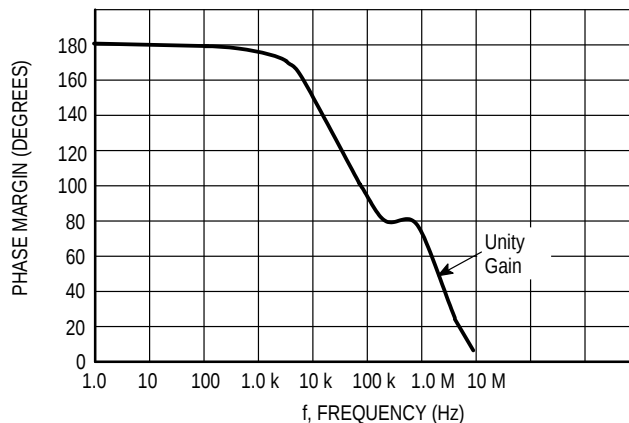


Figure 8. Positive Output Voltage Swing versus Load Resistance

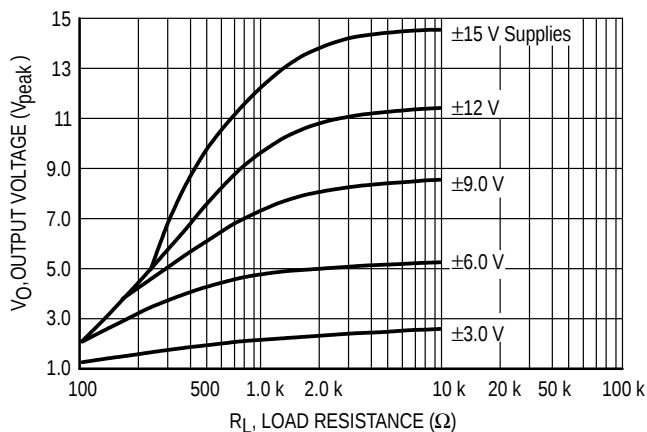


Figure 9. Negative Output Voltage Swing versus Load Resistance

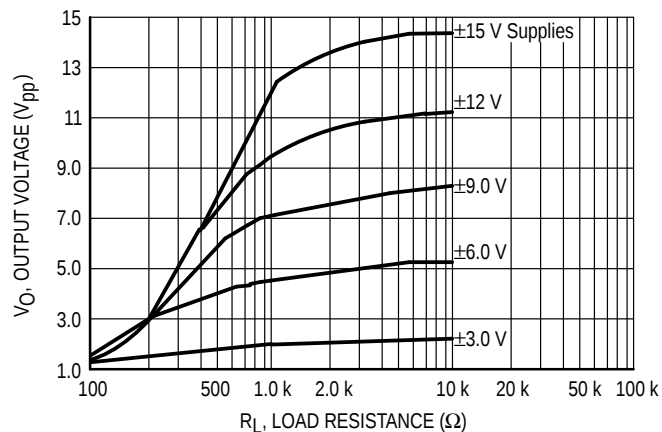


Figure 10. Power Bandwidth (Large Signal Swing versus Frequency)

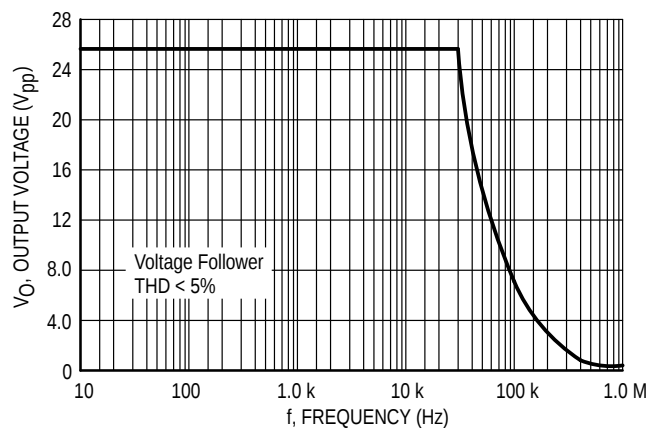
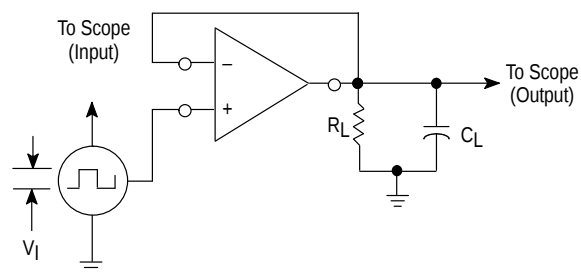
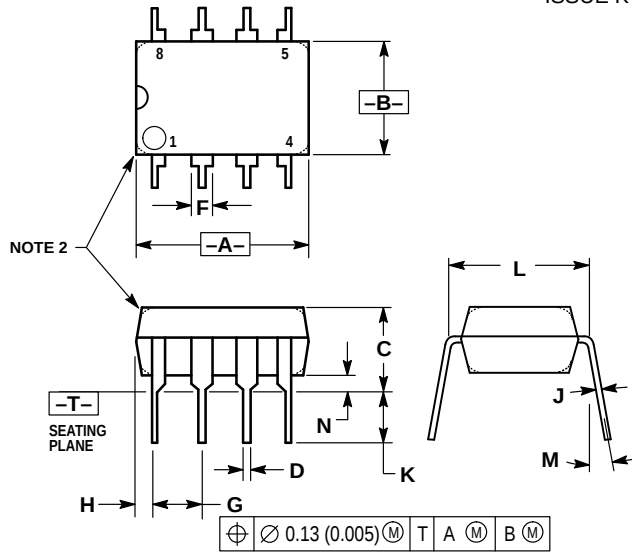


Figure 11. Transient Response Test Circuit



OUTLINE DIMENSIONS

P1 SUFFIX
PLASTIC PACKAGE
CASE 626-05
ISSUE K

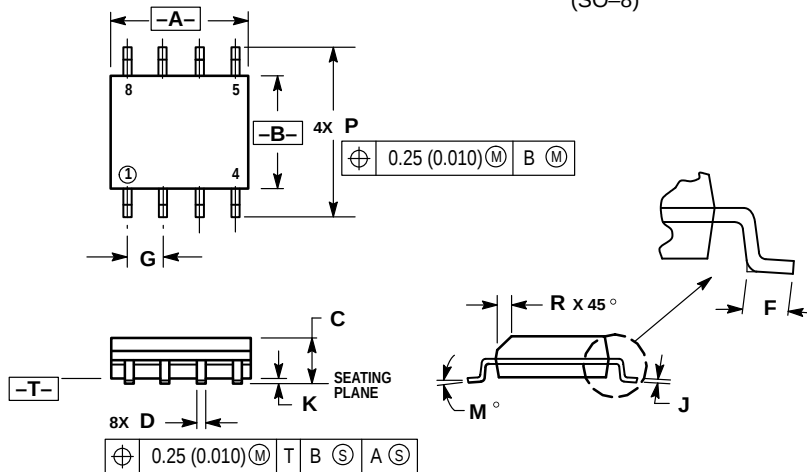


NOTES:

1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).
3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.40	10.16	0.370	0.400
B	6.10	6.60	0.240	0.260
C	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	10°		10°	
N	0.76	1.01	0.030	0.040

D SUFFIX
PLASTIC PACKAGE
CASE 751-05
ISSUE N
(SO-8)



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.80	5.00	0.189	0.196
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.18	0.25	0.007	0.009
K	0.10	0.25	0.004	0.009
M	0°		7°	
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and  are registered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

How to reach us:

USA / EUROPE: Motorola Literature Distribution;
P.O. Box 20912; Phoenix, Arizona 85036. 1-800-441-2447

MFAX: RMFAX0@email.sps.mot.com – TOUCHTONE (602) 244-6609
INTERNET: <http://Design-NET.com>

JAPAN: Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, Toshikatsu Otsuki,
6F Seibu-Butsuryu-Center, 3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-3521-8315

HONG KONG: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park,
51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298



◇ CODELINE TO BE PLACED HERE

MC4558AC/D

