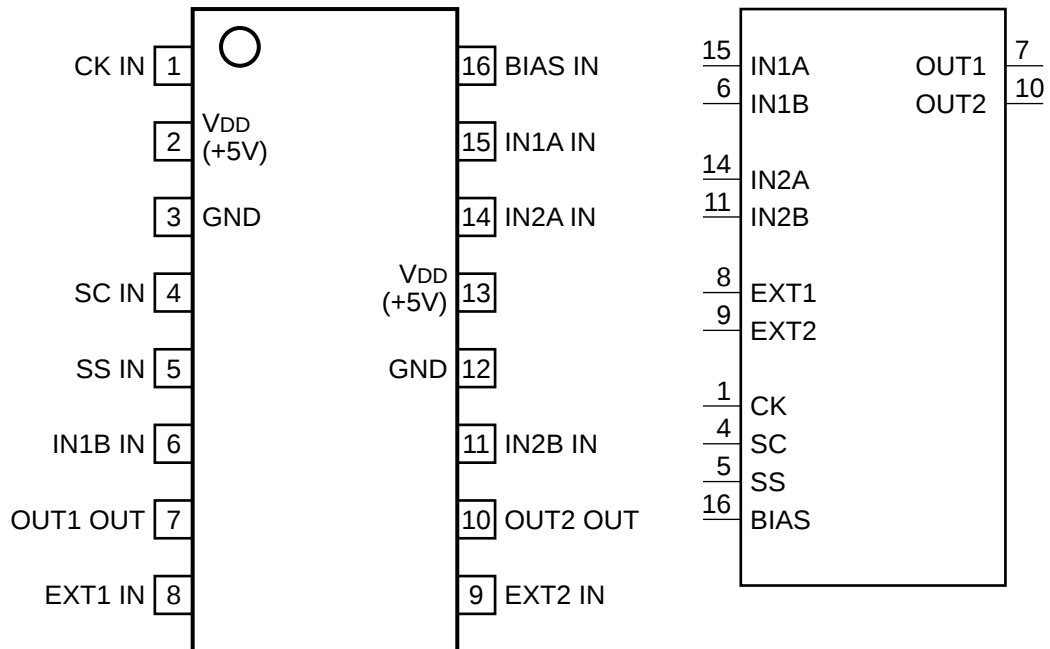

C-MOS CHROMA DELAY LINE

-TOP VIEW-



BIAS ; BIAS CURRENT INPUT
 CK ; SYSTEM CLOCK INPUT
 EXT1, 2 ; EXTARNAL R-Y (EXT1) AND B-Y (EXT2) INPUTS
 IN1A, B ; R-Y INPUTS
 IN2A, B ; B-Y INPUTS
 OUT1, 2 ; R-Y (OUT1) AND B-Y (OUT2) OUTPUTS
 SS ; SYSTEM SELECT INPUT
 SC ; SANDCASTLE PULSE INPUT

