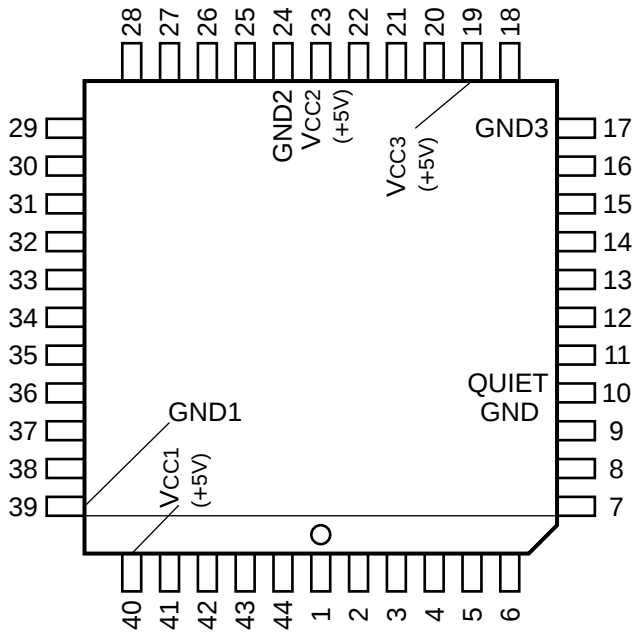

BUS CONTROLLED MULTISTANDARD VIDEO PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	COMP VIDEO1	23	—	VCC2
2	I	ACC FILTER	24	—	GND2
3	I	COMP VIDEO2	25	I	FC
4	O	VERTICAL SYNC	26	I	B
5	I	SCL	27	I	G
6	O	SDL	28	I	R
7	O	FIELD ID	29	I	Y2
8	O	BURST GATE	30	I	B-Y
9	I	IREF	31	I	R-Y
10	—	QUIET GND	32	I	Y1 CLAMP
11	I	H FILTER	33	O	Y1
12	I	FILTER SWITCH	34	O	SYSTEM SELECT
13	O	16FH/C SYNC	35	O	S/C
14	O	FH REF	36	I	XTAL2 (17.7 MHz)
15	I	15K RET	37	—	NC
16	I	PLL #2 FILTER	38	I	XTAL1 (14.3 MHz)
17	—	GND3	39	—	GND1
18	O	CLOCK	40	—	VCC1
19	—	VCC3	41	O	B-Y
20	O	R/V	42	O	R-Y
21	O	G/Y	43	I	IDENT FILTER
22	O	B/U	44	I	CRYSTAL PLL FILTER

