



Automotive Half-Amp High-Side Switch

The MC3399 is a High–Side Switch designed to drive loads from the positive side of the power supply. The output is controlled by a TTL compatible input Enable pin. In the "on" state, the device exhibits very low saturation voltages for load currents in excess of 750 mA. The device isolates the load from positive or negative going high voltage transients by abruptly "opening" thus protecting the load from the transient voltage for the duration of the transient. The device automatically re–establishes its original operating state following the transient condition.

The MC3399 is fabricated on a power BIMOS process which combines the best features of Bipolar and MOS technologies. The mixed technology provides higher gain PNP output devices and results in Power Integrated Circuits having substantially reduced quiescent currents.

The device operates over a wide power supply voltage range and can withstand voltage transients (positive or negative) of ± 100 V. A rugged PNP output stage along with active clamp circuitry, output current limit and thermal shutdown permit the driving of all types of loads, including inductive. The MC3399 is offered in 5–lead TO–220 and 16–lead SOIC plastic packages to facilitate either "thru–hole" or surface mount use. In addition, it is specified over a wide ambient operating temperature of –40°C to +125°C and is ideally suited for industrial and automotive applications where harsh environments exist.

- Low Switch Voltage Drop
- Load Currents in Excess of 750 mA
- Low Quiescent Current
- Transient Protection Up to ± 100 V
- TTL Compatible Enable Input
- On-Chip Current Limit and Thermal Shutdown Circuitry





ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC3399DW	T. 400 to 140500	SOP(8+8)L
MC3399T	$T_A = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	Plastic Power

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Ignition Input Voltage (Continuous)	VIGN		Vdc
Forward		25	
Reverse		-16	
Ignition Input Voltage (Transient)	VIGN		V
		±60	
		±100	
Input Voltage	V _{in}	-0.3 to +7.0	V
Output Current	IO	Internally	А
		Limited	
Thermal Resistance			°C/W
Plastic Power Package (Case 314D)			
Junction-to-Ambient	R _{0JA1}	65	
Junction-to-Tab	R _{0JT}	5.0	
SOP(8+8)L Plastic Package (Case 751G)			
Junction-to-Ambient	R _{0JA2}	138	
Junction-to-Lead 12	R _{θJL}	52	
Soldering Temperature (for 10 Seconds)	T _{solder}	260	°C
Junction Temperature	Тj	-40 to +150	°C
Storage Temperature	T _{stg}	-65 to +150	°C

$\textbf{ELECTRICAL CHARACTERISTICS} ~ (V_{IGN} = 12 ~ V, ~ I_L = 150 ~ mA, -40^{\circ}C \leq T_A = +125^{\circ}C, ~ V ~ Input = ``1", ~ unless ~ otherwise ~ noted.) (1)$

Characteristic	Symbol	Min	Тур	Max	Unit
Operating Voltage	VIGN(min)	4.5	-	-	V
Switch Voltage Drop (Saturation) $V_{IGN} = 4.5 \text{ V}$ I _O = 150 mA, T _A = 25°C I _O = 200 mA, T _A = -40°C I _O = 125 mA, T _A = 125°C $V_{IGN} = 12 \text{ V}$ I _O = 425 mA, T _A = 25°C	VIGN-VO	- - - -	0.2 0.3 0.3 0.3	0.5 0.5 0.5 0.7	V
$I_{O} = 550 \text{ mA}, T_{A} = -40^{\circ}\text{C}$ $V_{IGN} = 16 \text{ V} I_{O} = 375 \text{ mA}, T_{A} = 125^{\circ}\text{C}$			0.3 0.4	0.7 0.7	
Quiescent Current $V_{IGN} = 12 V I_{O} = 150 mA$, $T_{A} = 25^{\circ}C$ $I_{O} = 550 mA$, $T_{A} = -40^{\circ}C$ $I_{O} = 300 mA$, $T_{A} = 125^{\circ}C$	IGND		12 25 10	50 100 50	mA
Output Current Limit (V _O = 0 V)	I _{SC}	-	1.6	2.5	A
Output Leakage Current (V _{IGN} = 12 V, Input = "0")	l _{Leak}	-	10	150	μA
Input Voltage High Logic State Low Logic State	VIH VIL	2.0		- 0.8	V
Input Current High Logic State (VIH = 5.5 V) Low Logic State (VIL = 0.4 V)	Iн IL		120 20		μΑ
Output Turn–On Delay Time Input = "0" \rightarrow "1", T _A = +25°C (Figures 1 and 3)	^t DLY(on)	_	50	_	μs
Output Turn–Off Delay Time Input = "1" \rightarrow "0", T _A = +25°C (Figures 1 and 3)	^t DLY(off)	_	5.0	_	μs
Overvoltage Shutdown Threshold	Vin(OV)	26	31	36	V
Output Turn–Off Delay Time (T _A = + 25°C) to Overvoltage Condition, V_{in} stepped from 12 V to 40 V, V \leq 0.9 V _O (Figures 1 and 3)	tDLY	_	2.0	_	μs
Output Recovery Delay Time (T _A = + 25°C) V _{IGN} stepped from 40 V to 12 V, V \geq 0.9 V _O (Figures 1 and 3)	^t RCVY	_	5.0	_	μs

NOTES: 1. Typical values represent characteristics of operation at $T_A = 25^{\circ}C$.

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Figure 1. Transient Response Test Circuit



NOTE: * Depending on load current and transient duration, an output capacitor (C_O) of sufficient value may be used to hold up output voltage during the transient, and absorb turn–off delay voltage overshoot.

Figure 2. Timing Diagram





Figure 5. Quiescent Current versus Load Current



Figure 3. Response Time Diagram

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OUTLINE DIMENSIONS



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