



**MOTOROLA**

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# MC1748C

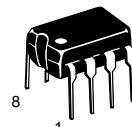
## High Performance Operational Amplifier

The MC1748C is designed for use as a summing amplifier, integrator, or amplifier with operating characteristics as a function of the external feedback components.

- Noncompensated MC1741C
- Single 30 pF Capacitor Compensation Required for Unity Gain
- Short Circuit Protection
- Offset Voltage Null Capability
- Wide Common Mode and Differential Voltage Ranges
- Low Power Consumption
- No Latch Up

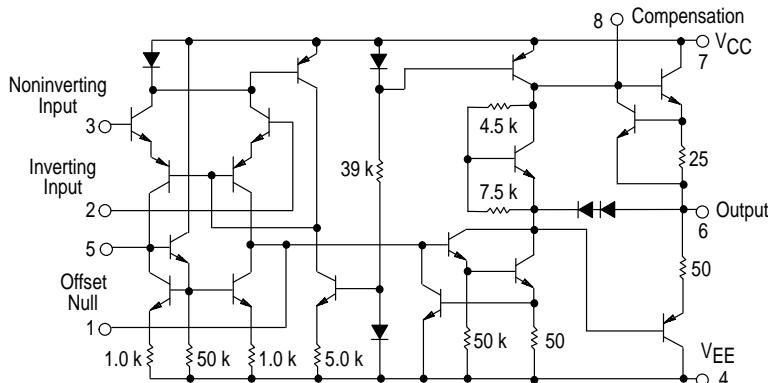
### OPERATIONAL AMPLIFIER

#### SEMICONDUCTOR TECHNICAL DATA

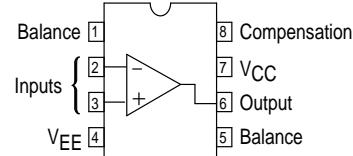


P1 SUFFIX  
PLASTIC PACKAGE  
CASE 626

#### Representative Schematic Diagram



#### PIN CONNECTIONS



#### ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC1748CP1	TA = 0° to +70°C	Plastic DIP

#### Typical Compensation Circuits

Figure 1. Offset Adjust and Frequency Compensation

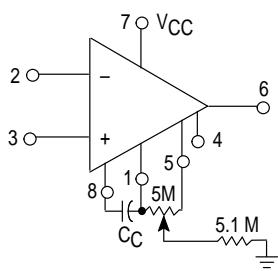


Figure 2. Single-Pole Compensation

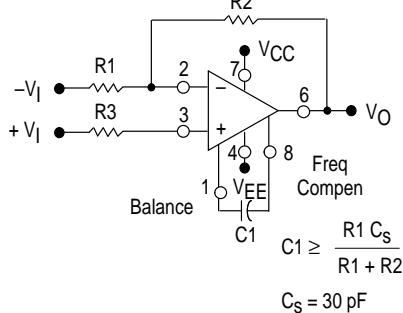
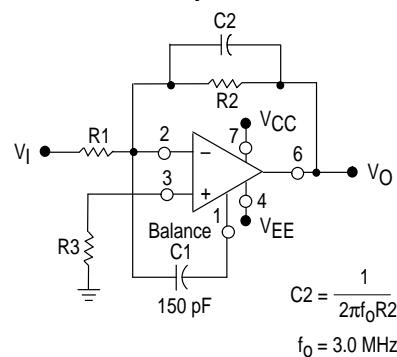


Figure 3. Feedforward Compensation



# MC1748C

**MAXIMUM RATINGS** ( $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

Rating	Symbol	Value	Unit
Power Supply Voltage	$V_{CC}$ $V_{EE}$	+18 -18	Vdc
Differential Input Signal	$V_{in}$	$\pm 30$	V
Common Mode Input Swing (Note 1)	$V_{ICR}$	$\pm 15$	V
Output Short Circuit Duration	$t_{SC}$	Continuous	
Power Dissipation (Package Limitation) Derate above $T_A = +25^\circ\text{C}$	$P_D$	680 4.6	mW mW/ $^\circ\text{C}$
Operating Temperature Range	$T_A$	0 to +70	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-65 to +150	$^\circ\text{C}$

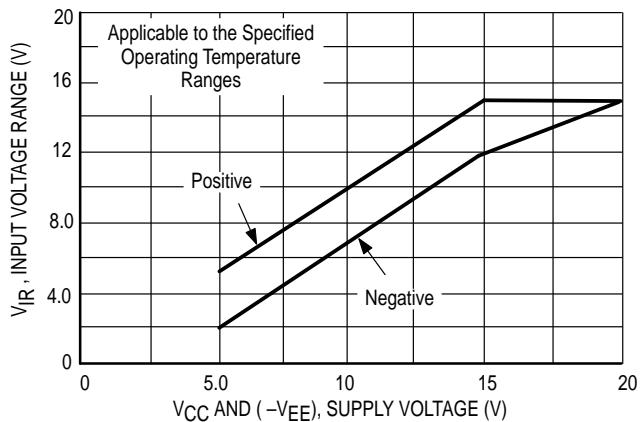
**ELECTRICAL CHARACTERISTICS** ( $V_{CC} = +15$  Vdc,  $V_{EE} = -15$  V,  $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Input Bias Current $T_A = +25^\circ\text{C}$ $T_A = T_{low}$ to $T_{high}$ (Note 2)	$I_{IB}$	— —	0.08 —	0.5 0.8	$\mu\text{A}/\text{dC}$
Input Offset Current $T_A = +25^\circ\text{C}$ $T_A = T_{low}$ to $T_{high}$	$ I_{IO} $	— —	0.02 —	0.2 0.3	$\mu\text{A}/\text{dC}$
Input Offset Voltage ( $R_S \leq 10$ k $\Omega$ ) $T_A = +25^\circ\text{C}$ $T_A = T_{low}$ to $T_{high}$	$ V_{IO} $	— —	1.0 —	6.0 7.5	mVdc
Differential Input Impedance (Open Loop, $f = 20$ Hz) Parallel Input Resistance Parallel Input Capacitance	$R_P$ $C_P$	0.3 —	2.0 1.4	— —	M $\Omega$ pF
Common Mode Input Impedance ( $f = 20$ Hz)	$Z_{in}$	—	200	—	M $\Omega$
Common Mode Input Voltage Swing	$V_{ICR}$	$\pm 12$	$\pm 13$	—	Vpk
Common Mode Rejection ( $f = 100$ Hz)	CMR	70	90	—	dB
Open Loop Voltage Gain, ( $V_O = \pm 10$ V, $R_L = 2.0$ k $\Omega$ ) $T_A = +25^\circ\text{C}$ $T_A = T_{low}$ to $T_{high}$	$A_{VOL}$	20,000 15,000	200,000 —	— —	V/V
Step Response ( $V_{in} = 20$ mV, $C_C = 30$ pF, $R_L = 2.0$ k $\Omega$ , $C_L = 100$ pF) Rise Time Overshoot Slew Rate	$t_r$ $os$ $dV_{out}/dt$	— — —	0.3 5.0 0.8	— — —	$\mu\text{s}$ % V/ $\mu\text{s}$
Output Impedance ( $f = 20$ Hz)	$Z_O$	—	75	—	$\Omega$
Short Circuit Output Current	$I_{sc}$	—	25	—	$\text{mA}/\text{dC}$
Output Voltage Swing ( $R_L = 10$ k $\Omega$ ) $R_L = 2$ k $\Omega$ ( $T_A = T_{low}$ to $T_{high}$ )	$V_O$	$\pm 12$ $\pm 10$	$\pm 14$ $\pm 13$	— —	Vpk
Power Supply Sensitivity $V_{EE} = \text{constant}$ , $R_S \leq 10$ k $\Omega$ $V_{CC} = \text{constant}$ , $R_S \leq 10$ k $\Omega$	PSR+ PSR-	75 75	— —	— —	dB
Power Supply Current	$I_D +$ $I_D -$	— —	1.67 1.67	2.83 2.83	$\text{mA}/\text{dC}$
DC Quiescent Power Dissipation ( $V_O = 0$ V)	$P_D$	—	50	85	mW

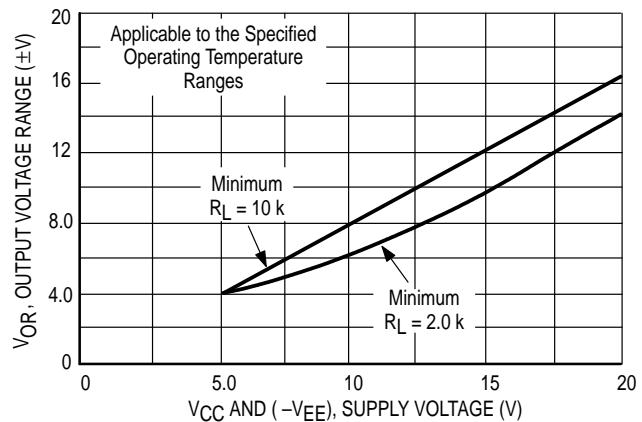
**NOTES:** 1. For supply voltages of less than  $\pm 15$  V, the maximum input voltage is equal to the supply voltage.

2.  $T_{low} = 0^\circ\text{C}$      $T_{high} = +70^\circ\text{C}$

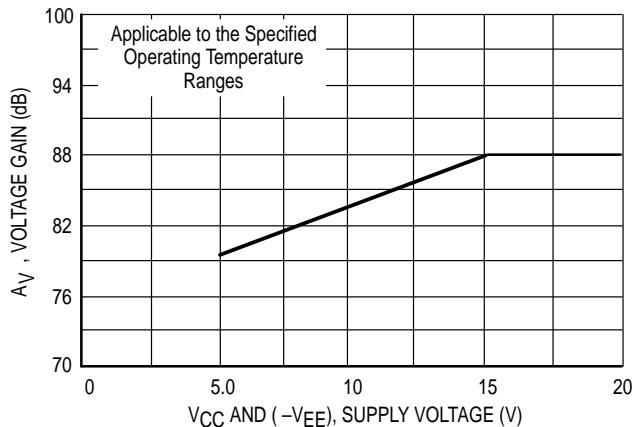
**Figure 4. Minimum Input Voltage Range**



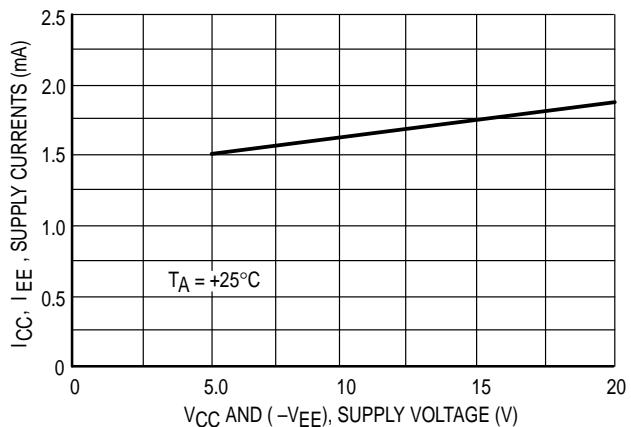
**Figure 5. Minimum Output Voltage Swing**



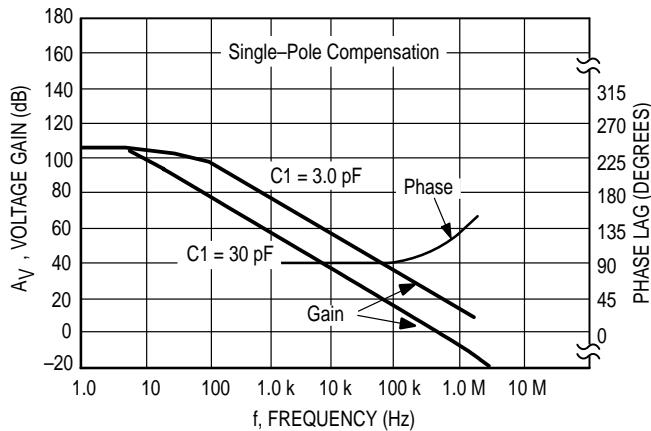
**Figure 6. Minimum Voltage Gain**



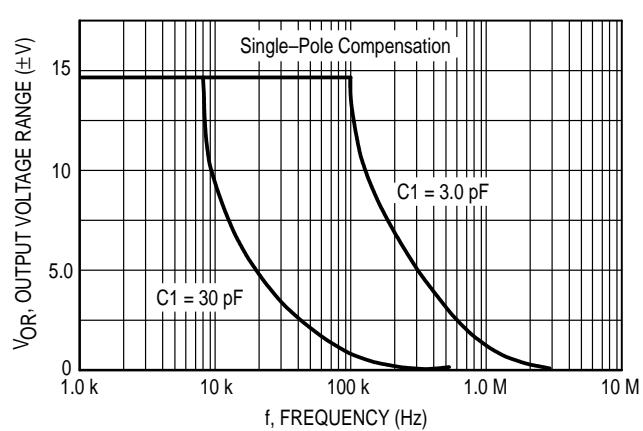
**Figure 7. Typical Supply Currents**



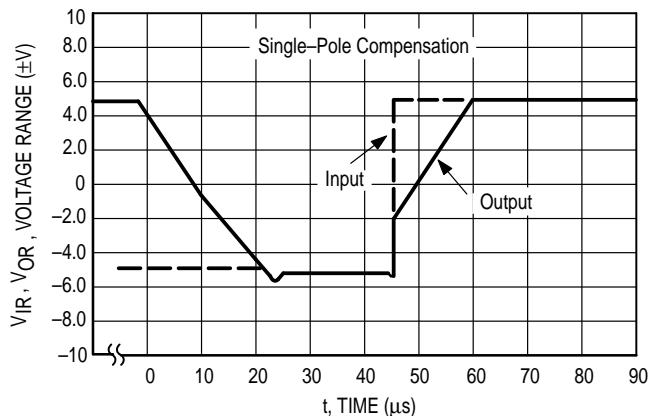
**Figure 8. Open Loop Frequency Response**



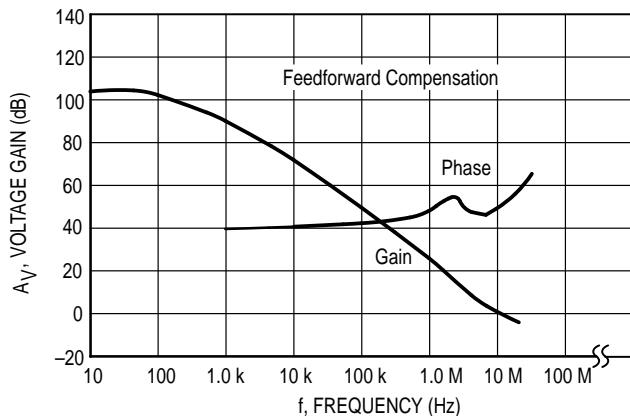
**Figure 9. Large-Signal Frequency Response**



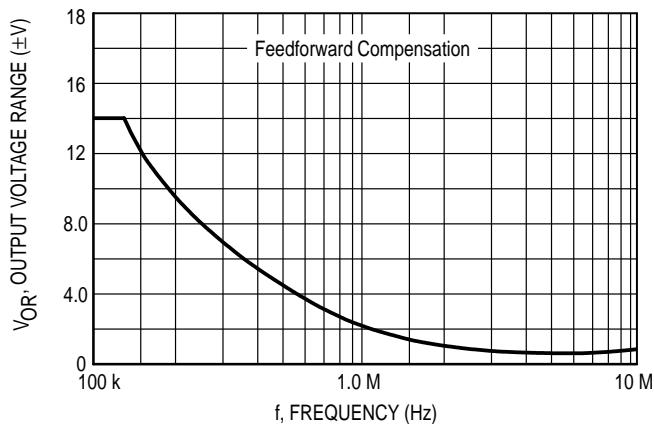
**Figure 10. Voltage Follower Pulse Response**



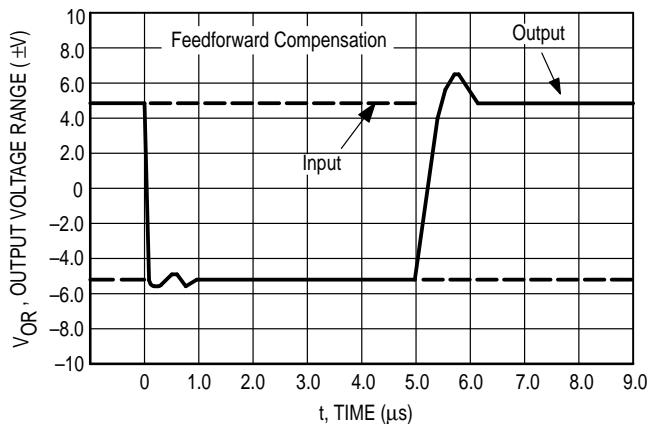
**Figure 11. Open Loop Frequency Response**



**Figure 12. Large-Signal Frequency Response**

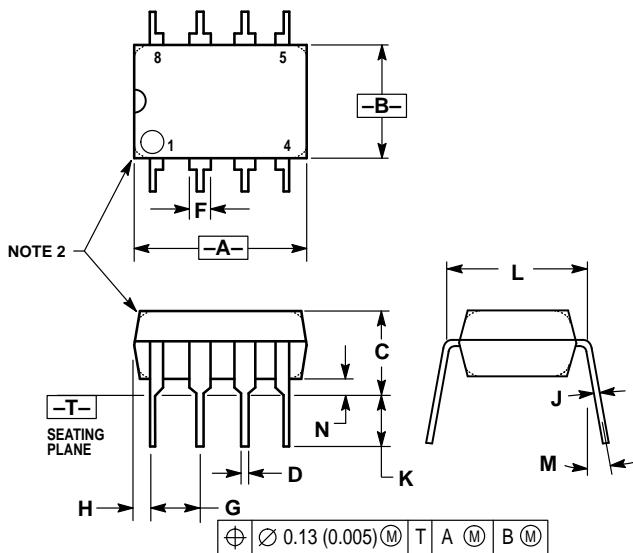


**Figure 13. Inverter Pulse Response**



## OUTLINE DIMENSIONS

**P1 SUFFIX**  
**PLASTIC PACKAGE**  
**CASE 626-05**  
**ISSUE K**



## NOTES:

1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).
3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.40	10.16	0.370	0.400
B	6.10	6.60	0.240	0.260
C	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	—	10°	—	10°
N	0.76	1.01	0.030	0.040

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