

MC145536EVK

Advance Information

Codec-Filter/ADPCM Transcoder Evaluation Kit

The MC145536EVK is the primary tool for evaluation and demonstration of the MC145480 Single + 5 V Supply PCM Codec-Filter and the MC145532 ADPCM Transcoder. The MC145536EVK provides the user with the hardware needed to evaluate the many separate operating modes under which the MC145480 and MC145532 are intended to operate.

General

- Provides Stand Alone Evaluation on Single Board
- + 5 V Only Power Supply
- Easily Interfaced to Test Equipment, Customer System, Second MC145536EVK or MC145537EVK
- Convenient Access to Key Signals
- Generous Prototype Area for Application Development
- The Kit Provides Analog-to-Analog, Analog-to-Digital (64 kbps PCM; 32, 24, or 16 kbps ADPCM) or Digital- (64 kbps PCM; 32, 24, or 16 kbps ADPCM) to-Analog Connections
- Handset Included
- Schematics, Data Sheets, and User's Manual Included

MC145480

- Single + 5 V Power Supply
- Typical Power Dissipation of 25 mW, Power Down of < 1 mW
- Conforms to CCITT and Bell Specifications
- Mu-Law or A-Law Companding
- Differential Analog Circuit Design for Lowest Noise
- 20-Pin Plastic Package
- Production in 1.5 Micron CMOS Process
- UDR Design Layout Rules for Core Cell Applications

MC145532

- Single-Chip Full-Duplex PCM-to-ADPCM Encoder and ADPCM-to-PCM Decoder
- Achieves High Audio Quality at Reduced Bit Rates
- PCM Data Rate of 64 kbps
- ADPCM Data Rates of 32, 24, or 16 kbps
- Conforms to CCITT and ANSI ADPCM Standards
- Custom DSP Engine Optimized for ADPCM Algorithm
- Volume Production in 1.5 Micron CMOS Process
- UDR Design Layout Rules for Core Cell Applications

This document contains information on a new product. Specifications and information herein are subject to change without notice.

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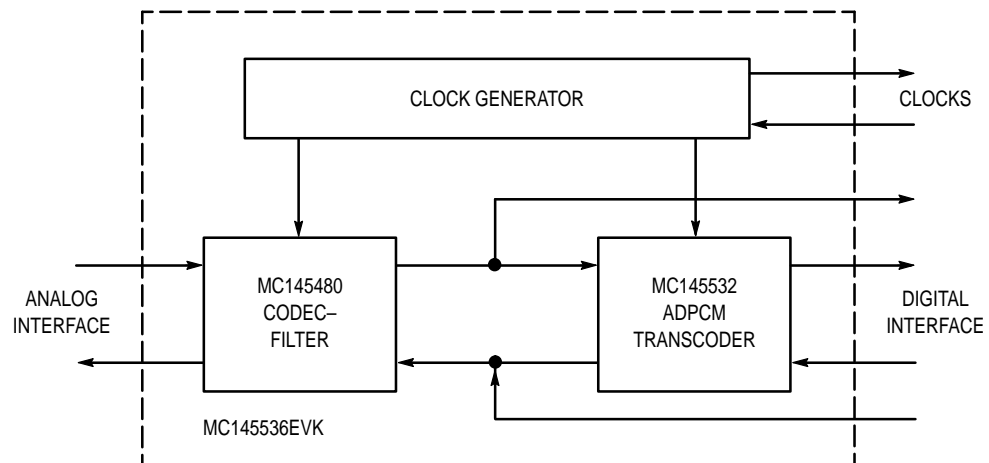


Figure 1. MC145536EVK System-Level Block Diagram