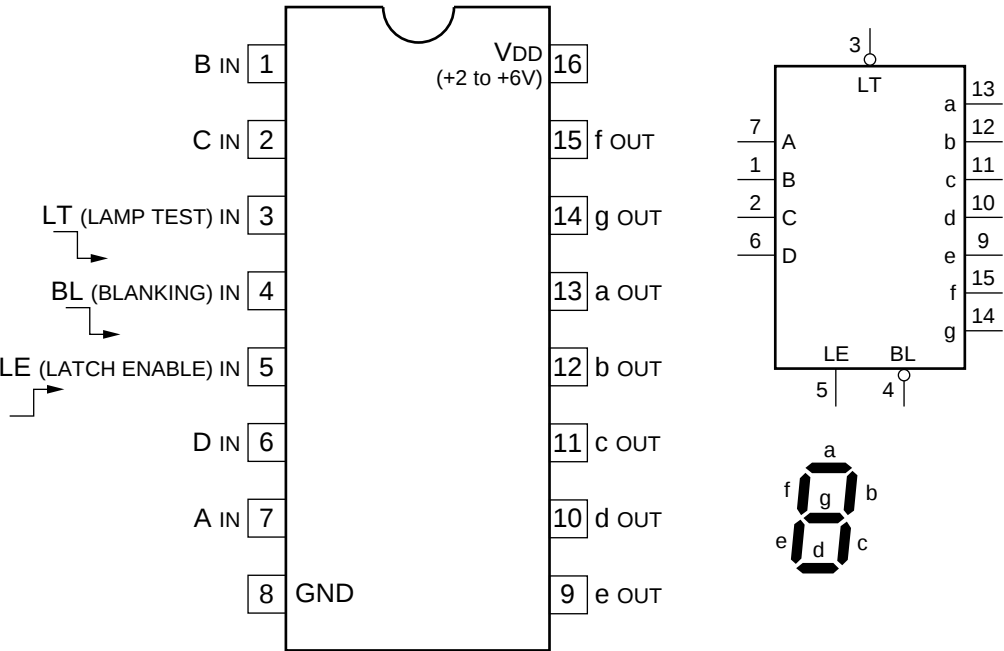

C-MOS BCD TO 7 SEGMENT LATCH/DECODER/DRIVER
-TOP VIEW-



INPUTS							OUTPUTS							DISPLAY
LE	BL	LT	D	C	B	A	a	b	c	d	e	f	g	
X	X	0	X	X	X	X	1	1	1	1	1	1	1	8
X	0	1	X	X	X	X	0	0	0	0	0	0	0	BLANK
0	1	1	0	0	0	0	1	1	1	1	1	1	0	0
0	1	1	0	0	0	1	0	1	1	0	0	0	0	1
0	1	1	0	0	1	0	1	1	0	1	1	0	1	2
0	1	1	0	0	1	1	1	1	1	1	0	0	1	3
0	1	1	0	1	0	0	0	1	1	0	0	1	1	4
0	1	1	0	1	0	1	1	0	1	1	0	1	1	5
0	1	1	0	1	1	0	0	0	1	1	1	1	1	6
0	1	1	0	1	1	1	1	1	1	0	0	0	0	7
0	1	1	1	0	0	0	1	1	1	1	1	1	1	8
0	1	1	1	0	0	1	1	1	1	0	0	1	1	9
0	1	1	1	0	1	0	0	0	0	0	0	0	0	BLANK
0	1	1	1	0	1	1	0	0	0	0	0	0	0	BLANK
0	1	1	1	1	0	0	0	0	0	0	0	0	0	BLANK
0	1	1	1	1	0	1	0	0	0	0	0	0	0	BLANK
0	1	1	1	1	1	0	0	0	0	0	0	0	0	BLANK
0	1	1	1	1	1	1	0	0	0	0	0	0	0	BLANK
1	1	1	X	X	X	X	*							*

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; LOW OR HIGH
* ; DEPEND UPON THE BCD CODE
PREVIOUSLY APPLIED WHEN LE=0.