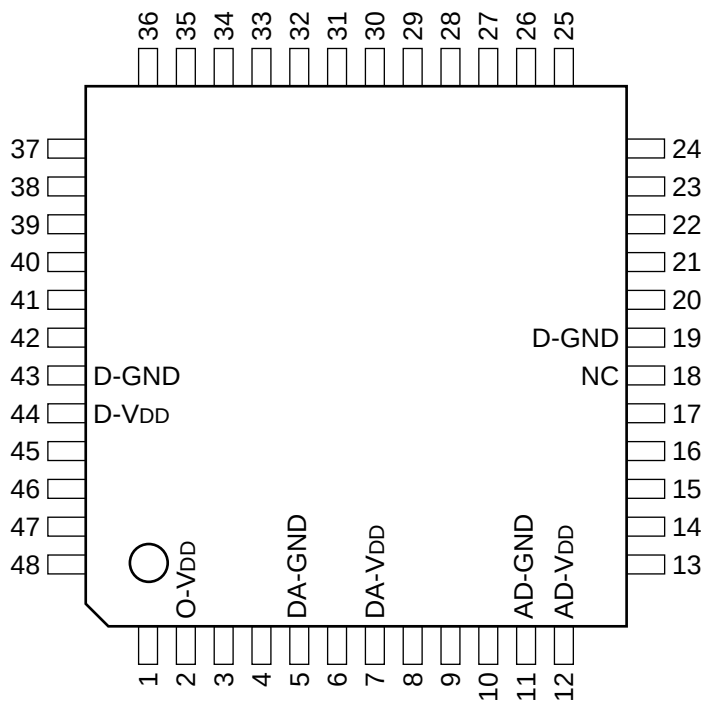


C-MOS DIGITAL Y/C SEPARATION FILTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	PCO	13	—	RTP	25	I/O	C7	37	I/O	D3
2	—	O-VDD	14	—	RBT	26	I/O	C6	38	I/O	D2
3	I	BIAS	15	I	VIN	27	I/O	C5	39	I/O	D1
4	I	FILIN	16	O	CLOUT	28	I/O	C4	40	I/O	D0
5	—	DA-GND	17	—	CLC	29	I/O	C3	41	I	BYPASS
6	O	YOUT	18	—	NC	30	I/O	C2	42	I	VH
7	—	DA-VDD	19	—	D-GND	31	I/O	C1	43	—	D-GND
8	O	COUT	20	I	ADCLK	32	I/O	C0	44	—	D-VDD
9	—	DAREF	21	I	MODE0	33	I/O	D7	45	I	FSC
10	—	IBIAS	22	I	MODE1	34	I/O	D6	46	I	N/M
11	—	AD-GND	23	I	TE0	35	I/O	D5	47	I	PAL/NTSC
12	—	AD-VDD	24	I	TE1	36	I/O	D4	48	I	COMB/BPF

INPUT

ADCLK : A/D CONVERTER CLOCK
BIAS : VCO REFERENCE
BYPASS : B/W
COMB/BPF : COMB/BPF SELECT
FILIN : VCO CONTROL VOLTAGE
FSC : SUBCARRIER
MODE0, MODE1 : MODE
N/M : PAL-N/PAL-M
PAL/NTSC : PAL/NTSC SELECT
TE0, TE1 : TEST MODE
VH : VERTICAL CONTOUR CONTROL
VIN : A/D CONVERTER

OUTPUT

CLOUT : CLAMP VOLTAGE
COUT : CHROMA SIGNAL
PCO : PHASE COMPARATOR
YOUT : Y SIGNAL

INPUT/OUTPUT

C0 - C7 : DIGITAL INTERFACE 1
D0 - D7 : DIGITAL INTERFACE 2

OTHER

CLC : CLAMP TIME CONSTANT SETTING
DAREF : D/A CONVERTER
IBIAS : A/D AND D/A CONVERTERS BIAS CIRCUIT CURRENT CONTROL
RBT : A/D CONVERTER BOTTOM REFERENCE VOLTAGE
RTP : A/D CONVERTER TOP REFERENCE VOLTAGE

