+2, +4, +8 1.1GHz Low Power **Prescaler with Stand-By Mode**

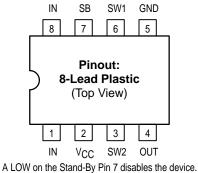
The MC12093 is a single modulus prescaler for low power frequency division of a 1.1GHz high frequency input signal. Motorola's advanced MOSAIC[™] V technology is utilized to acheive low power dissipation of 6.75mW at a minimum supply voltage of 2.7V.

On-chip output termination provides output current to drive a 2pF (typical) high impedance load. If additional drive is required for the prescaler output, an external resistor can be added parallel from the OUT pin to GND to increase the output power. Care must be taken not to exceed the maximum allowable current through the output.

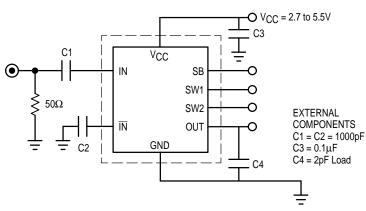
Divide ratio control inputs SW1 and SW2 select the required divide ratio of +2, +4, or +8.

Stand-By mode is featured to reduce current drain to 50µA typical when the standby pin SB is switched LOW disabling the prescaler.

- 1.1GHz Toggle Frequency
- Supply Voltage 2.7V to 5.5VDC
- Low Power 3.0mA Typical
- Operating Temperature –40°C to +85°C
- Divide by 2, 4 or 8 Selected by SW1 and SW2 Pins
- On–Chip Termination



AC TEST CIRCUIT



MC12093

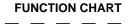
MECL PLL COMPONENTS +2, +4, +8 LOW POWER PRESCALER WITH STAND-BY MODE **D SUFFIX** PLASTIC SOIC PACKAGE CASE 751-05 SD SUFFIX PLASTIC SSOP PACKAGE CASE 940-02

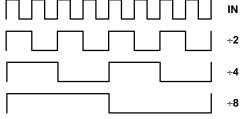
FUNCTION TABLE

SW1	SW2	Divide Ratio		
L	L	8		
н	L	4		
L	н	4		
Н	н	2		

Note: SW1 & SW2: $H = (V_{CC} - 0.5V)$ to V_{CC} ; L = OPEN

SB: H = 2.0V to V_{CC} ; L = GND to 0.8V







12/94

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
VCC	Power Supply Voltage, Pin 2	-0.5 to +6.0	VDC
TA	Operating Temperature Range	-40 to +85	°C
T _{stg}	Storage Temperature Range	-65 to +150	°C
IO	Maximum Output Current, Pin 4	4.0	mA

ELECTRICAL CHARACTERISTICS (V_{CC} = 2.7 to 5.5V; $T_A = -40$ to +85°C)

Symbol	Parameter	Min	Тур	Max	Unit
ft	Toggle Frequency (Sine Wave)	0.1	1.4	1.1	GHz
ICC	Supply Current		3.0	4.5	mA
ISB	Stand-By Current		120	200	μΑ
VIH1	Stand-By Input HIGH (SB)	2.0		VCC	V
V _{IL1}	Stand-By Input LOW (SB)	GND		0.8	V
V _{IH2}	Divide Ratio Control Input HIGH (SW1 & SW2)	V _{CC} – 0.5	VCC	V _{CC} + 0.5	V
V _{IL2}	Divide Ratio Control Input LOW (SW1 & SW2)	OPEN	OPEN	OPEN	
Vout	Output Voltage Swing (2pF Load) Output Frequency 12.5–350MHz ¹ Output Frequency 350–400MHz ² Output Frequency 400–450MHz ³ Output Frequency 450–550MHz ⁴	0.6 0.5 0.4 0.3	0.80 0.70 0.55 0.45		Vpp
V _{IN}	Input Voltage Sensitivity 250–1100MHz 100–250MHz	100 400		1000 1000	mV _{PP}

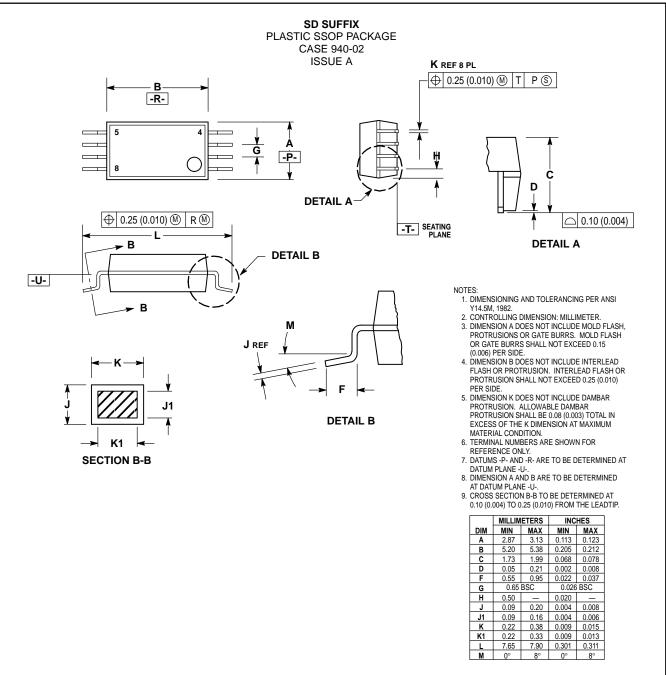
1 Input frequency 1.1GHz, ÷8, minimum output frequency of 12.5MHz.

2 Input frequency 700-800MHz, ÷2.

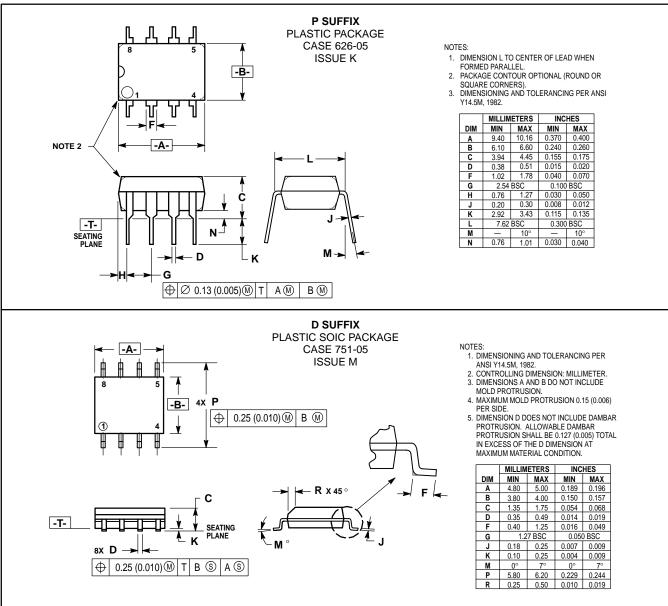
3 Input frequency 800–900MHz, ÷2.

4 Input frequency 900–1100MHz, ÷2.

OUTLINE DIMENSIONS



OUTLINE DIMENSIONS



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