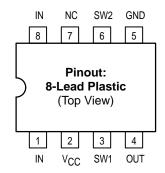
# 2.8GHz Prescaler

The MC12079 is a single modulus divide by 64, 128, 256 prescaler for low power frequency division of a 2.8GHz (typical) high frequency input signal. Divide ratio control inputs SW1 and SW2 select the required divide ratio of  $\div$ 64,  $\div$ 128, or  $\div$ 256.

An external load resistor is required to terminate the output. A  $1.2k\Omega$  resistor is recommended to achieve a  $1.6V_{pp}$  output swing, when dividing a 1.1GHz input signal by the minimum divide ratio of 64, assuming a 12pF load. Output current can be minimized dependent on conditions such as output frequency, capacitive load being driven, and output voltage swing required. Typical values for load resistors are included in the  $V_{out}$  specification for various divide ratios at 2.8GHz input frequency.

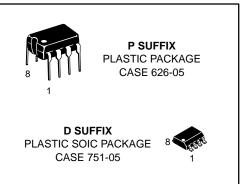
- 2.8GHz Toggle Frequency
- Supply Voltage 4.5V to 5.5V
- Low Power 9mA Typical at V<sub>CC</sub> = 5.0V
- Operating Temperature Range of -40°C to +85°C



# MC12079

## **MECL PLL COMPONENTS**

÷64/128/256 PRESCALER



### **MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
Vcc	Power Supply Voltage, Pin 2	−0.5 to +7.0	VDC
TA	Operating Temperature Range	-40 to +85	°C
T <sub>stg</sub>	Storage Temperature Range	−65 to +150	°C
lo	Maximum Output Current, Pin 4	4	mA

# **ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 4.5 \text{ to } 5.5V$ ; $T_A = -40 \text{ to } +85^{\circ}C$ )

Symbol	Parameter	Min	Тур	Max	Unit
ft	Toggle Frequency (Sine Wave)	0.25	3.4	2.8	GHz
Icc	Supply Current Output (Pin 2)	_	9.0	11.5	mA
V <sub>in</sub>	Input Voltage Sensitivity 250–500MHz 500–2800MHz	400 100	_ _	1000 1000	mV <sub>PP</sub>
VIH	Divide Ratio Control Input High (SW)	V <sub>CC</sub> – 0.5	Vcc	V <sub>CC</sub> + 0.5	V
V <sub>IL</sub>	Divide Ratio Control Input Low (SW)	Open	Open	Open	_
V <sub>out</sub>	Output Voltage Swing $(C_L = 12pF; R_L = 1.2k\Omega; I_O = 2.7mA)^{1}$ $(C_L = 12pF; R_L = 2.2k\Omega; I_O = 1.5mA)^{2}$ $(C_L = 12pF; R_L = 3.9k\Omega; I_O = 0.85mA)^{3}$	1.0	1.6	_	Vpp

- 1 Divide ratio of ÷64 at 2.8GHz.
- 2 Divide ratio of ÷128 at 2.8GHz.
- 3 Divide ratio of ÷256 at 2.8GHz.

MOTOROLA

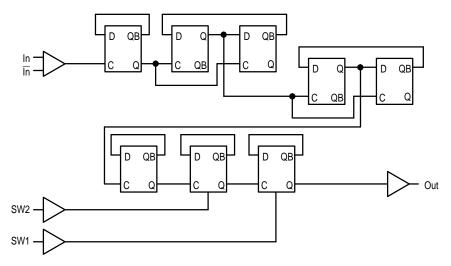


Figure 1. Logic Diagram (MC12079)

## **FUNCTION TABLE**

SW1	SW2	Divide Ratio
Н	Н	64
Н	L	128
L	Н	128
L	L	256

Note: SW1 & SW2: H = V<sub>CC</sub>; L = Open

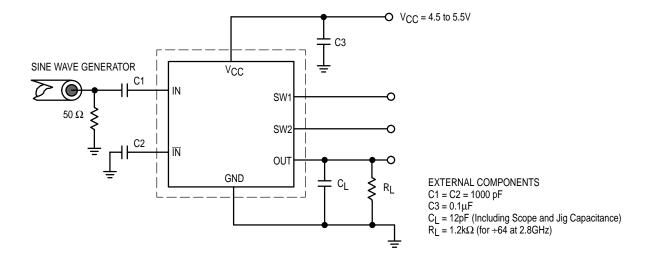


Figure 2. AC Test Circuit

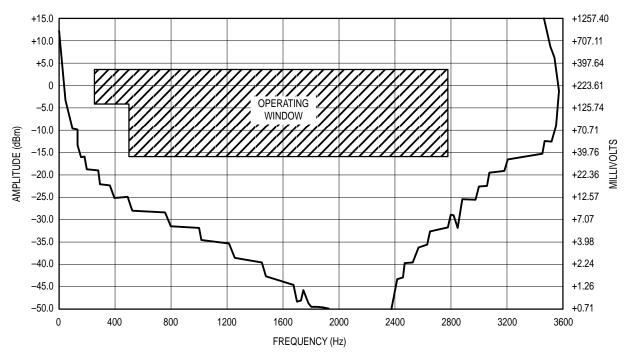


Figure 3. Input Signal Amplitude versus Input Frequency Divide Ratio = 64;  $V_{CC}$  = 5.0V;  $T_A$  = 25°C

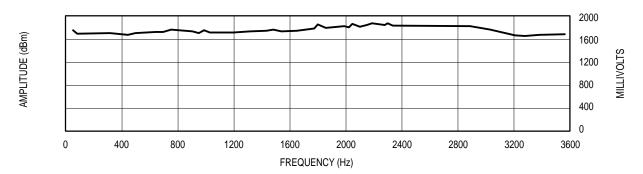
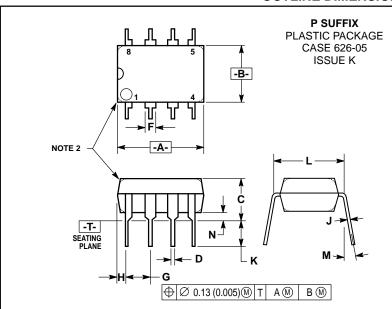


Figure 4. Output Amplitude versus Input Frequency

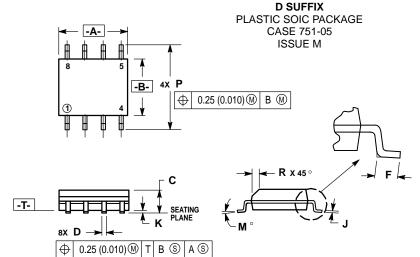
### **OUTLINE DIMENSIONS**



#### NOTES:

- 1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL
- PACKAGE CONTOUR OPTIONAL (ROUND OR
- SQUARE CORNERS).
  DIMENSIONING AND TOLERANCING PER ANSI

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	9.40	10.16	0.370	0.400	
В	6.10	6.60	0.240	0.260	
С	3.94	4.45	0.155	0.175	
D	0.38	0.51	0.015	0.020	
F	1.02	1.78	0.040	0.070	
G	2.54 BSC		0.100 BSC		
Н	0.76	1.27	0.030	0.050	
J	0.20	0.30	0.008	0.012	
K	2.92	3.43	0.115	0.135	
L	7.62 BSC		0.300 BSC		
М		10°	_	10°	
N	0.76	1.01	0.030	0.040	



#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER 3. DIMENSIONS A AND B DO NOT INCLUDE
- MOLD PROTRUSION.
- 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006)
- 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.196
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27 BSC		0.050 BSC	
J	0.18	0.25	0.007	0.009
K	0.10	0.25	0.004	0.009
М	0°	7°	0°	7°
Р	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

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