

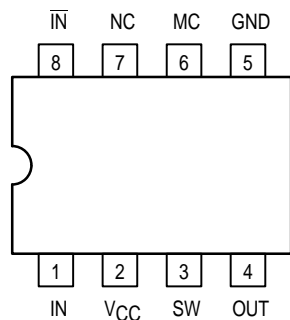
1.1GHz Low Power Dual Modulus Prescaler

The MC12058 is a low power $\div 126/128$, $\div 254/256$ dual modulus prescaler. Motorola's advanced Bipolar MOSAIC™ V technology is utilized to achieve low power dissipation of 3.0mW at a minimum supply voltage of 2.7V. The MC12058 can be operated down to a minimum supply voltage of 2.7V required for battery operated portable systems.

On-chip output termination provides 250μA (typical) output current to drive a 8pF (typical) high impedance load. The Divide Ratio Control input, SW, permits selection of divide ratio as desired. A HIGH on SW selects $\div 126/128$; an OPEN on SW selects $\div 254/256$. The Modulus Control input, MC, selects the proper divide number after SW has been biased to select the desired divide ratio.

- 1.1GHz Toggle Frequency
- Supply Voltage 2.7V to 5.5V
- Low Power 1.1mA Typical at $V_{CC} = 3.0V$
- Operating Temperature Range of $-40^{\circ}C$ to $+85^{\circ}C$
- On-Chip Output Termination

Pinout: 8-Lead Plastic (Top View)



MC12058

MECL PLL COMPONENTS

$\div 126/128$, $\div 254/256$

LOW POWER DUAL MODULUS PRESCALER



D SUFFIX
PLASTIC SOIC PACKAGE
CASE 751-05



SD SUFFIX
PLASTIC SSOP PACKAGE
CASE 940-02

FUNCTIONAL TABLE

SW	MC	Divide Ratio
H	H	126
H	L	128
L	H	254
L	L	256

Note: SW: H = V_{CC} , L = Open

MC: H = 2.0 V to V_{CC} , L = Gnd to 0.8 V

MAXIMUM RATINGS

Symbol	Characteristic	Range	Unit
V_{CC}	Power Supply Voltage, Pin 2	-0.5 to $+7.0$	Vdc
T_A	Operating Temperature Range	-40 to $+85$	$^{\circ}C$
T_{stg}	Storage Temperature Range	-65 to $+150$	$^{\circ}C$
MC	Modulus Control Input, Pin 6	-0.5 to $+V_{CC}$	Vdc
I_O	Maximum Output Current, Pin 4	4.0	mA

MOSAIC V is a trademarks of Motorola.



ELECTRICAL CHARACTERISTICS ($V_{CC} = 2.7V$ to $5.5V$; $T_A = -40^{\circ}C$ to $+85^{\circ}C$)

Symbol	Characteristic	Min	Typ	Max	Unit
f_t	Toggle Frequency (Sine Wave Input)	0.1	1.4	1.1	GHz
I_{CC}	Supply Current Output (Pin 2)		1.1	2.0	mA
V_{IH1}	Modulus Control Input HIGH (MC)	2.0		V_{CC}	V
V_{IL1}	Modulus Control Input LOW (MC)	GND		0.8	V
V_{IH2}	Divide Ratio Control Input HIGH (SW)	$V_{CC} - 0.5$	V_{CC}	$V_{CC} + 0.5$	V
V_{IL2}	Divide Ratio Control Input LOW (SW)	Open	Open	Open	
V_{out}	Output Voltage Swing ¹	0.8	1.1		V_{PP}
t_{set}	Modulus Setup Time MC to OUT at 1100MHz		11	16	ns
V_{in}	Input Voltage Sensitivity	250–1100MHz 100–250MHz	100 400	1000 1000	mV _{PP}

¹ Assumes 8pF high impedance load.

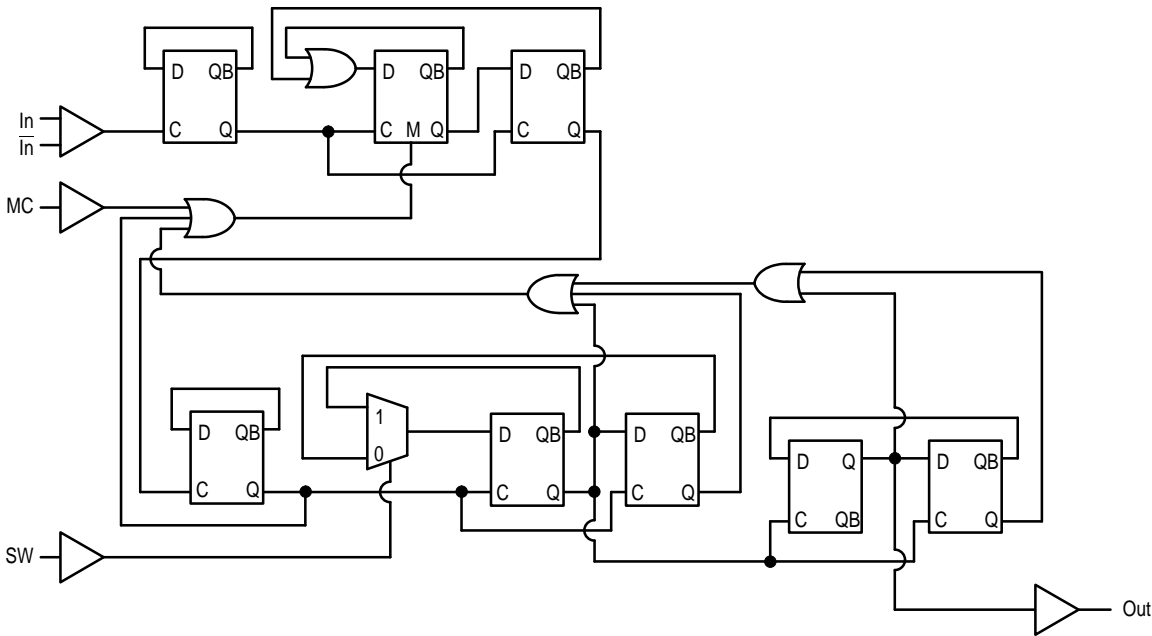
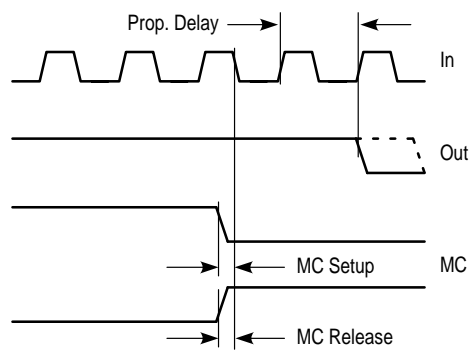


Figure 1. Logic Diagram (MC12058)



Modulus setup time MC to out is the MC setup or MC release plus the prop delay.

Figure 2. Modulus Setup Time

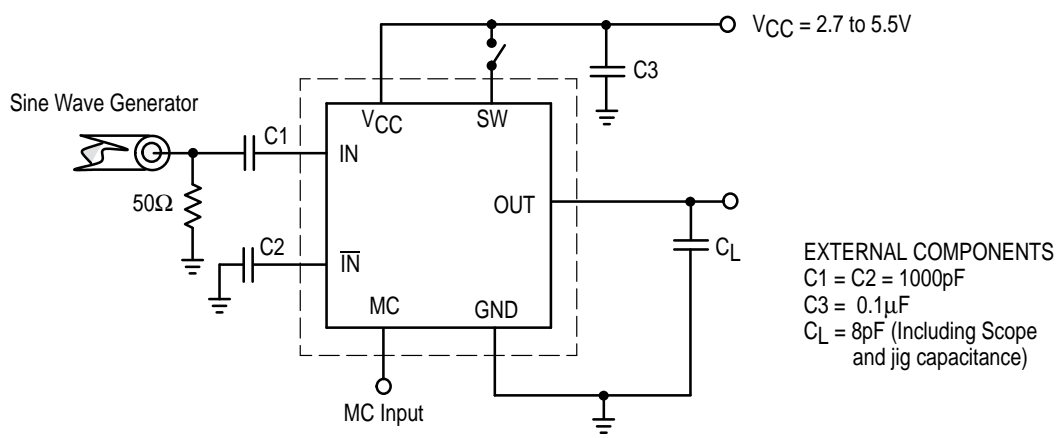


Figure 3. AC Test Circuit

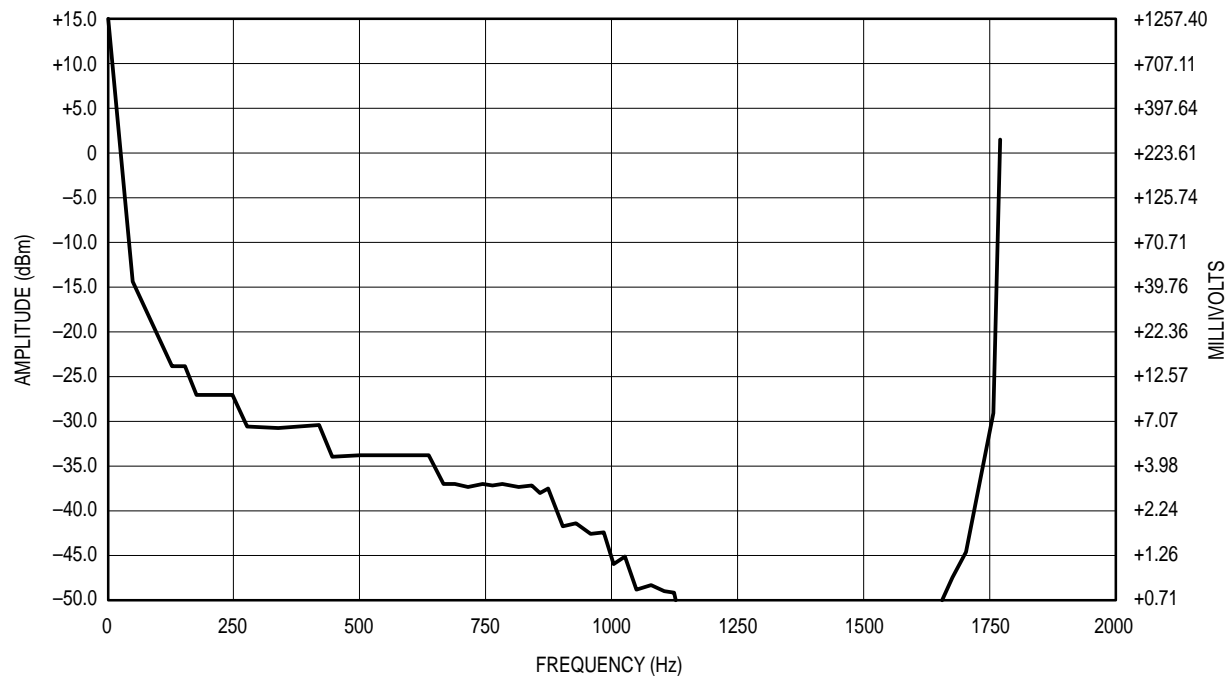


Figure 4. Input Signal Amplitude versus Input Frequency
Divide Ratio = 126; $V_{CC} = 5.5V$; $T_A = 25^\circ C$

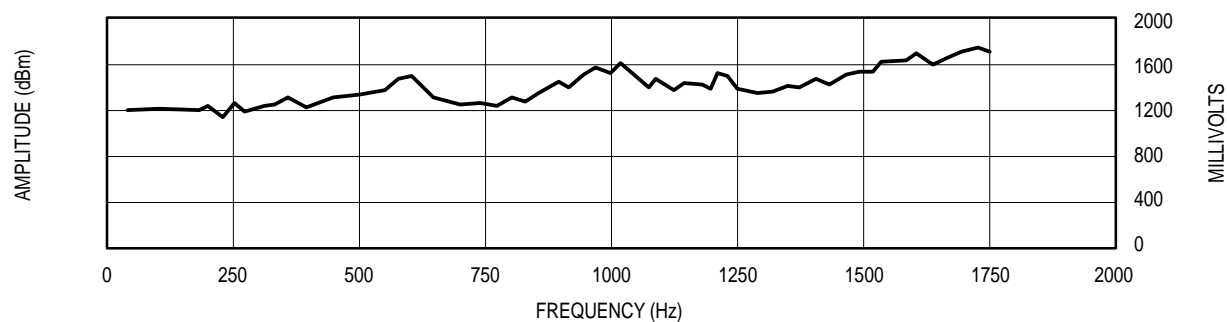
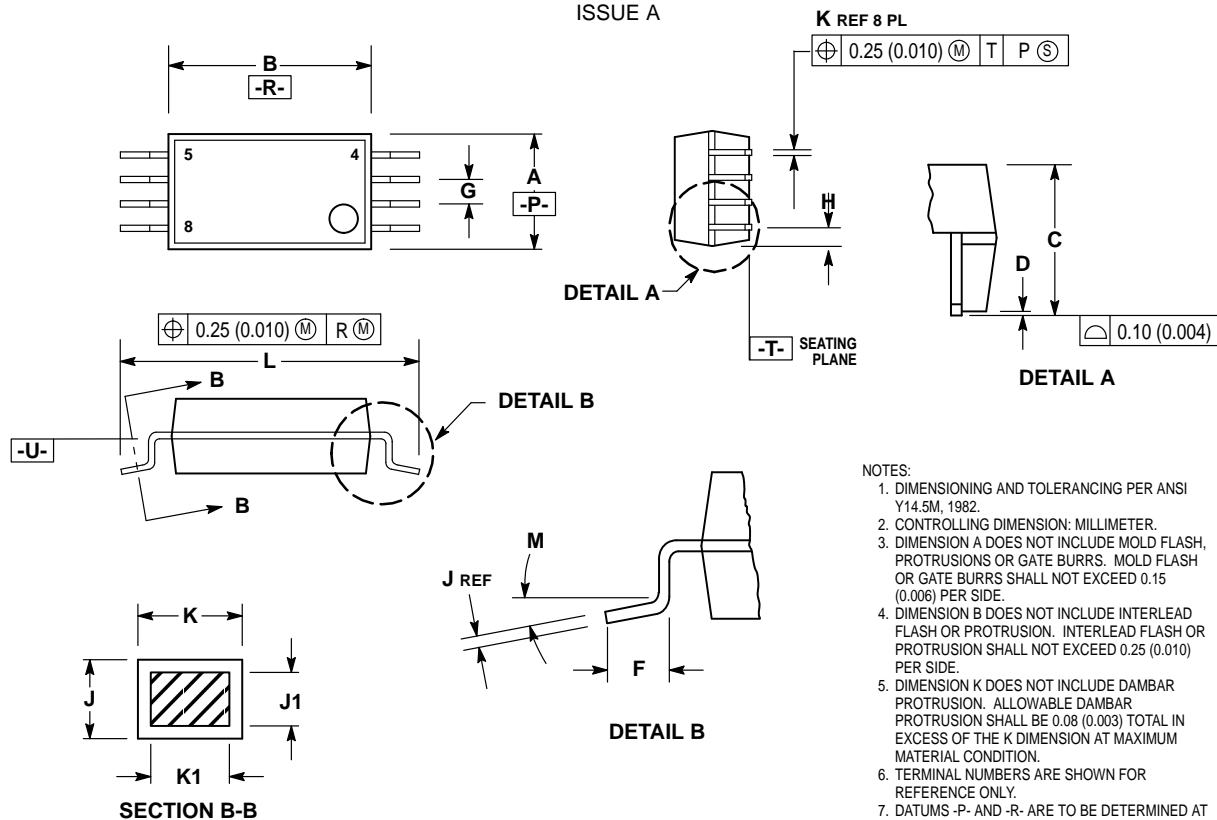


Figure 5. Output Amplitude versus Input Frequency

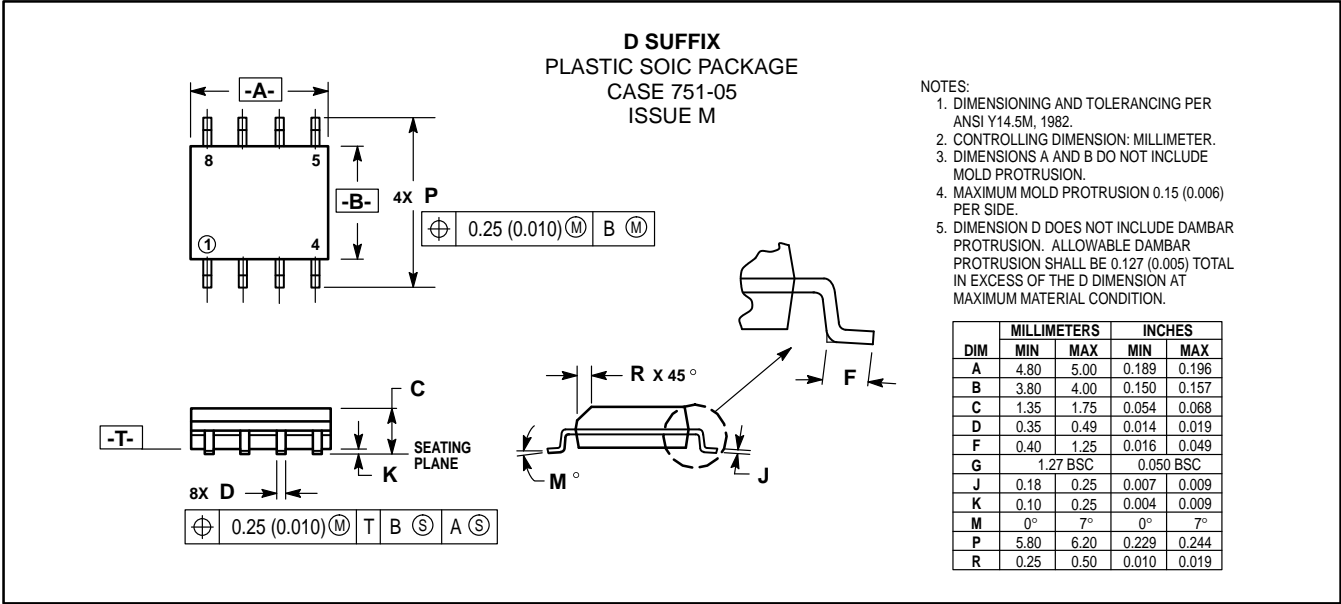
OUTLINE DIMENSIONS


SD SUFFIX
PLASTIC SSOP PACKAGE
CASE 940-02
ISSUE A



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.87	3.13	0.113	0.123
B	5.20	5.38	0.205	0.212
C	1.73	1.99	0.068	0.078
D	0.05	0.21	0.002	0.008
F	0.55	0.95	0.022	0.037
G	0.65 BSC		0.026 BSC	
H	0.50	—	0.020	—
J	0.09	0.20	0.004	0.008
J1	0.09	0.16	0.004	0.006
K	0.22	0.38	0.009	0.015
K1	0.22	0.33	0.009	0.013
L	7.65	7.90	0.301	0.311
M	0°	8°	0°	8°

OUTLINE DIMENSIONS



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