1.1GHz Low Power Dual Modulus Prescaler

The MC12038A can be used with CMOS synthesizers requiring positive edges to trigger internal counters such as Motorola's MC145XXX series in a PLL to provide tuning signals up to 1.1GHz in programmable frequency steps.

A Divide Ratio Control (SW) permits selection of a 127/128 or 255/256 divide ratio as desired.

The Modulus Control (MC) selects the proper divide number after SW has been biased to select the desired divide ratio.

- 1.1 GHz Toggle Frequency
- Supply Voltage of 4.5 to 5.5V
- Low–Power 4.8mA Typical
- Operating Temperature Range of -40 to +85°C
- Short Setup Time (tset) 16ns Maximum @ 1.1GHz
- Modulus Control Input Level Is Compatible With Standard CMOS and TTL
- On-Chip Output Termination

FUNCTIONAL TABLE

SW	MC	Divide Ratio		
Н	Н	127		
Н	L	128		
L	н	255		
L	L	256		

Note: SW: $H = V_{CC}$, L = Open

MC: H = 2.0 V to V_{CC} , L = GND to 0.8 V

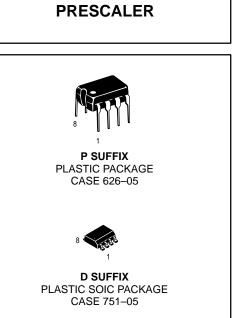
DESIGN GUIDE

A K C		Unit	
Criteria	Value		
Internal Gate Count*	67	ea	
Internal Gate Propagation Delay	200	ps	
Internal Gate Power Dissipation	0.75	mW	
Speed Power Product	0.15	pJ	

* Equivalent to a two-input NAND gate

MAXIMUM RATINGS

Symbol	Characteristic	Range	Unit
V _{CC}	Power Supply Voltage, Pin 2	-0.5 to + 7.0	Vdc
T _A	Operating Temperature Range	-40 to + 85	°C
T _{stg}	Storage Temperature Range	-65 to + 150	°C
MC	Modulus Control Input, Pin 6	-0.5 to + 6.5	Vdc



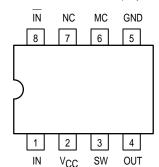
MC12038A

MECL PLL COMPONENTS

÷127/128, ÷255/256

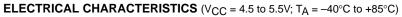
DUAL MODULUS

Pinout: 8-Lead Plastic (Top View)



🕅 MOTOROLA

Symbol	Characteristic	Min	Тур	Max	Unit
ft	Toggle Frequency (Sine Wave Input)	0.1	1.4	1.1	GHz
ICC	Supply Current Output Unloaded (Pin 2) at 5.0Vdc		4.8	6.5	mA
VIH1	Modulus Control Input High (MC)	2.0		V _{CC} + 0.5V	V
V _{IL1}	Modulus Control Input Low (MC)			0.8	V
V _{IH2}	Divide Ratio Control Input High (SW)	$V_{CC} - 0.5V$	VCC	V _{CC} + 0.5V	Vdc
V _{IL2}	Divide Ratio Control Input Low (SW)	Open	Open	Open	—
V _{out}	Output Voltage Swing ($C_L = 8pF$)	1.0	1.6		V _{p-p}
t _{set}	Modulus Setup Time MC to Out		11	16	ns
Vin(min)	Input Voltage Sensitivity 250–1100 MHz 100–250 MHz	100 400		1500 1500	mVpp



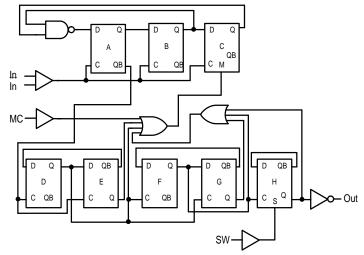
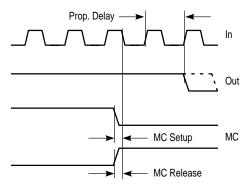
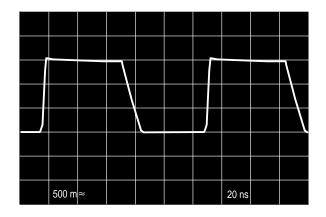


Figure 1. Logic Diagram (MC12038A)



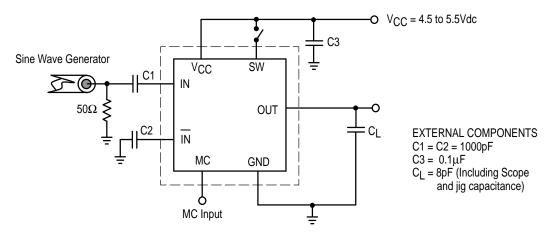
Modulus setup time MC to out is the MC setup or MC release plus the prop delay.

Figure 2. Modulus Setup Time

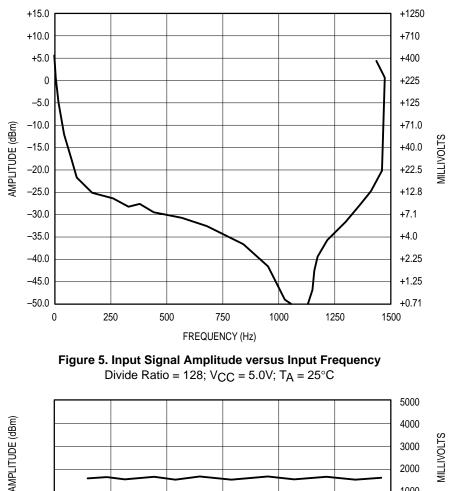


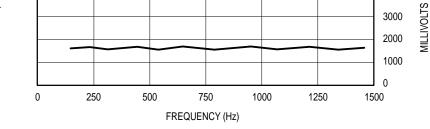
(+128, 1.1GHz Input Frequency, V_{CC} = 5.0V, T_A = 25°C, Output Loaded)

Figure 3. Typical Output Waveforms











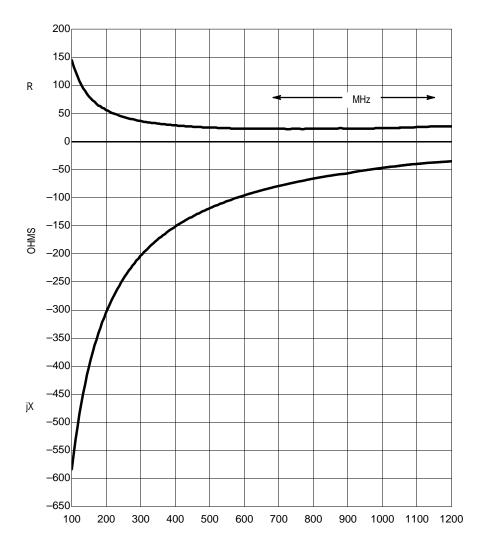
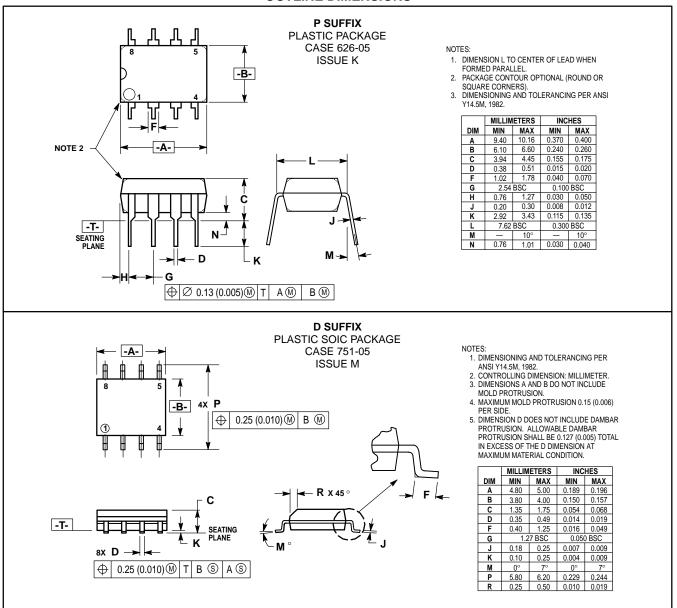


Figure 7. Typical Input Impedance versus Input Frequency

MC12022SLA/D

OUTLINE DIMENSIONS



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